



Electronics components for upgrade

Wednesday 11 November 2009
from 14:00 to 19:00
Europe/Zurich
at CERN ([40-S2-D01](#))

Material: EVO information

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|-------|--|---|
| 14:00 | Introduction and opening (10') | Philippe Farthouat (CERN) |
| 14:10 | Deep submicron technologies access and IP blocks (30') (Slides) | Kostas Kloukinas (CERN) |
| 14:40 | The GBT chip set (30') (Slides) | Ken Wyllie (CERN) |
| 15:10 | The versatile link project (30') (Slides) | Francois Vasey (CERN) |
| 15:40 | Array based optical links proposal (20') | K.K. Gan (<i>The Ohio State University</i>) |
| 16:00 | Tea break (20') | |
| 16:20 | Radiation hard DC-DC converters (30') | |
| | <ul style="list-style-type: none"> • System issues for the development of DC-DC converters (15') (Slides) • Development of radiation-tolerant DC-DC ASICs (15') (Slides) | Georges Blanchot (CERN)
Federico Faccio (CERN) |
| 16:50 | Interconnect and packaging (30') (Slides) | Sami Vaehaenen |
| 17:20 | ADC development for the LAr upgrade (30') (Slides) | Gustaaf Brooijmans (<i>Columbia University</i>) |
| 17:50 | Update on DAQ upgrade R&D with RCE on ATCA platform (30') | Dong Su (<i>SLAC</i>) |