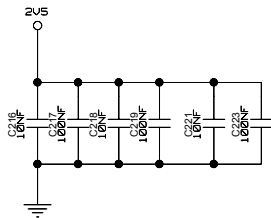
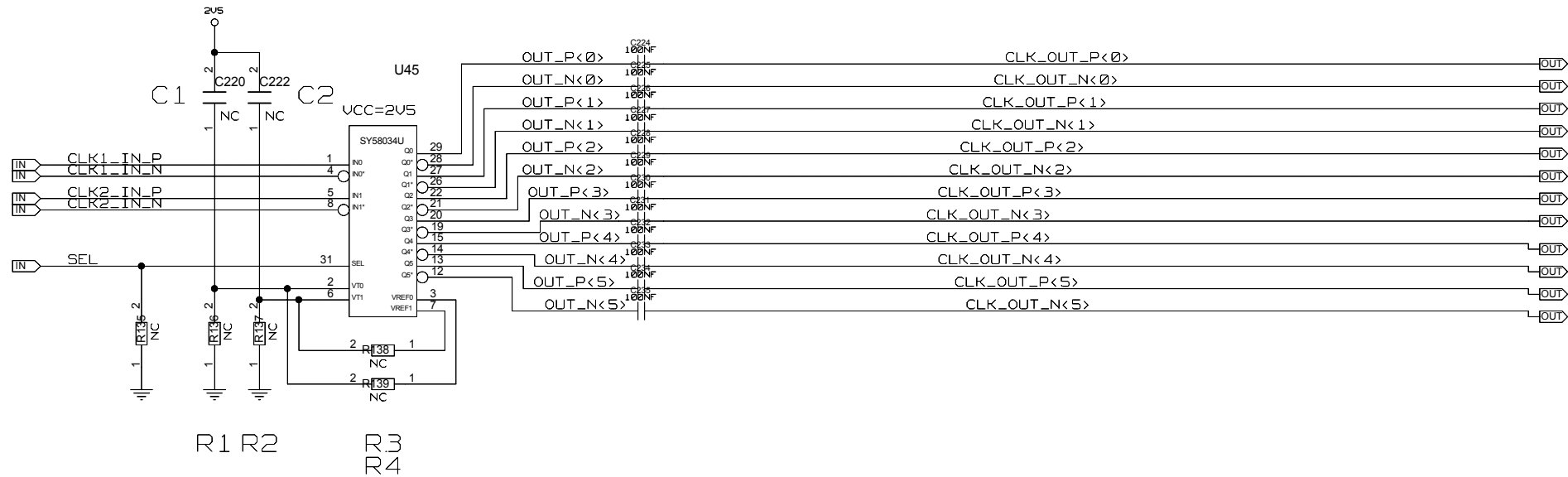


REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



INPUT INTERFACE APPLICATIONS

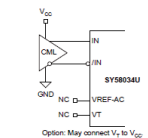


Figure 3a. CML Interface (DC-Coupled)

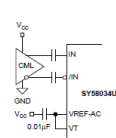


Figure 3b. CML Interface (AC-Coupled)

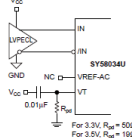


Figure 3c. LVPECL Interface (DC-Coupled)

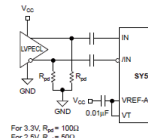


Figure 3d. LVPECL Interface (AC-Coupled)

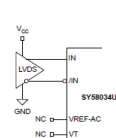


Figure 3e. LVDS Interface

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BLOCK: CLOCK

DRAWING TITLE

CLOCK CML FANOUT6 AC

SIZE

C

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SCALE

SHEET OF