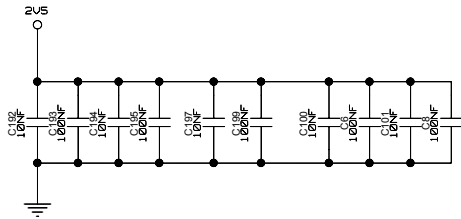
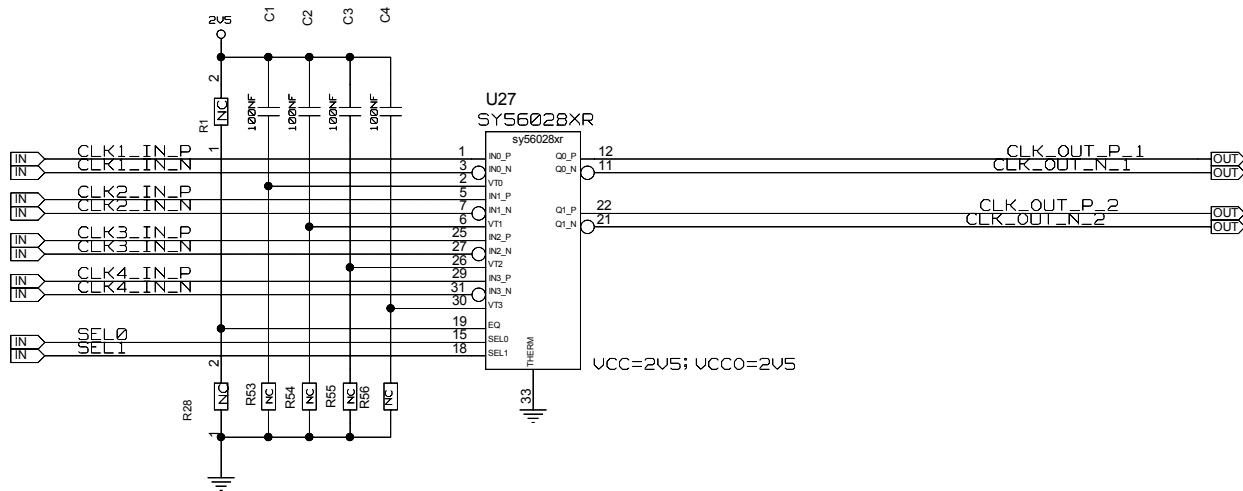


REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



Input Interface Applications

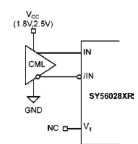


Figure 4a. CML Interface
100 Ω Differential
(DC-Coupled, 1.8V, 2.5V)

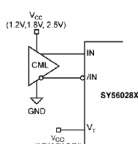


Figure 4b. CML Interface
50 Ω to Vcc
(DC-Coupled, 1.2V, 1.8V, 2.5V)

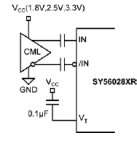


Figure 4c. CML Interface
(AC-Coupled)
*See note in Functional Description
for 1.2V CML driver with AC-Coupling

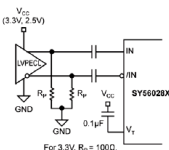


Figure 4d. LVPECL Interface
(AC-Coupled)

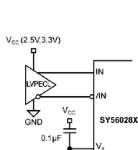


Figure 4e. LVPECL Interface
(DC-Coupled 2.5V and 3.3V)

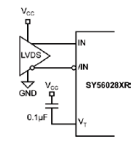


Figure 4f. LVDS Interface

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BLOCK: CLOCK

DRAWING TITLE

MULTIPLEXER

SIZE
C

REV.

DRAWING NO.

SCALE

SHEET OF