

|    | JFEX ZYNC MEZZANINE PINMAPPING |             |          |             |          |              |          |              |           |            |            |            |           |                  |              |                 |                 |               |
|----|--------------------------------|-------------|----------|-------------|----------|--------------|----------|--------------|-----------|------------|------------|------------|-----------|------------------|--------------|-----------------|-----------------|---------------|
|    | A                              | B           | C        | D           | E        | F            | G        | H            | J         | K          | L          | M          | N         | P                | Q            | R               | S               | T             |
| 1  | GND                            | GND         | GND      | GND         | GND      | GND          | GND      | GND          | GND       | IPMC_USER1 | IPMC_USER2 | IPMC_HA0   | IPMC_HA1  | IPMC_Mgt_I2C_SCL | IPMC_PG_A    | IPMC_EXT_GB_A_P | IPMC_EXT_GB_A_N | IPMC_ATCA_SW  |
| 2  | GND                            | CLK_MGT1_P  | GND      | CLK_MGT3_P  | GND      | U1_EMCCCLK_P | GND      | U3_EMCCCLK_P | GND       | IPMC_USER3 | IPMC_USER4 | IPMC_HA2   | IPMC_HA3  | IPMC_Mgt_I2C_SDA | IPMC_PG_B    | IPMC_EXT_GB_B_P | IPMC_EXT_GB_A_N | GND           |
| 3  | GND                            | CLK_MGT1_N  | GND      | CLK_MGT3_N  | GND      | U1_EMCCCLK_N | GND      | U3_EMCCCLK_N | GND       | IPMC_A_SCL | IPMC_A_SDA | IPMC_HA4   | IPMC_HA5  | IPMC_Sensor_SCL_ | IPMC_12V_ENA | IPMC_EXT_GB_C_P | IPMC_EXT_GB_A_N | CPLD<0>       |
| 4  | GND                            | GND         | GND      | GND         | GND      | GND          | GND      | GND          | GND       | IPMC_B_SCL | IPMC_B_SDA | IPMC_HA6   | IPMC_HA7  | IPMC_Sensor_SDA_ | IPMC_RES     | IPMC_EXT_GB_D_P | IPMC_EXT_GB_A_N | CPLD<1>       |
| 5  | GND                            | CLK_MGT2_P  | GND      | CLK_MGT4_P  | GND      | U2_EMCCCLK_P | GND      | U4_EMCCCLK_P | GND       | CPLD<18>   | CPLD<16>   | CPLD<14>   | CPLD<12>  | CPLD<10>         | CPLD<8>      | CPLD<6>         | CPLD<4>         | CPLD<2>       |
| 6  | GND                            | CLK_MGT2_N  | GND      | CLK_MGT4_N  | GND      | U2_EMCCCLK_N | GND      | U4_EMCCCLK_N | GND       | CPLD<19>   | CPLD<17>   | CPLD<15>   | CPLD<13>  | CPLD<11>         | CPLD<9>      | CPLD<7>         | CPLD<5>         | CPLD<3>       |
| 7  | GND                            | GND         | GND      | GND         | GND      | GND          | GND      | GND          | GND       | GND        | GND        |            |           |                  |              | JC_CTRL1        | AVAGO_I2C_SDA   | AVAGO_I2C_SCL |
| 8  | GND                            | U1_GCK_P    | GND      | U2_GCK_P    | GND      | U3_GCK_P     | GND      | U4_GCK_P     | GND       | LHC_CLK_P  | GND        |            |           |                  |              | JC_CTRL2        | AVAGO_I2C_SDA   | AVAGO_I2C_SCL |
| 9  | GND                            | U1_GCK_N    | GND      | U2_GCK_N    | GND      | U3_GCK_N     | GND      | U4_GCK_N     | GND       | LHC_CLK_N  | GND        |            |           |                  |              | JC_CTRL3        | AVAGO_I2C_SDA   | AVAGO_I2C_SCL |
| 10 | GND                            | GND         | GND      | GND         | GND      | GND          | GND      | GND          | GND       | GND        | GND        |            |           |                  |              |                 | AVAGO_I2C_SDA   | AVAGO_I2C_SCL |
| 11 | GND                            | U1_IPBUS_RX | GND      | U1_IPBUS_TX | GND      | U3_IPBUS_RX  | GND      | U3_IPBUS_TX  | GND       | TTC_DATA_P | GND        |            |           |                  |              |                 |                 |               |
| 12 | GND                            | U1_IPBUS_RX | GND      | U1_IPBUS_TX | GND      | U3_IPBUS_RX  | GND      | U3_IPBUS_TX  | GND       | TTC_DATA_N | GND        |            |           |                  |              |                 |                 |               |
| 13 | GND                            | GND         | GND      | GND         | GND      | GND          | GND      | GND          | GND       | GND        | GND        |            |           |                  |              |                 |                 |               |
| 14 | GND                            | U1_IPBUS_RX | GND      | U1_IPBUS_TX | GND      | U3_IPBUS_RX  | GND      | U3_IPBUS_TX  | GND       | TTC_DATA_P | GND        |            |           |                  |              |                 |                 |               |
| 15 | GND                            | U1_IPBUS_RX | GND      | U1_IPBUS_TX | GND      | U3_IPBUS_RX  | GND      | U3_IPBUS_TX  | GND       | TTC_DATA_N | GND        |            |           |                  |              |                 |                 |               |
| 16 | GND                            | GND         | GND      | GND         | GND      | GND          | GND      | GND          | GND       | GND        | GND        | GND        | GND       |                  |              |                 |                 |               |
| 17 | GND                            | U2_IPBUS_RX | GND      | U2_IPBUS_TX | GND      | U4_IPBUS_RX  | GND      | U4_IPBUS_TX  | GND       | BaseCh_1_P | GND        | BaseCh_3_P | GND       |                  |              |                 |                 |               |
| 18 | GND                            | U2_IPBUS_RX | GND      | U2_IPBUS_TX | GND      | U4_IPBUS_RX  | GND      | U4_IPBUS_TX  | GND       | BaseCh_1_N | GND        | BaseCh_3_N | GND       |                  |              |                 |                 |               |
| 19 | GND                            | GND         | GND      | GND         | GND      | GND          | GND      | GND          | GND       | GND        | GND        | GND        | GND       |                  |              |                 |                 |               |
| 20 | GND                            | U2_IPBUS_RX | GND      | U2_IPBUS_TX | GND      | U4_IPBUS_RX  | GND      | U4_IPBUS_TX  | GND       | BaseCh_2_P | GND        | BaseCh_4_P | GND       |                  |              |                 |                 |               |
| 21 | GND                            | U2_IPBUS_RX | GND      | U2_IPBUS_TX | GND      | U4_IPBUS_RX  | GND      | U4_IPBUS_TX  | GND       | BaseCh_2_N | GND        | BaseCh_4_N | GND       |                  |              |                 |                 |               |
| 22 | GND                            | GND         | GND      | GND         | GND      | GND          | GND      | GND          | GND       | GND        | GND        | GND        | GND       |                  |              |                 |                 |               |
| 23 | U4_SPARE                       | U4_SPARE    | U4_SPARE | U4_SPARE    | U4_SPARE | U4_SPARE     | U4_SPARE | U4_SPARE     |           |            |            |            |           |                  |              |                 |                 |               |
| 24 | U4_SPARE                       | U4_SPARE    | U4_SPARE | U4_SPARE    | U4_SPARE | U4_SPARE     | U4_SPARE | U4_SPARE     |           |            |            |            |           |                  |              |                 |                 |               |
| 25 | U4_SPARE                       | U4_SPARE    | U4_SPARE | U4_SPARE    | U4_SPARE | U4_SPARE     | U4_SPARE | U4_SPARE     |           |            |            |            |           |                  |              |                 |                 |               |
| 26 | GND                            | GND         | GND      | GND         | GND      | GND          | GND      | GND          |           |            |            |            |           |                  |              |                 |                 |               |
| 27 | U3_SPARE                       | U3_SPARE    | U3_SPARE | U3_SPARE    | U3_SPARE | U3_SPARE     | U3_SPARE | U3_SPARE     |           |            |            |            |           |                  |              |                 |                 |               |
| 28 | U3_SPARE                       | U3_SPARE    | U3_SPARE | U3_SPARE    | U3_SPARE | U3_SPARE     | U3_SPARE | U3_SPARE     |           |            |            |            |           |                  |              |                 |                 |               |
| 29 | U3_SPARE                       | U3_SPARE    | U3_SPARE | U3_SPARE    | U3_SPARE | U3_SPARE     | U3_SPARE | U3_SPARE     | GND       | GND        | GND        | GND        | GND       | GND              | GND          | GND             | GND             | GND           |
| 30 | GND                            | GND         | GND      | GND         | GND      | GND          | GND      | GND          | U4_CONFIG | U4_CONFIG  | U4_CONFIG  | U4_CONFIG  | U4_CONFIG | U4_CONFIG        | U4_CONFIG    | U4_CONFIG       | U4_CONFIG       | U4_CONFIG     |
| 31 | U2_SPARE                       | U2_SPARE    | U2_SPARE | U2_SPARE    | U2_SPARE | U2_SPARE     | U2_SPARE | U2_SPARE     | U4_CONFIG | U4_CONFIG  | U4_CONFIG  | U4_CONFIG  | U4_CONFIG | U4_CONFIG        | U4_CONFIG    | U4_CONFIG       | U4_CONFIG       | U4_CONFIG     |
| 32 | U2_SPARE                       | U2_SPARE    | U2_SPARE | U2_SPARE    | U2_SPARE | U2_SPARE     | U2_SPARE | U2_SPARE     | U3_CONFIG | U3_CONFIG  | U3_CONFIG  | U3_CONFIG  | U3_CONFIG | U3_CONFIG        | U3_CONFIG    | U3_CONFIG       | U3_CONFIG       | U3_CONFIG     |
| 33 | U2_SPARE                       | U2_SPARE    | U2_SPARE | U2_SPARE    | U2_SPARE | U2_SPARE     | U2_SPARE | U2_SPARE     | U3_CONFIG | U3_CONFIG  | U3_CONFIG  | U3_CONFIG  | U3_CONFIG | U3_CONFIG        | U3_CONFIG    | U3_CONFIG       | U3_CONFIG       | U3_CONFIG     |
| 34 | GND                            | GND         | GND      | GND         | GND      | GND          | GND      | GND          | U2_CONFIG | U2_CONFIG  | U2_CONFIG  | U2_CONFIG  | U2_CONFIG | U2_CONFIG        | U2_CONFIG    | U2_CONFIG       | U2_CONFIG       | U2_CONFIG     |
| 35 | U1_SPARE                       | U1_SPARE    | U1_SPARE | U1_SPARE    | U1_SPARE | U1_SPARE     | U1_SPARE | U1_SPARE     | U2_CONFIG | U2_CONFIG  | U2_CONFIG  | U2_CONFIG  | U2_CONFIG | U2_CONFIG        | U2_CONFIG    | U2_CONFIG       | U2_CONFIG       | U2_CONFIG     |
| 36 | U1_SPARE                       | U1_SPARE    | U1_SPARE | U1_SPARE    | U1_SPARE | U1_SPARE     | U1_SPARE | U1_SPARE     | U1_CONFIG | U1_CONFIG  | U1_CONFIG  | U1_CONFIG  | U1_CONFIG | U1_CONFIG        | U1_CONFIG    | U1_CONFIG       | U1_CONFIG       | U1_CONFIG     |
| 37 | U1_SPARE                       | U1_SPARE    | U1_SPARE | U1_SPARE    | U1_SPARE | U1_SPARE     | U1_SPARE | U1_SPARE     | U1_CONFIG | U1_CONFIG  | U1_CONFIG  | U1_CONFIG  | U1_CONFIG | U1_CONFIG        | U1_CONFIG    | U1_CONFIG       | U1_CONFIG       | U1_CONFIG     |

CLOCK

IPMC

CPLD

SPARE Bank68

U1-U4 CONFIG 80