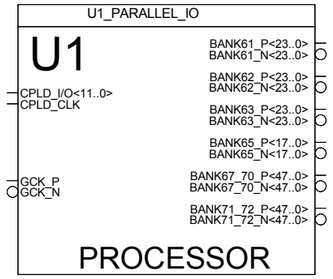
REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

I1



	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE

DRAWING TITLE

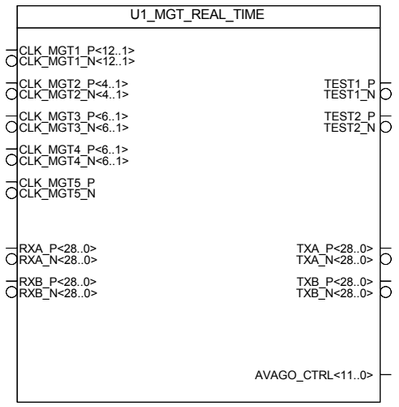
UNIVERSITY OF MAINZ

SIZE C | REV. | DRAWING NO.

SCALE | SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

I1



	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE

DRAWING TITLE

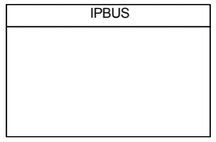
UNIVERSITY OF MAINZ

SIZE C | REV. | DRAWING NO.

SCALE | SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

I6



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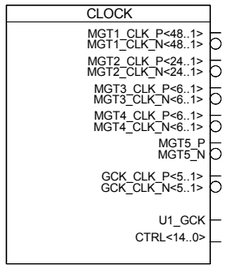
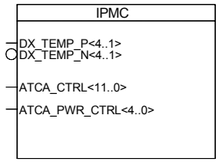
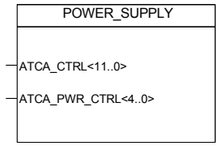


I3

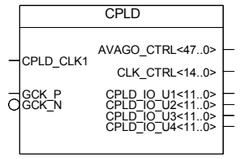


I7

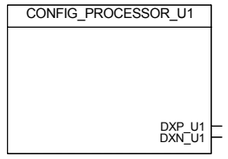
I1



I4



I5



	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE

DRAWING TITLE

UNIVERSITY OF MAINZ

SIZE	REV.	DRAWING NO.
C		
SCALE		SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

ATCA POWER IN & CTRL

3V3_UP

J6

FUSE1

POLYFUSE 300MA

JTAG_USB

VREF, TDI, TDO, TMS, TCK

CONNECTOR

CABLE LOOP TO IPMC
JTAG CONNECTOR

J7

TDI
TDO
TMS
TCK

HA0
HA1
HA2
HA3
HA4
HA5
HA6
HA7/P

SCL_A
SDA_A
SCL_B
SDA_B

SHELF_GND
LOGIC_GND

ENABLE_B

VRTN_A
VRTN_B

EARLY_A
EARLY_B

ENABLE_A

-48V_A
-48V_B

SEGMENT3

FU1 ENB NANO2_1A

FU2 VRTN_A NANO2_15A
FU3 VRTN_B NANO2_15A

R27 100R
R23 100R

FU4 ENA NANO2_1A

FU5 -48V_A NANO2_15A
FU6 -48V_B NANO2_15A

R2512

3V3_UP

R2K

ATCA_CTRL<11..0>

C13 C16 C18 C21
1N 1N 1N 1N
C14 C17 C20 C22

R26 22
R2512

C15 0.22UF/100V
C19 0.22UF/100V

12V

ST1 33 POWER STATUS MEZZANINE

12V

0V

45

REG1

PIM4000

-48V_A
-48V_B
VRTN_A
VRTN_B
ENA
ENB
SEGMENT3

-48VA, -48VB, RTN A, RTN B, ENA, ENB, SGND

OUT+, HU+, HU+ VADJ, OUT-, ALARM, +3V3, +5V, GND, SCL, SDA, ADR

3.3V

R18 10K 0.1W

ATCA_PWR_CTRL<0>
ATCA_PWR_CTRL<1>

C1 C2 C3 C4
0.22UF/100V

R17 NA
C5 22UF
C6 10UF/6.3V

C8 C9 C10
470UF/100V

REG2

VIN+, VIN-, ON/OFF, TRIM

VOUT+, SENSE+, SENSE-, VOUT-

QBVW033A0B-1

ST1 34 POWER STATUS MEZZANINE

MANUAL POWER-ON

SW1 ON

U59 HCPL_817

C11 330UF/25V
C12 1UF/16V

3V3_UP

C7 100N

R19 NC

U58

SN74AHC1G86

B, A, =1, Y, XOR

SN74AHC1G86
VCC=3V3_UP

ATCA_PWR_CTRL<2>

HIGH--> POWER ON
LOW --> POWER OFF

R21 470 0.1W

R20 4K7

TO ATCA_PWR_CTRL<4..0>

ATCA_PWR_CTRL<0> = SCL
ATCA_PWR_CTRL<1> = SDA
ATCA_PWR_CTRL<2> = 12V_ENA
ATCA_PWR_CTRL<3> = PWR_GD
ATCA_PWR_CTRL<4> = PWR_EN

TP GND1
TP GND2
TP GND3
TP GND4
TP GND5
TP GND6
TP GND7
TP GND8
TP GND9
TP GND10

pad PAD1

SEGMENT3 R24 10M/025W

SEGMENT2 R25 10M/025W

SEGMENT1

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	03.11.2015	

BLOCK TITLE: POWERSUPPLY

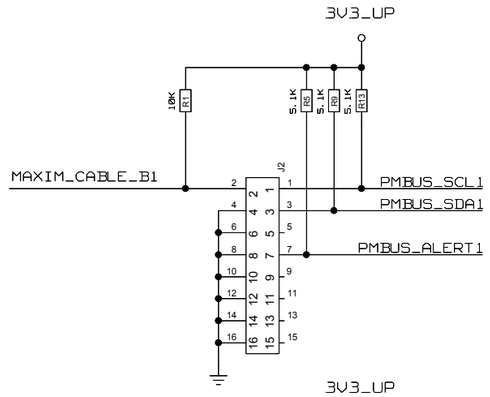
DRAWING TITLE: ATCA POWER

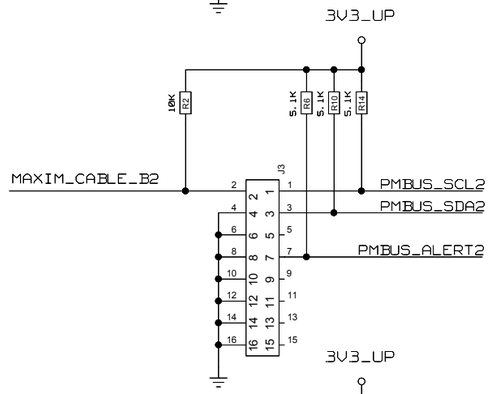
UNIVERSITY OF MAINZ

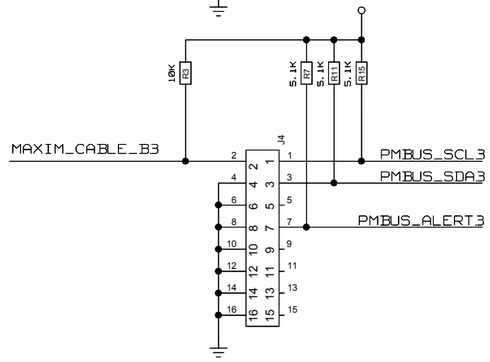
SIZE C | REV. | DRAWING NO.

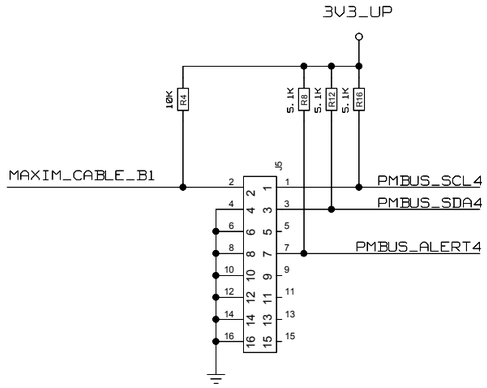
SCALE | SHEET OF

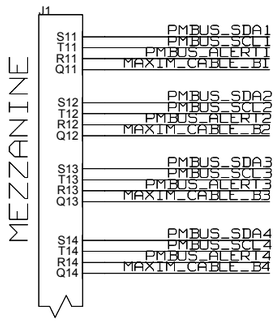
REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.











	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE: POWERSUPPLY

DRAWING TITLE: PMBUS

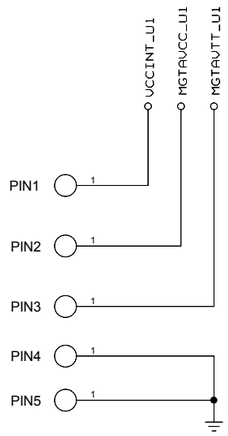
BOARDLEVEL 3V3, 2V5

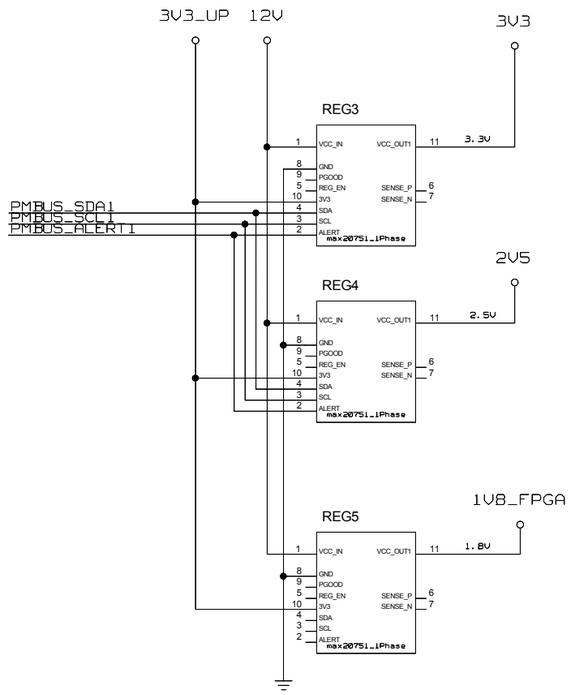
UNIVERSITY OF MAINZ

SIZE: C | REV. | DRAWING NO.

SCALE | SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.





	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE

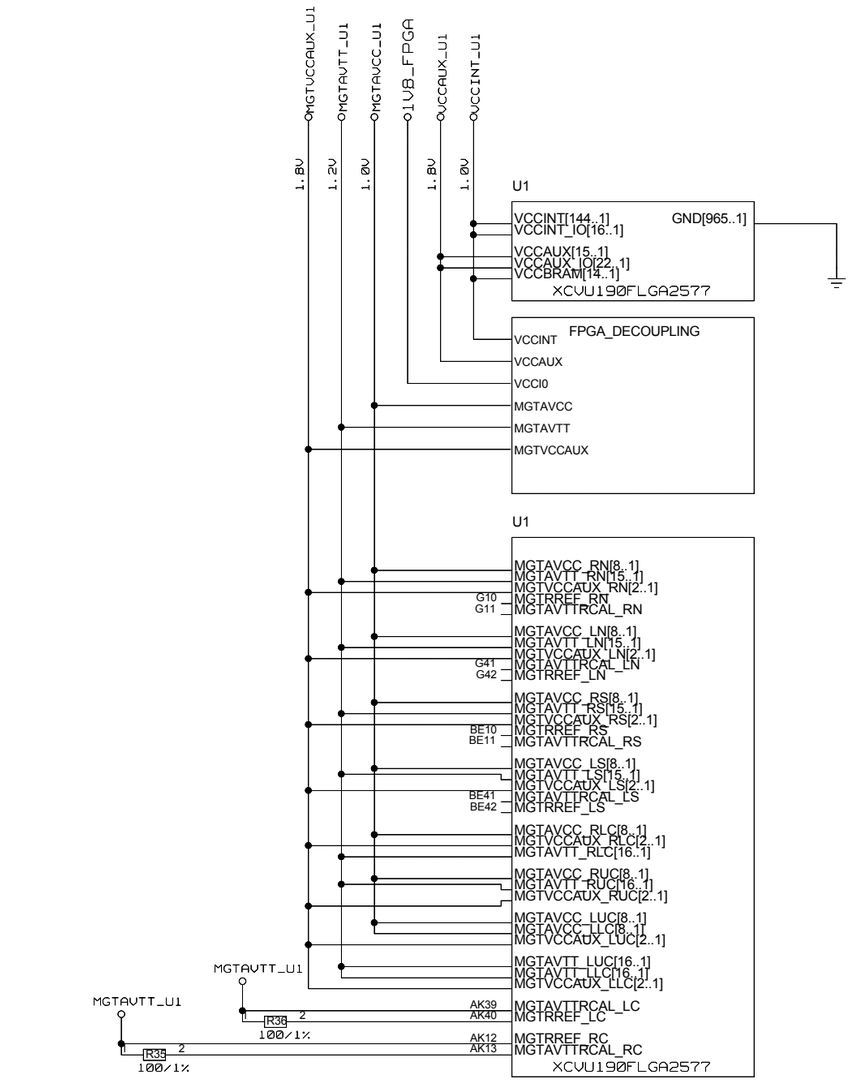
DRAWING TITLE

UNIVERSITY OF MAINZ

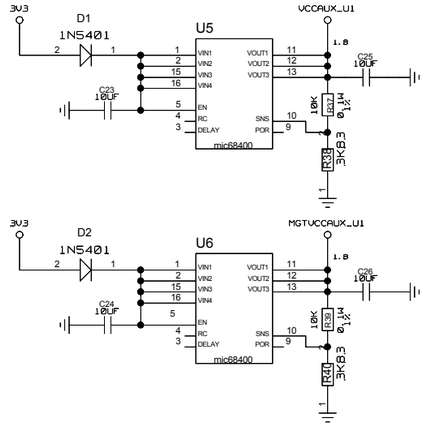
SIZE C | REV. | DRAWING NO.

SCALE | SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



TRACE LENGTH FROM RESISTOR PINS
TO PIN SAME LENGTH
UG576 SHEET 297



	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE

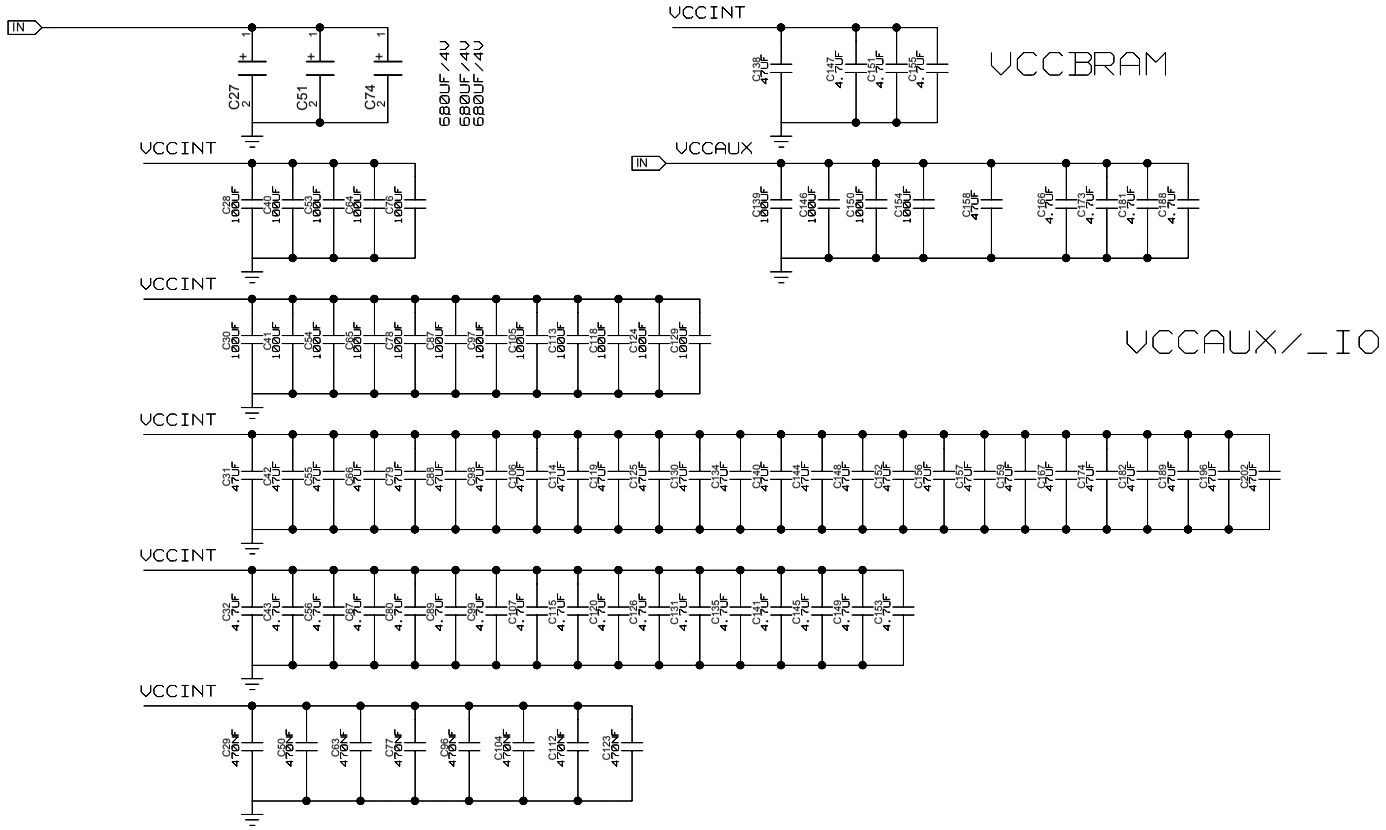
DRAWING TITLE

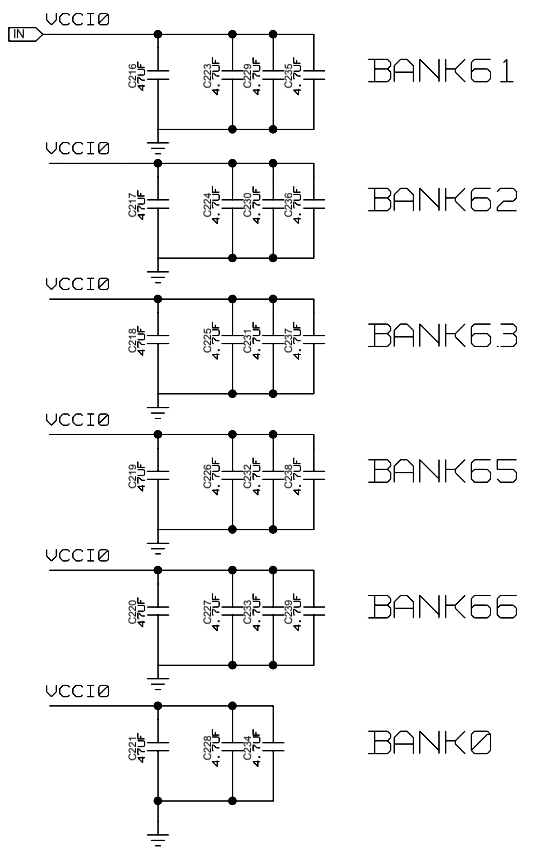
UNIVERSITY OF MAINZ

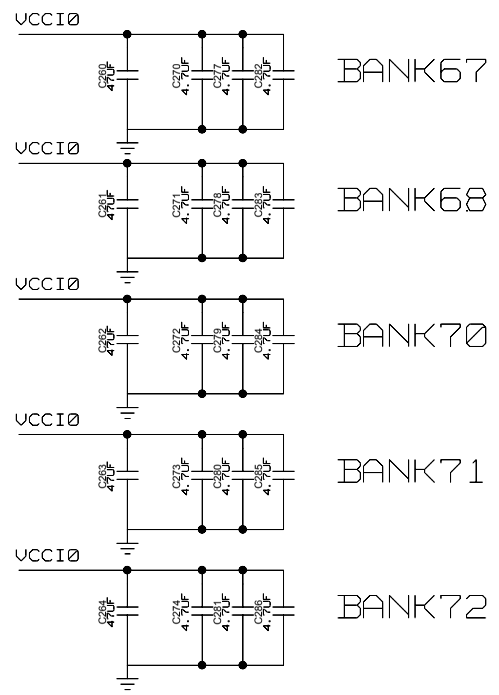
SIZE C | REV. | DRAWING NO.

SCALE | SHEET OF

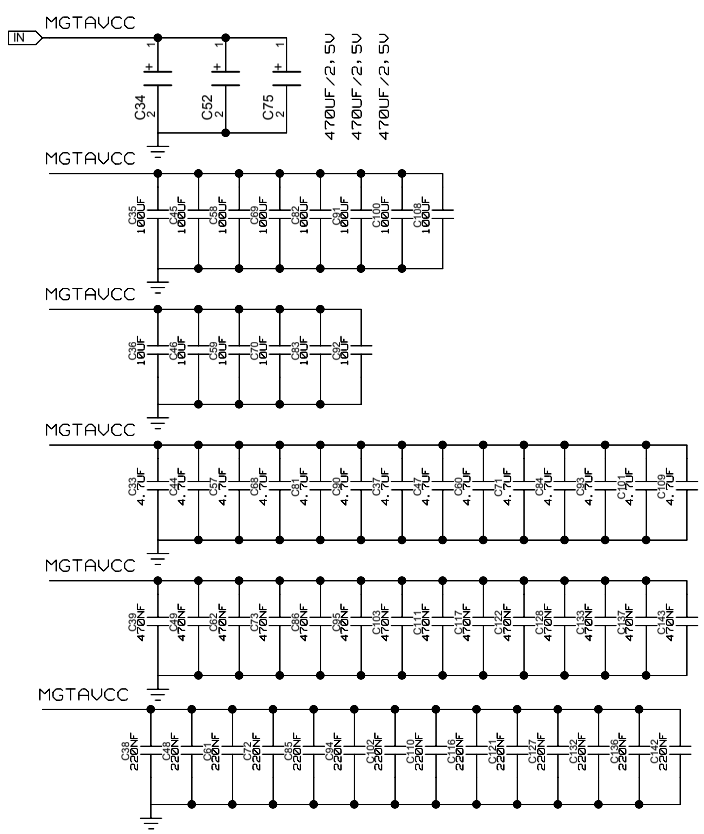
REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

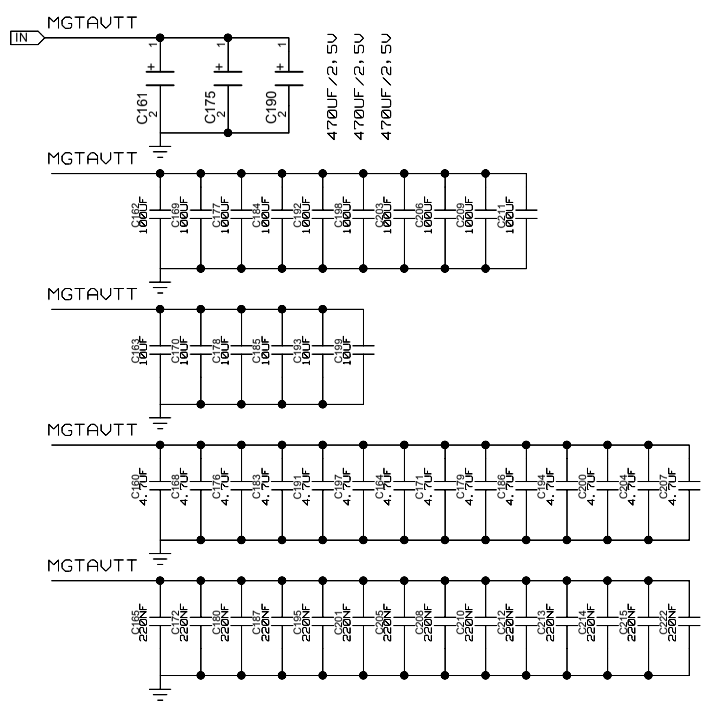


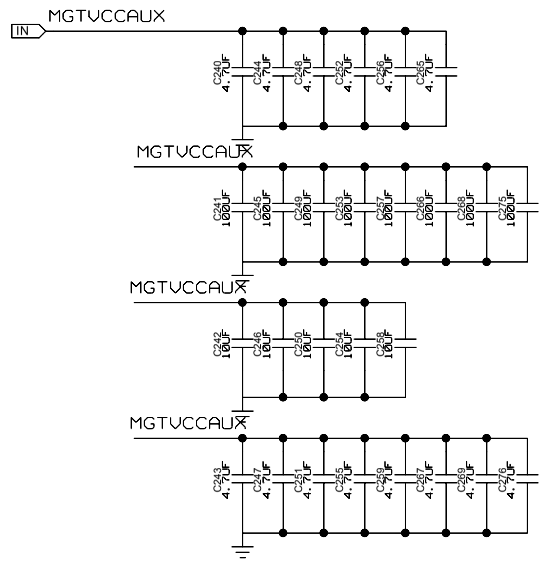




MGT DECOUPLING







	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE

DRAWING TITLE

UNIVERSITY OF MAINZ

SIZE	REV.	DRAWING NO.
C		
SCALE		SHEET OF

MGT1 CLOCK

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

MGT1_JC_P
MGT1_JC_N CML_AC

MEZZANINE J1 B2 B3 LVPECL_AC

CLK3IN=CMLAC
CLK4IN=LVPECLAC
R1=NA C1=NA
R2=NA C2=NA
R3=NA C3=100NF
R4=NA C4=100NF
R5=NA
R6=NA

CLOCK_LVPECL_SI591_AC — CLK_P / CLK_N → CLK_160_P / CLK_160_N — 160.3148MHZ
CLOCK_LVPECL_SI591_AC — CLK_P / CLK_N → CLK_120_P / CLK_120_N — 120.2361MHZ
CLOCK_LVPECL_SI591_AC — CLK_P / CLK_N → CLK_280_P / CLK_280_N — 280.5509MHZ

MUX_CML_4X2_DC
CLK1_IN_P / CLK1_IN_N
CLK2_IN_P / CLK2_IN_N
CLK3_IN_P / CLK3_IN_N
CLK4_IN_P / CLK4_IN_N SY56028XR_DC_CML
CLK_OUT_P_1, CLK_OUT_N_1, CLK_OUT_P_2, CLK_OUT_N_2, SEL0, SEL1

MUX_CML_4X2_DC
CLK1_IN_P / CLK1_IN_N
CLK2_IN_P / CLK2_IN_N
CLK3_IN_P / CLK3_IN_N
CLK4_IN_P / CLK4_IN_N SY56028XR_DC_CML
CLK_OUT_P_1, CLK_OUT_N_1, CLK_OUT_P_2, CLK_OUT_N_2, SEL0, SEL1

CLK_GEN_P / CLK_GEN_N

QUAD_CLOCK_GEN
CTRL_<3..0>
CLK0_P / CLK0_N
CLK1_P / CLK1_N
CLK2_P / CLK2_N
CLK3_P / CLK3_N
SI5335

CTRL<3..0>

CLK1IN=LVPECL_AC
CLK2IN=LVPECL_AC
CLK3IN=LVPECL_AC
CLK4IN=LVPECL_AC
R1=NA C1=100NF
R2=NA C2=100NF
R3=NA C3=100NF
R4=NA C4=100NF
R5=NA
R6=NA

CLKIN=CML
R1=NC C1=NC
R2=NC C2=NC
R3=NC R4=NC

CLOCK_CML_FANOUT6_DC
CLK1_IN_P / CLK1_IN_N
CLK2_IN_P / CLK2_IN_N
CLK_OUT_P<5..0>, CLK_OUT_N<5..0>, SEL
SY58034U_DC_CML
MGT1_1_CLK_P<5..0>
MGT1_1_CLK_N<5..0>

CLOCK_CML_FANOUT6_DC
CLK1_IN_P / CLK1_IN_N
CLK2_IN_P / CLK2_IN_N
CLK_OUT_P<5..0>, CLK_OUT_N<5..0>, SEL
SY58034U_DC_CML
MGT1_2_CLK_P<5..0>
MGT1_2_CLK_N<5..0>

CLKIN=CML
R1=NC C1=NC
R2=NC C2=NC
R3=NC R4=NC

TEST MGT1
U32 MMCX 1 2 4
U31 MMCX 1 4
100 FB

CTRL<4>
CTRL<5>
CTRL<6>
CTRL<7>
CTRL<8>

CLOCK_CML_FANOUT6_AC
CLK1_IN_P / CLK1_IN_N
CLK2_IN_P / CLK2_IN_N
CLK_OUT_P<5..0>, CLK_OUT_N<5..0>, SEL
SY58034U_DC_CML

A LEFT: MGT1_CLK_P<6..1> / MGT1_CLK_N<6..1>
B RIGHT: MGT1_CLK_P<12..7> / MGT1_CLK_N<12..7>
C LEFT: MGT1_CLK_P<18..13> / MGT1_CLK_N<18..13>
D RIGHT: MGT1_CLK_P<24..19> / MGT1_CLK_N<24..19>
A LEFT: MGT1_CLK_P<30..25> / MGT1_CLK_N<30..25>
B RIGHT: MGT1_CLK_P<36..31> / MGT1_CLK_N<36..31>
C LEFT: MGT1_CLK_P<42..37> / MGT1_CLK_N<42..37>
D RIGHT: MGT1_CLK_P<48..43> / MGT1_CLK_N<48..43>

OUT MGT1_CLK_P<48..1> → MGT1_CLK_P<6..1>, MGT1_CLK_P<12..7>, MGT1_CLK_P<18..13>, MGT1_CLK_P<24..19>, MGT1_CLK_P<6..1>, MGT1_CLK_P<12..7>, MGT1_CLK_P<18..13>, MGT1_CLK_P<24..19>

MGT1 CLOCK

OUT MGT1_CLK_N<48..1> → MGT1_CLK_N<6..1>, MGT1_CLK_N<12..7>, MGT1_CLK_N<18..13>, MGT1_CLK_N<24..19>, MGT1_CLK_N<25..30>, MGT1_CLK_N<31..36>, MGT1_CLK_N<37..42>, MGT1_CLK_N<43..48>

OUT CTRL<14..0>

UPDATE PICK & PLACE DATA FOR FANOUT AND MUX

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	17.08.2015	

BLOCK TITLE: CLOCK

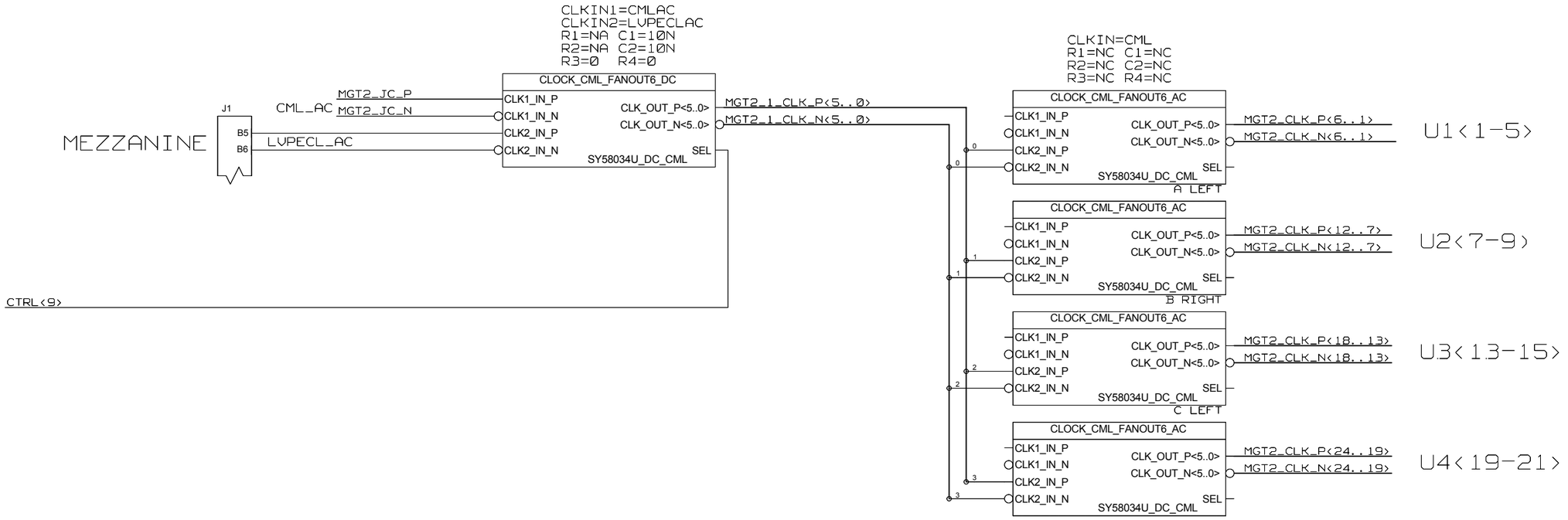
DRAWING TITLE: JFEX — MGT1 CLOCK

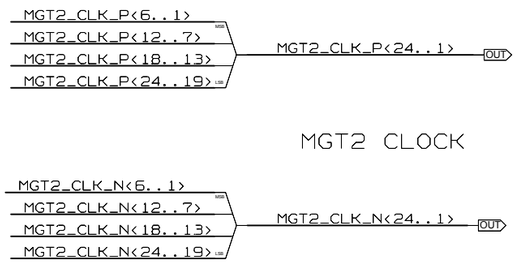
UNIVERSITY OF MAINZ

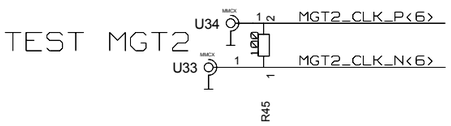
SIZE C | REV. | DRAWING NO. | SCALE | SHEET 1 OF 5

MGT2 CLOCK

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.







UPDATE PICK & PLACE DATA FOR FANOUT AND MUX

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	17.08.2015	

BLOCK TITLE: CLOCK

UNIVERSITY OF MAINZ

DRAWING TITLE: JFEX
MGT2 CLOCK

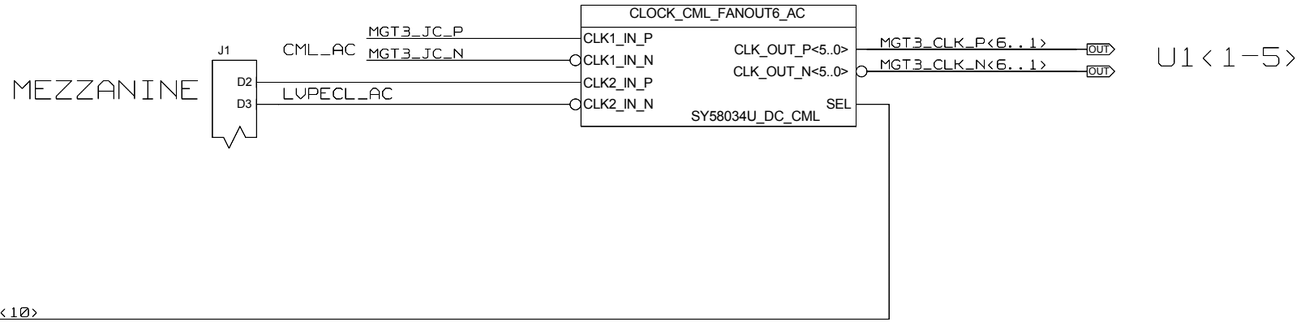
SIZE C | REV. | DRAWING NO.

SCALE | SHEET 2 OF 5

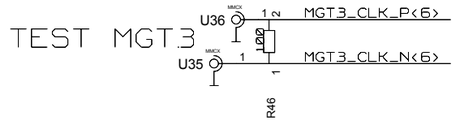
MGT3 CLOCK

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

CLKIN1=CMLAC
CLKIN2=LVPECLAC
R1=NA C1=10N
R2=NA C2=10N
R3=0 R4=0



TEST MGT3



UPDATE PICK & PLACE DATA FOR FANOUT AND MUX

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	17.08.2015	

BLOCK TITLE: CLOCK

UNIVERSITY OF MAINZ

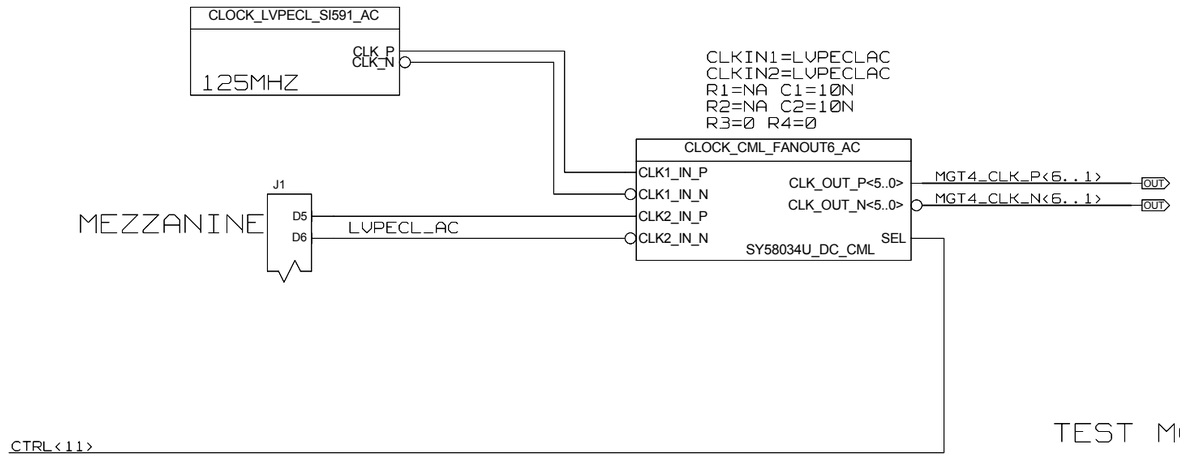
DRAWING TITLE: JFEX
MGT3 CLOCK

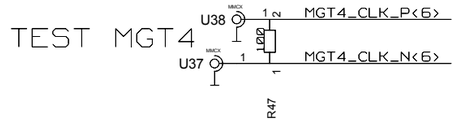
SIZE C | REV. | DRAWING NO.

SCALE | SHEET 3 OF 5

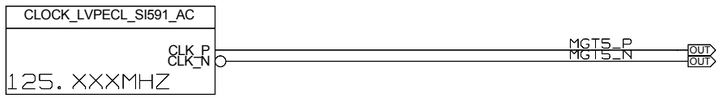
REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

MGT4 CLOCK





MGT5 CLOCK



UPDATE PICK & PLACE DATA FOR FANOUT AND MUX

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	17.08.2015	

BLOCK TITLE: CLOCK

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DRAWING TITLE: JFEX

MGT4 CLOCK

SIZE: C | REV. | DRAWING NO.

SCALE | SHEET 4 OF 5

NEAR TO FPGA

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

LVCMOS U1_GCK 1 R48 150 2 → U7 SY58605U → LVDS U1_GCK_P U1_GCK_N

2V5 R49 240 0.9V BIAS C397 100N D4 BAS85 D3 BAS85

FABRIC CHANNEL

J23 DP+ C4 DP- D4 GND CD4 DP+ C2 DP- D2 GND CD2

C403 100NF C405 100NF C404 100NF C406 100NF

CLOCK_LVPECL_SI591_AC
CLK_P CLK_N
40.0787MHZ

CLK1IN=LVPECL_AC
CLK2IN=LVPECL_AC
CLK3IN=LVPECL_AC
CLK4IN=LVPECL_AC
R1=NA C1=100NF
R2=NA C2=100NF
R3=NA C3=100NF
R4=NA C4=100NF
R5=NA
R6=NA

MUX_CML_4X2_DC
CLK1_IN_P CLK1_IN_N CLK2_IN_P CLK2_IN_N CLK3_IN_P CLK3_IN_N CLK4_IN_P CLK4_IN_N
CLK_OUT_P_1 CLK_OUT_N_1 CLK_OUT_P_2 CLK_OUT_N_2 SEL0 SEL1
SY56028XR_DC_CML

3POL_COMATEL J8

J1 T15 S15 R15 MEZZANINE

JITTERCLEANER_SI5338
IN1 IN2 IN3 IN4 IN5 IN6 SCL SDA INTR
CLK0A CLK0B VDDO0 CLK1A CLK1B VDDO1 CLK2A CLK2B VDDO2 CLK3A CLK3B VDDO3
MGT1_JC_P MGT1_JC_N MGT2_JC_P MGT2_JC_N MGT3_JC_P MGT3_JC_N

2V5 R50 R51 R52 100 100 100 R53 4K7 R54 4K7 R55 4K7
CTRL<12> CTRL<13> CTRL<14>

NA R58 NA R59 R60 0 R61 0 R62 0 R63 0

CLOCK_LVDS_FANOUT8
OUT0_P OUT0_N OUT1_P OUT1_N OUT2_P OUT2_N OUT3_P OUT3_N OUT4_P OUT4_N OUT5_P OUT5_N OUT6_P OUT6_N OUT7_P OUT7_N
IN0_P IN0_N IN1_P IN1_N SEL
SY89838U

GCK_CLK_P<1> GCK_CLK_N<1>
GCK_CLK_P<2> GCK_CLK_N<2>
GCK_CLK_P<3> GCK_CLK_N<3>
GCK_CLK_P<4> GCK_CLK_N<4>
GCK_CLK_P<5> GCK_CLK_N<5>

J1 K8 K9 LHC_CLK_P LHC_CLK_N MEZZANINE

CLKIN0=LVDS
CLKIN1=CML
R1=NA C1=10NF
R2=NA C2=NA
R3=NA R4=0R

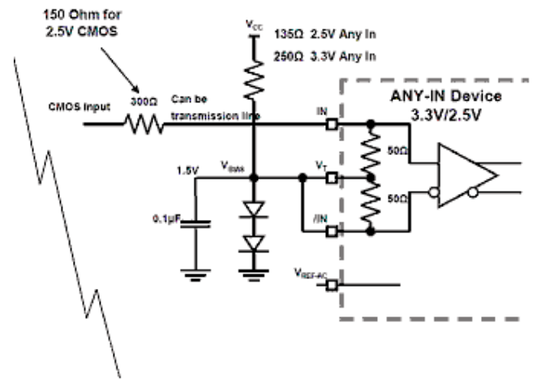
TEST GCK U40 U39

LVDS GCK_CLK_P<5..1> GCK_CLK_N<5..1>

GCK1 = CLK CPLD
GCK2 = CLK U1
GCK3 = CLK U2
GCK4 = CLK U3
GCK5 = CLK U4

2V5 C392 100NF C393 100NF C394 100NF C395 100NF C396 100NF C397 100NF C398 100NF C399 100NF C400 100NF C401 100NF C402 100NF

CMOS to Any-In



	NAME	DATE	SIGN
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CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	17.08.2015	

BLOCK TITLE: CLOCK

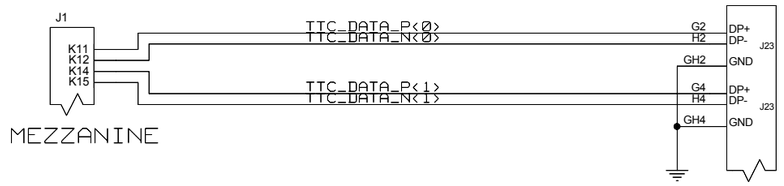
DRAWING TITLE: JFEX
GLOBAL CLOCK

UNIVERSITY OF MAINZ

SIZE C | REV. | DRAWING NO.

SCALE | SHEET 5 OF 5

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



	NAME	DATE	SIGN
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CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	06.11.2015	

BLOCK TITLE: CLOCK

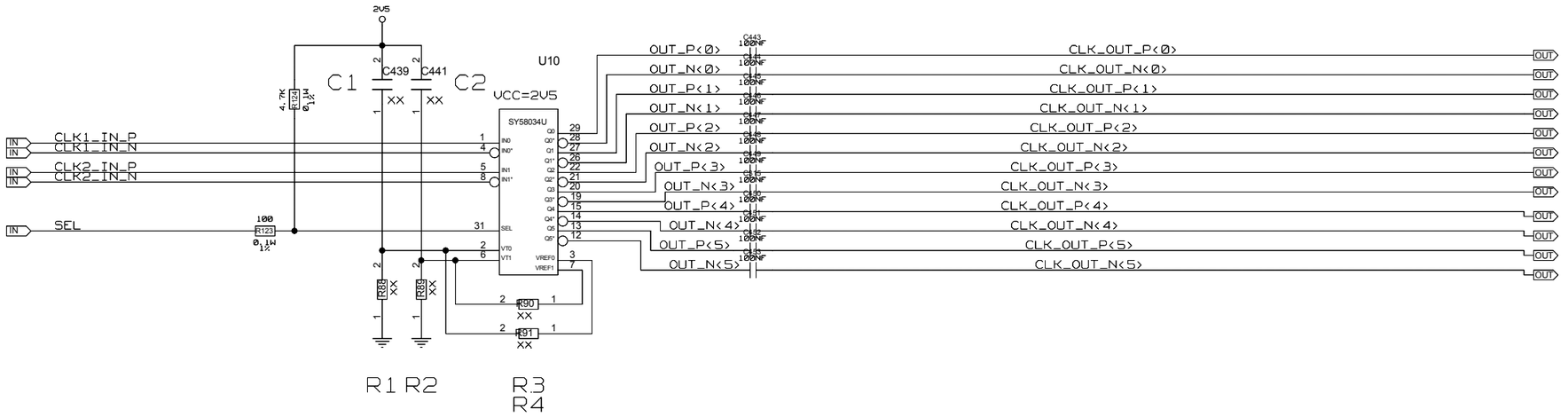
DRAWING TITLE: JFEX
TTC DATA

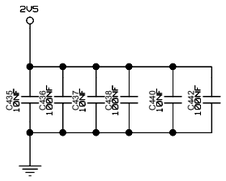
UNIVERSITY OF MAINZ

SIZE: C | REV. | DRAWING NO.

SCALE | SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.





INPUT INTERFACE APPLICATIONS

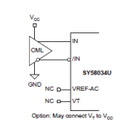


Figure 3a. CML Interface (DC-Coupled)

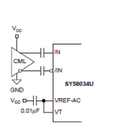


Figure 3b. CML Interface (AC-Coupled)

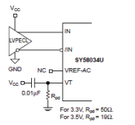


Figure 3c. LVPECL Interface (DC-Coupled)

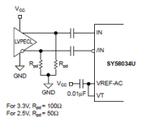


Figure 3d. LVPECL Interface (AC-Coupled)

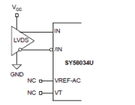


Figure 3e. LVDS Interface

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE

CLOCK CML FANOUT6 AC

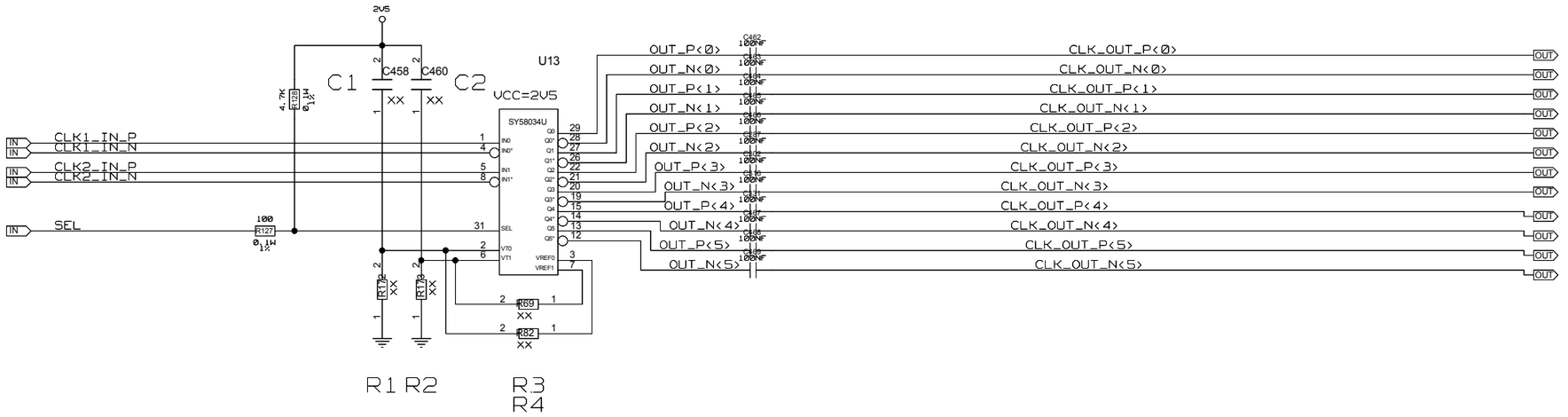
DRAWING TITLE

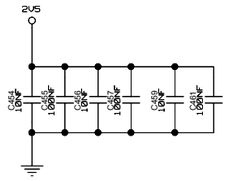
CLOCK

UNIVERSITY OF MAINZ

SIZE	REV.	DRAWING NO.
C		
SCALE		SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.





INPUT INTERFACE APPLICATIONS

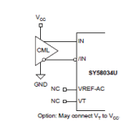


Figure 3a. CML Interface (DC-Coupled)

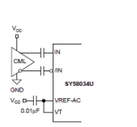


Figure 3b. CML Interface (AC-Coupled)

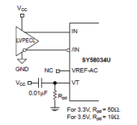


Figure 3c. LVPECL Interface (DC-Coupled)

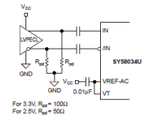


Figure 3d. LVPECL Interface (AC-Coupled)

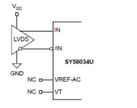


Figure 3e. LVDS Interface

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE

CLOCK CML FANOUT6 AC

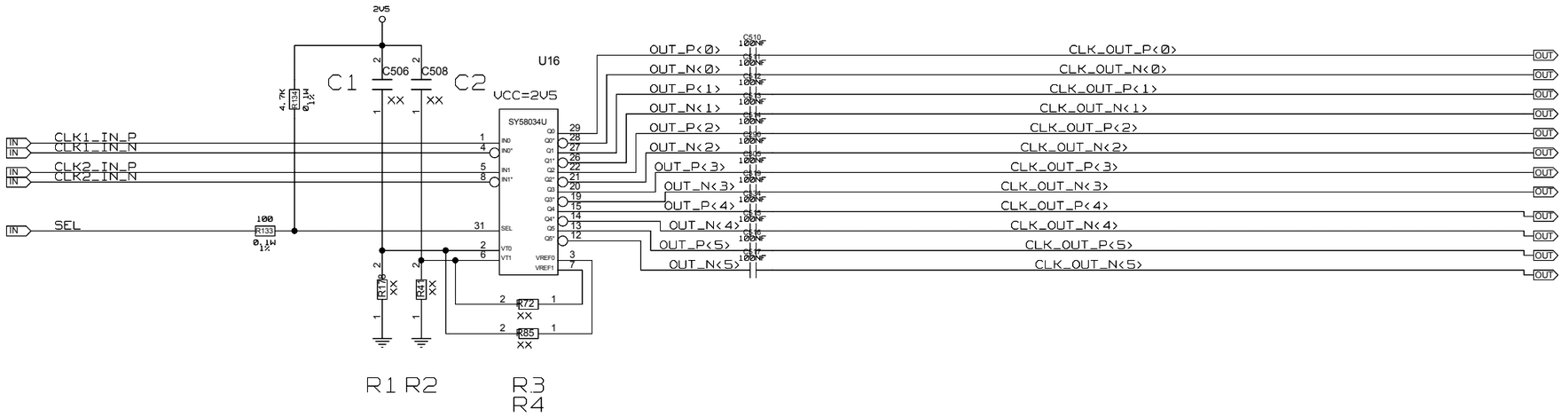
DRAWING TITLE

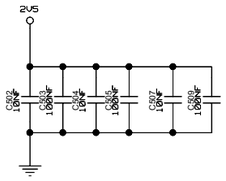
CLOCK

UNIVERSITY OF MAINZ

SIZE	REV.	DRAWING NO.
C		
SCALE		SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.





INPUT INTERFACE APPLICATIONS

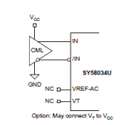


Figure 3a. CML Interface (DC-Coupled)

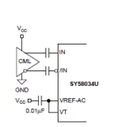


Figure 3b. CML Interface (AC-Coupled)

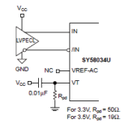


Figure 3c. LVPECL Interface (DC-Coupled)

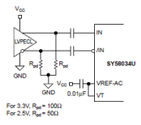


Figure 3d. LVPECL Interface (AC-Coupled)

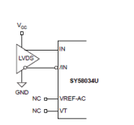


Figure 3e. LVDS Interface

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE

CLOCK CML FANOUT6 AC

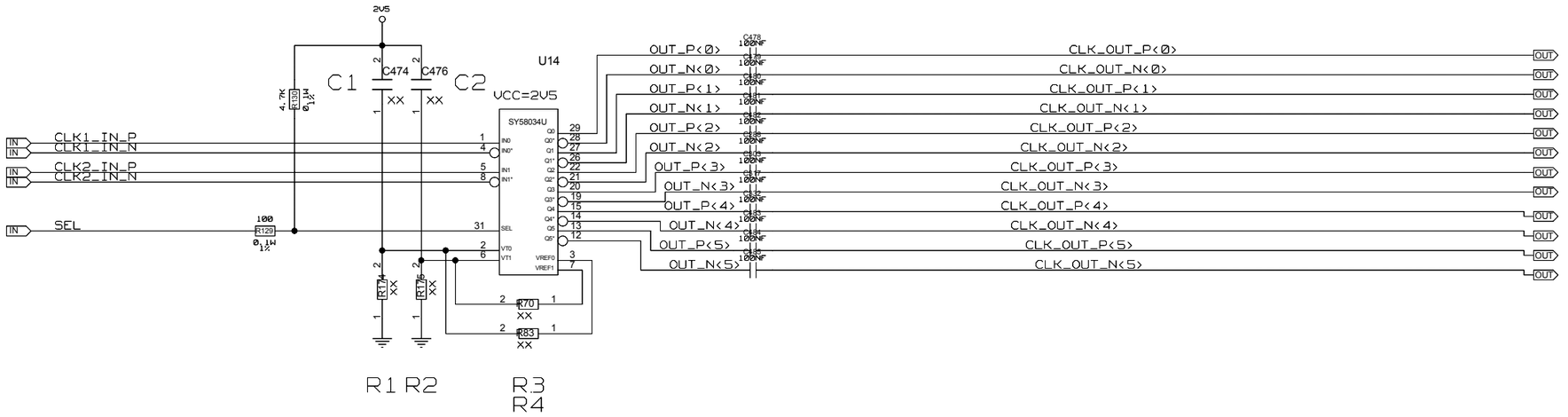
DRAWING TITLE

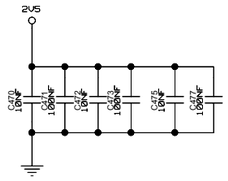
CLOCK

UNIVERSITY OF MAINZ

SIZE	REV.	DRAWING NO.
C		
SCALE		SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.





INPUT INTERFACE APPLICATIONS

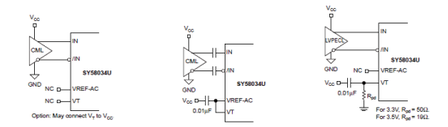


Figure 3a. CML Interface (DC-Coupled)

Figure 3b. CML Interface (AC-Coupled)

Figure 3c. LVPECL Interface (DC-Coupled)

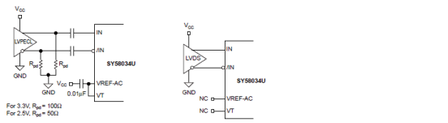


Figure 3d. LVPECL Interface (AC-Coupled)

Figure 3e. LVDS Interface

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE

CLOCK CML FANOUT6 AC

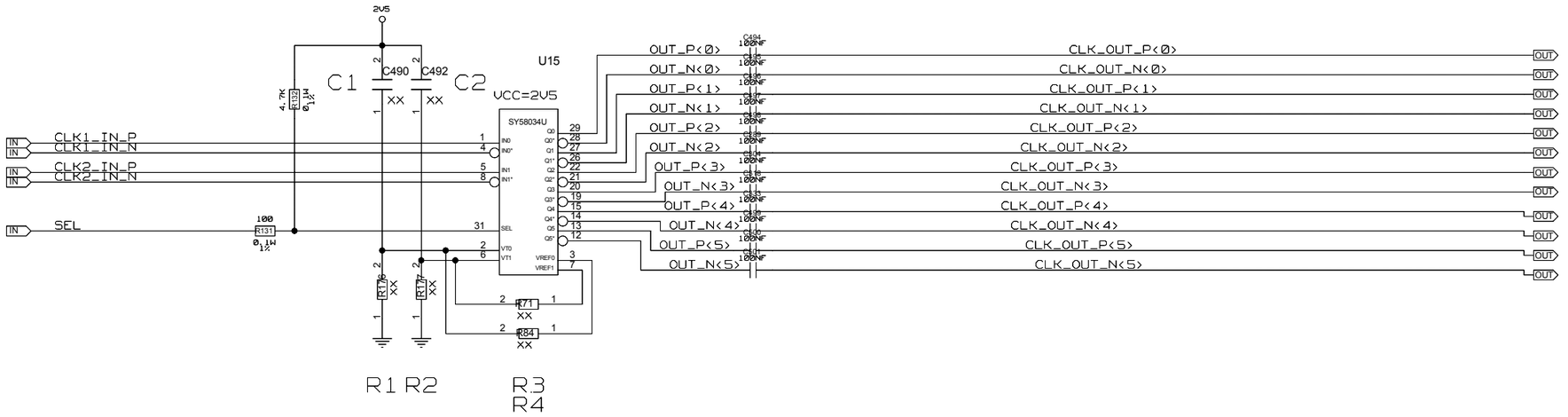
DRAWING TITLE

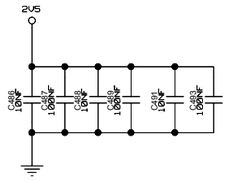
CLOCK

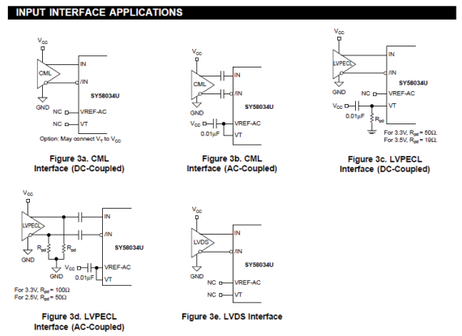
UNIVERSITY OF MAINZ

SIZE	REV.	DRAWING NO.
C		
SCALE		SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.







	NAME	DATE	SIGN
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CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE

CLOCK CML FANOUT6 AC

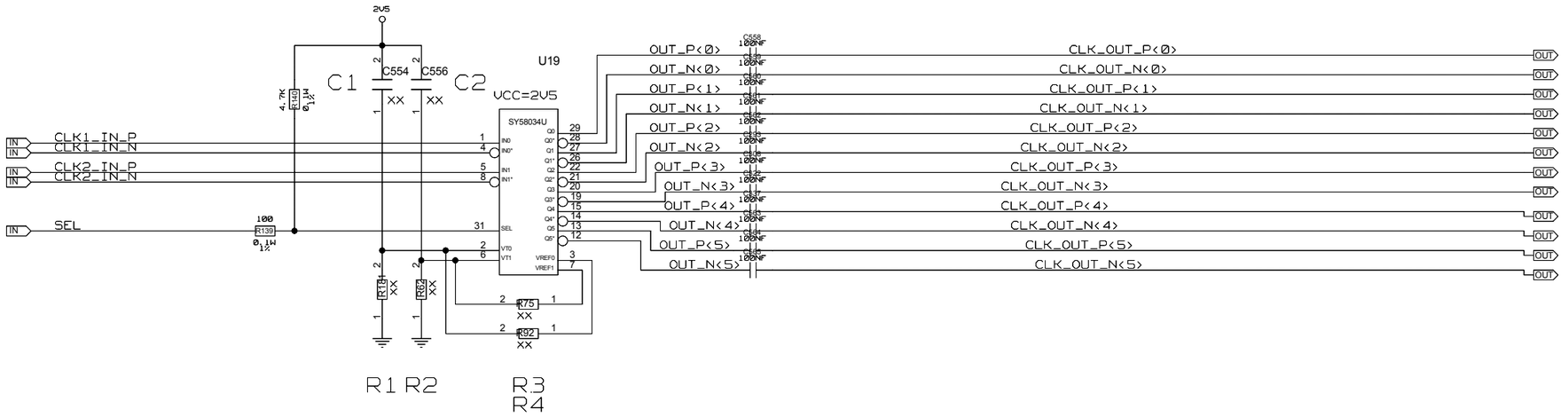
DRAWING TITLE

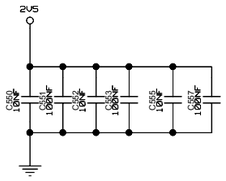
CLOCK

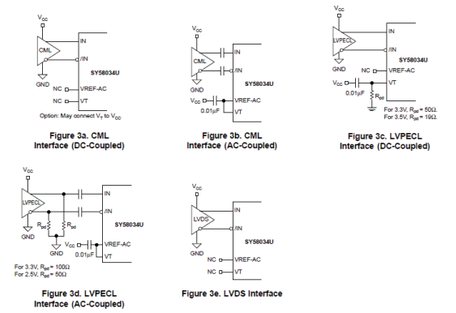
UNIVERSITY OF MAINZ

SIZE	REV.	DRAWING NO.
C		
SCALE		SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.







	NAME	DATE	SIGN
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CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE: CLOCK CML FANOUT6 AC

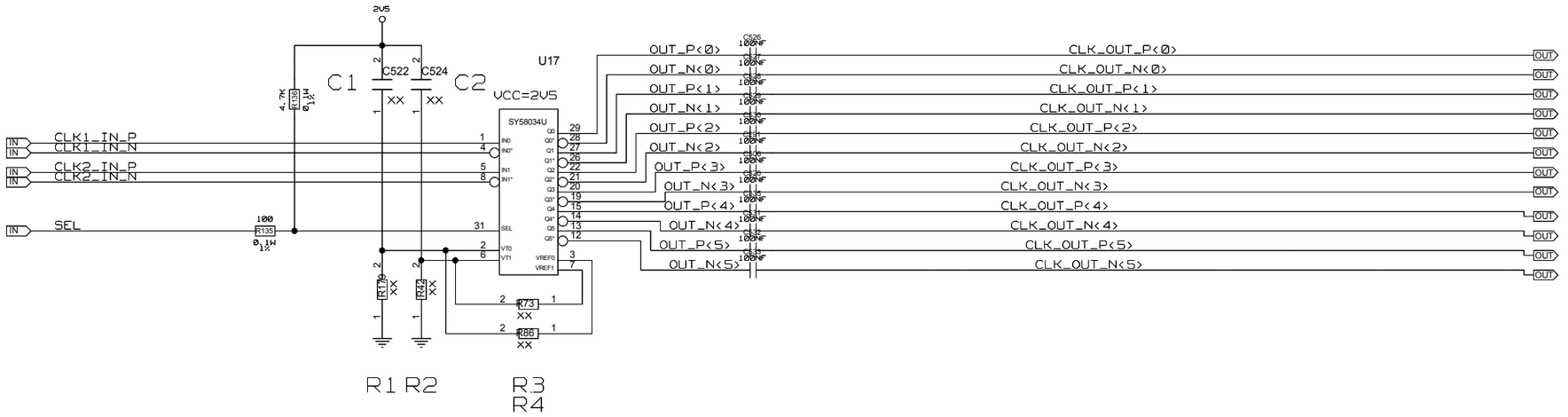
DRAWING TITLE: CLOCK

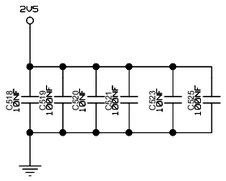
UNIVERSITY OF MAINZ

SIZE: C | REV. | DRAWING NO.

SCALE | SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.





INPUT INTERFACE APPLICATIONS

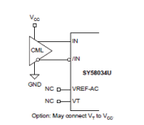


Figure 3a. CML Interface (DC-Coupled)

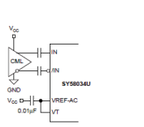


Figure 3b. CML Interface (AC-Coupled)

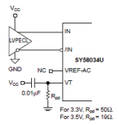


Figure 3c. LVPECL Interface (DC-Coupled)

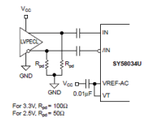


Figure 3d. LVPECL Interface (AC-Coupled)

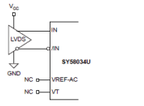


Figure 3e. LVDS Interface

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE: CLOCK CML FANOUT6 AC

DRAWING TITLE: CLOCK

UNIVERSITY OF MAINZ

SIZE: C | REV. | DRAWING NO.

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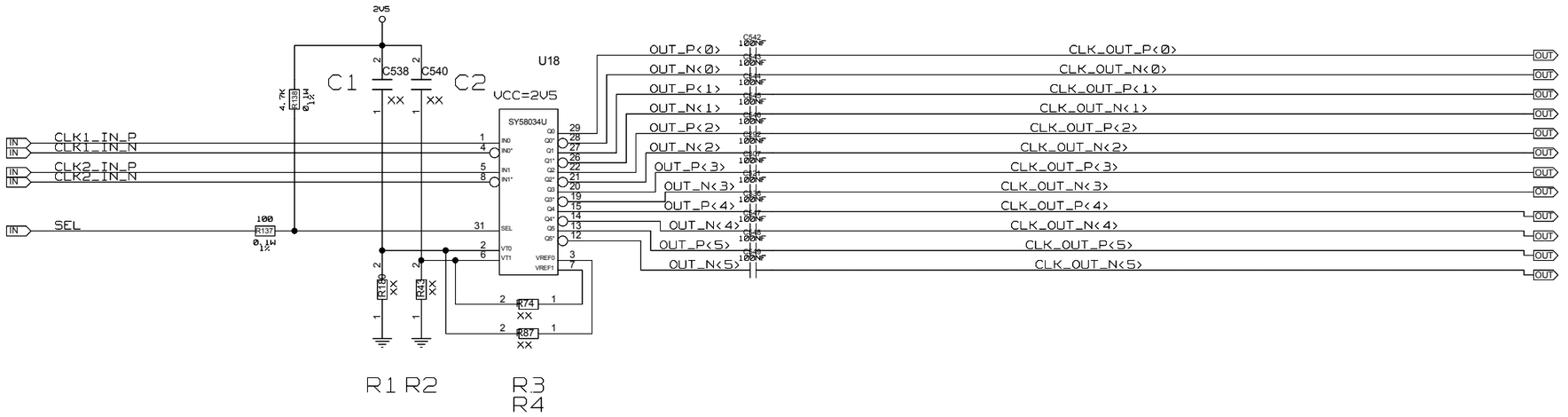
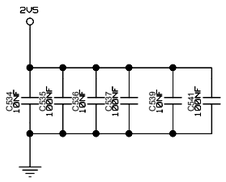
REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.





INPUT INTERFACE APPLICATIONS

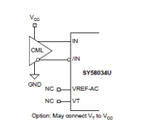


Figure 3a. CML Interface (DC-Coupled)

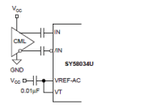


Figure 3b. CML Interface (AC-Coupled)

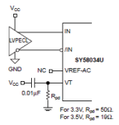


Figure 3c. LVPECL Interface (DC-Coupled)

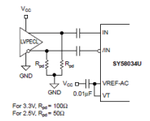


Figure 3d. LVPECL Interface (AC-Coupled)

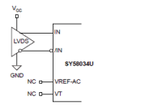


Figure 3e. LVDS Interface

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE

CLOCK CML FANOUT6 AC

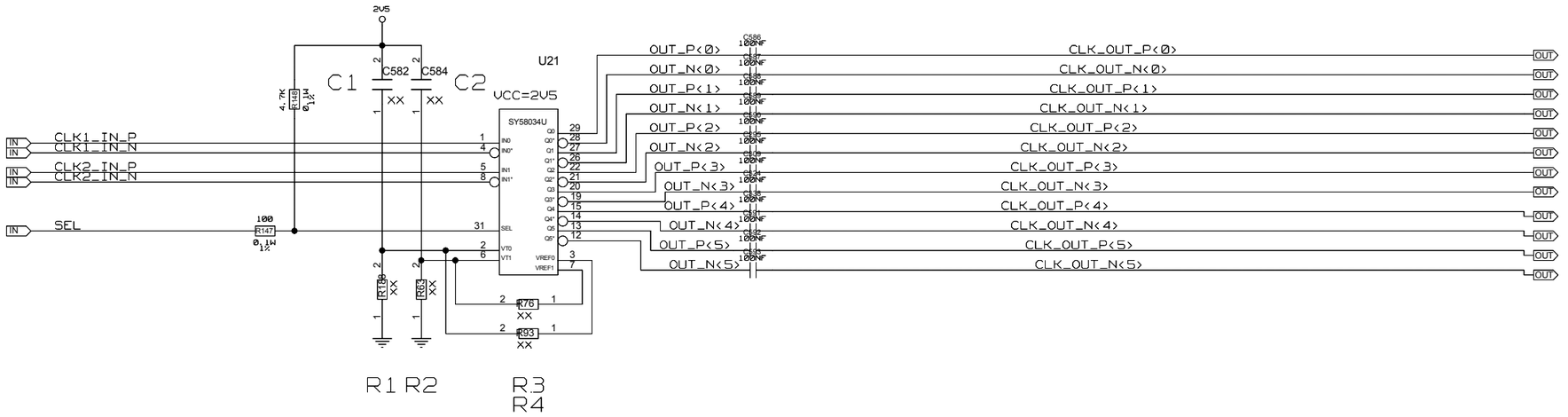
DRAWING TITLE

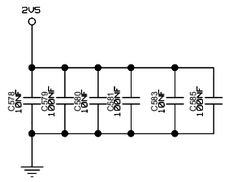
CLOCK

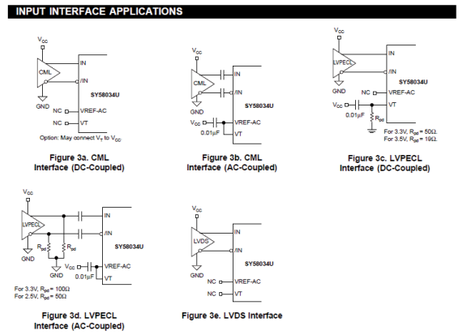
UNIVERSITY OF MAINZ

SIZE	REV.	DRAWING NO.
C		
SCALE		SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.







	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE

CLOCK CML FANOUT6 AC

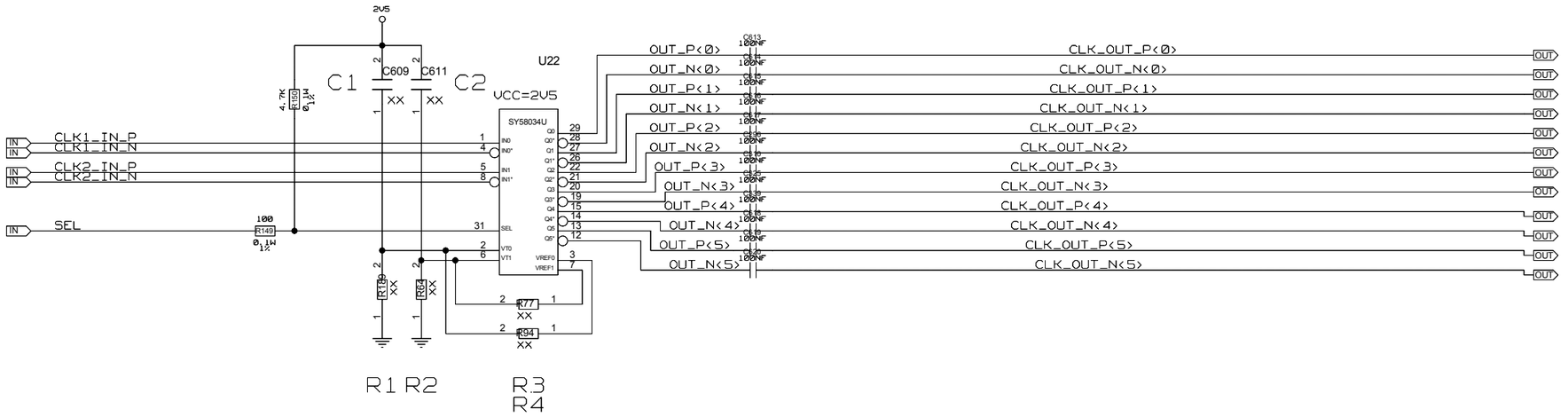
DRAWING TITLE

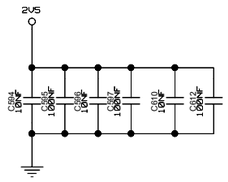
CLOCK

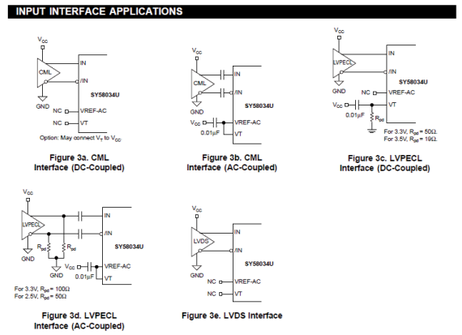
UNIVERSITY OF MAINZ

SIZE	REV.	DRAWING NO.
C		
SCALE		SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.







	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE

CLOCK CML FANOUT6 AC

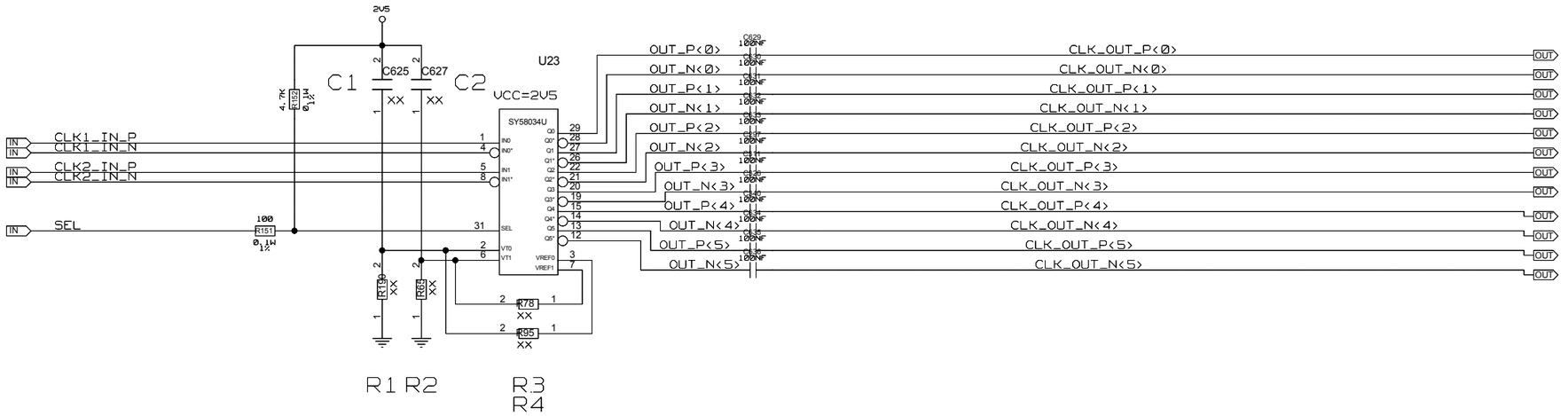
DRAWING TITLE

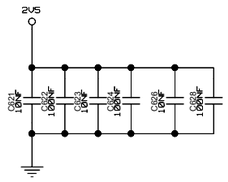
CLOCK

UNIVERSITY OF MAINZ

SIZE	REV.	DRAWING NO.
C		
SCALE		SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.





INPUT INTERFACE APPLICATIONS

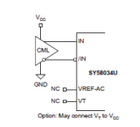


Figure 3a. CML Interface (DC-Coupled)

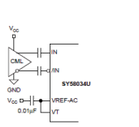


Figure 3b. CML Interface (AC-Coupled)

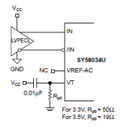


Figure 3c. LVPECL Interface (DC-Coupled)

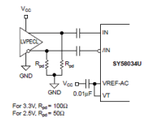


Figure 3d. LVPECL Interface (AC-Coupled)

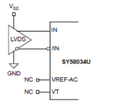


Figure 3e. LVDS Interface

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE

CLOCK CML FANOUT6 AC

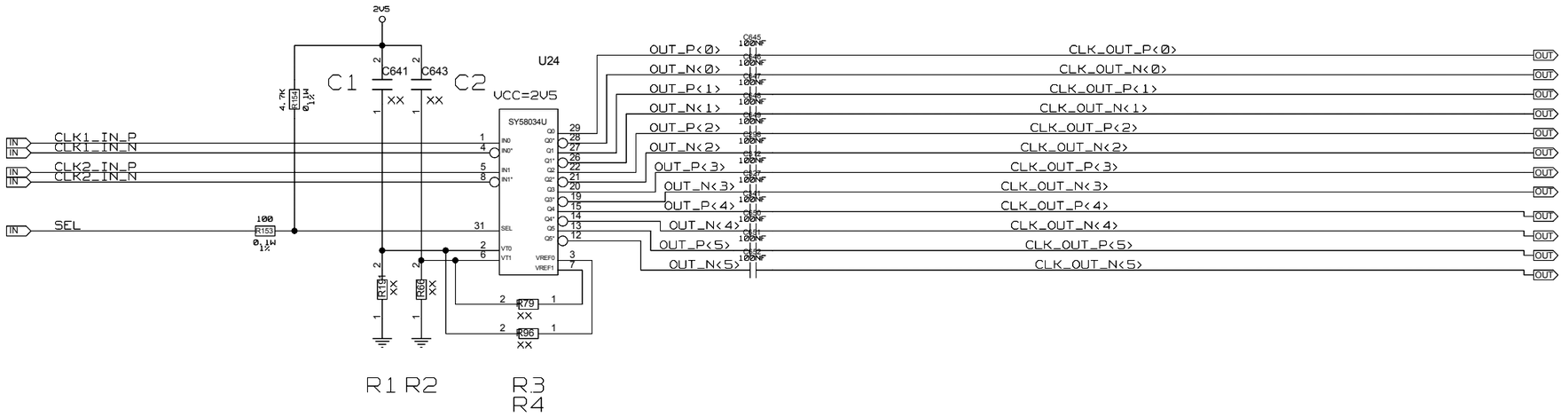
DRAWING TITLE

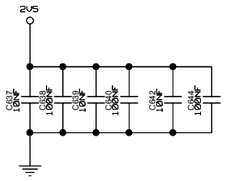
CLOCK

UNIVERSITY OF MAINZ

SIZE	REV.	DRAWING NO.
C		
SCALE		SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.





INPUT INTERFACE APPLICATIONS

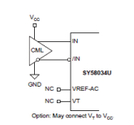


Figure 3a. CML Interface (DC-Coupled)

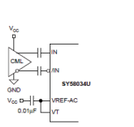


Figure 3b. CML Interface (AC-Coupled)

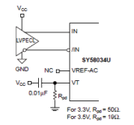


Figure 3c. LVPECL Interface (DC-Coupled)

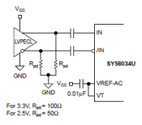


Figure 3d. LVPECL Interface (AC-Coupled)

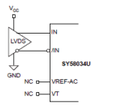


Figure 3e. LVDS Interface

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE

CLOCK CML FANOUT6 AC

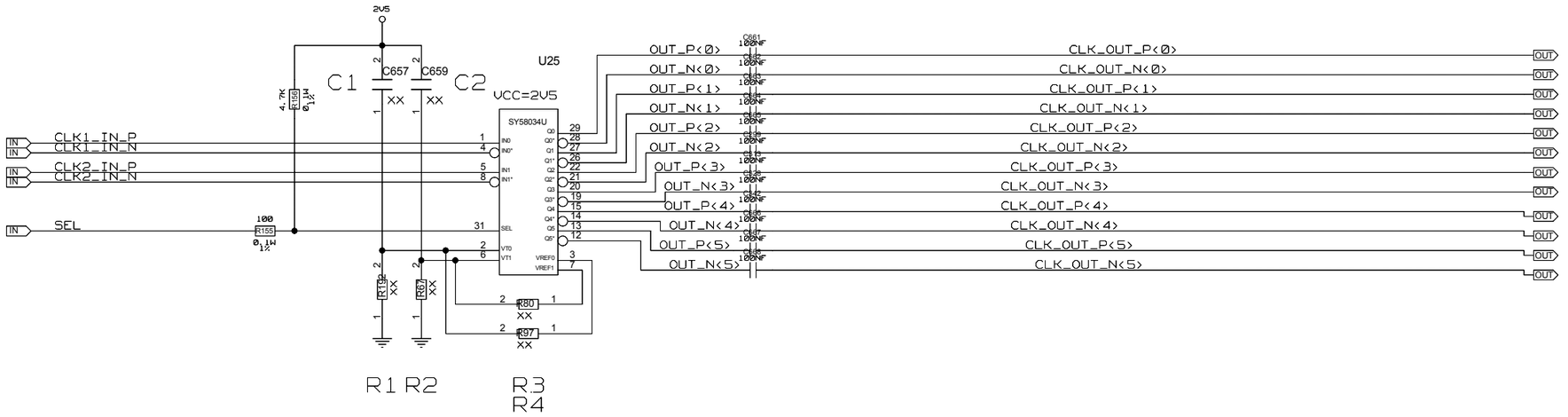
DRAWING TITLE

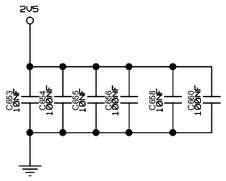
CLOCK

UNIVERSITY OF MAINZ

SIZE	REV.	DRAWING NO.
C		
SCALE		SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.





INPUT INTERFACE APPLICATIONS

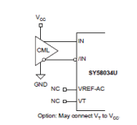


Figure 3a. CML Interface (DC-Coupled)

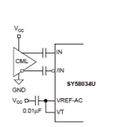


Figure 3b. CML Interface (AC-Coupled)

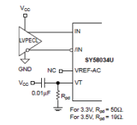


Figure 3c. LVPECL Interface (DC-Coupled)

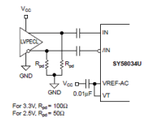


Figure 3d. LVPECL Interface (AC-Coupled)

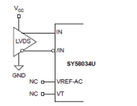


Figure 3e. LVDS Interface

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE

CLOCK CML FANOUT6 AC

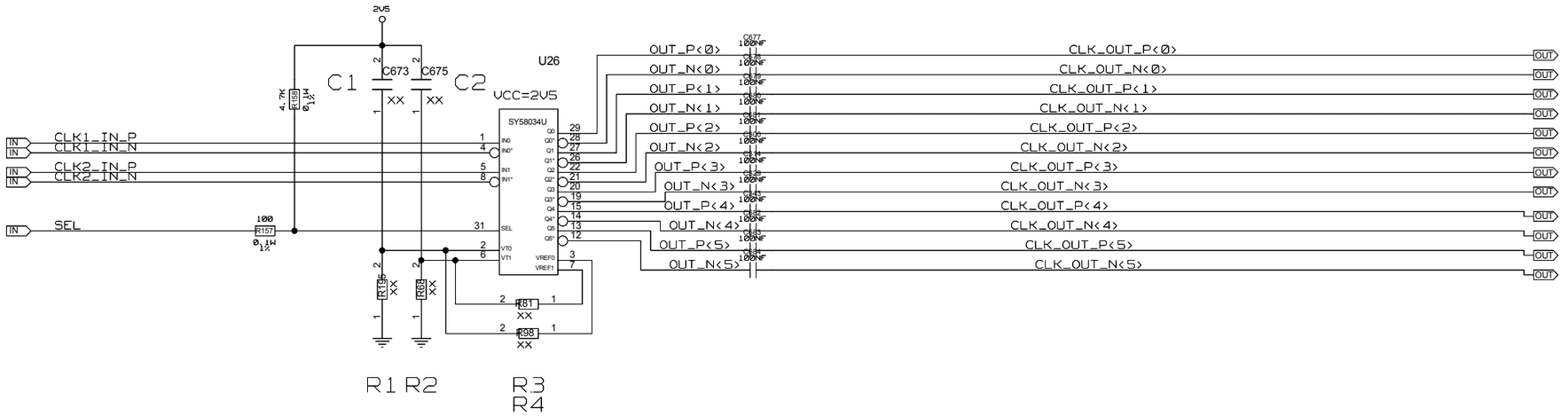
DRAWING TITLE

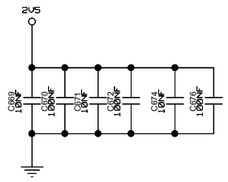
CLOCK

UNIVERSITY OF MAINZ

SIZE	REV.	DRAWING NO.
C		
SCALE		SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.





INPUT INTERFACE APPLICATIONS

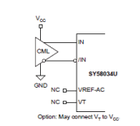


Figure 3a. CML Interface (DC-Coupled)

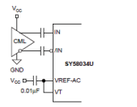


Figure 3b. CML Interface (AC-Coupled)

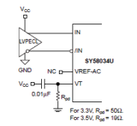


Figure 3c. LVPECL Interface (DC-Coupled)

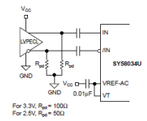


Figure 3d. LVPECL Interface (AC-Coupled)

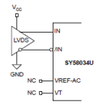


Figure 3e. LVDS Interface

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE

CLOCK CML FANOUT6 AC

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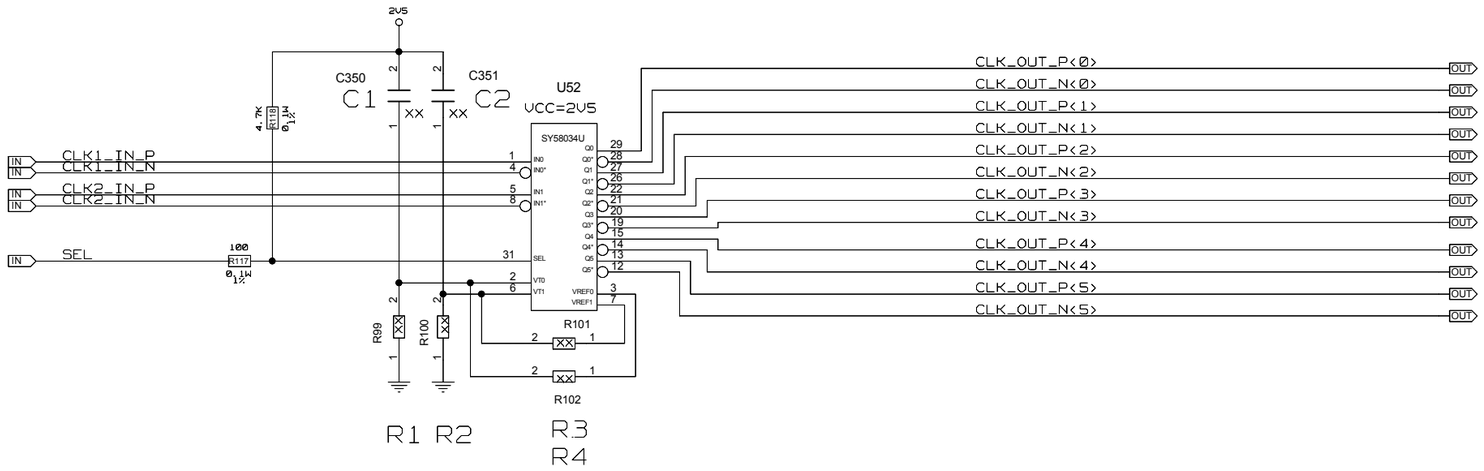
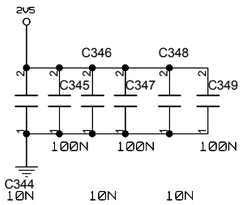
REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.





INPUT INTERFACE APPLICATIONS

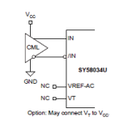


Figure 3a. CML Interface (DC-Coupled)

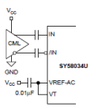


Figure 3b. CML Interface (AC-Coupled)

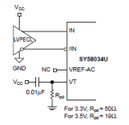


Figure 3c. LVPECL Interface (DC-Coupled)

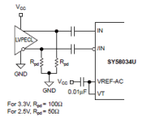


Figure 3d. LVPECL Interface (AC-Coupled)

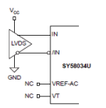


Figure 3e. LVDS Interface

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE
CLOCK_CML_FANOUT6_DC

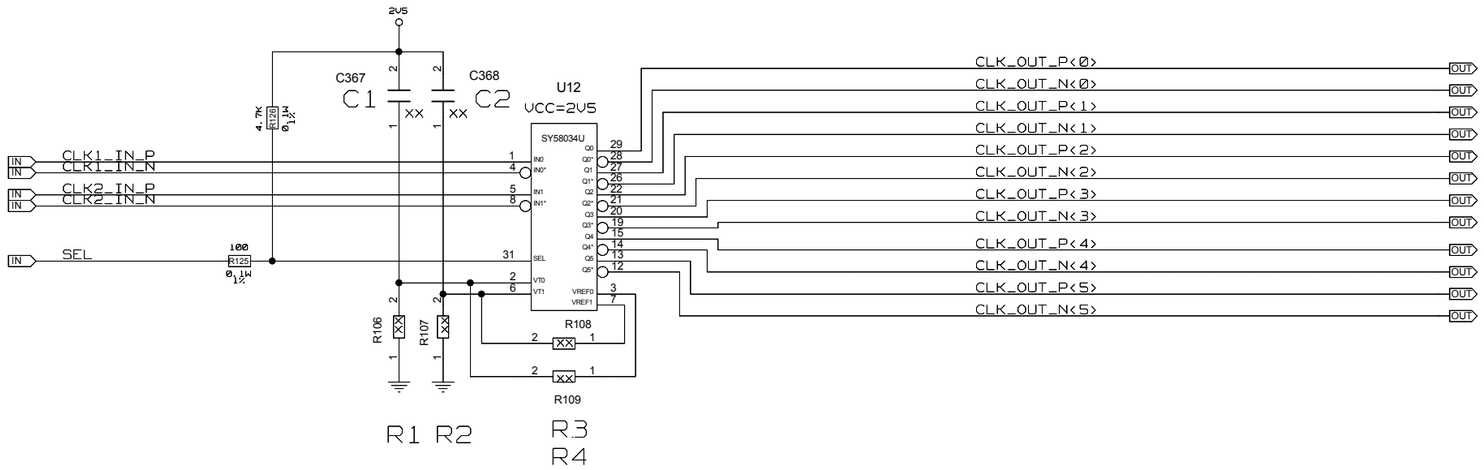
DRAWING TITLE
CLOCK

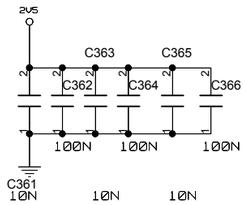
UNIVERSITY OF MAINZ

SIZE	REV.	DRAWING NO.
C		

SCALE | SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.





INPUT INTERFACE APPLICATIONS

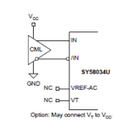


Figure 3a. CML Interface (DC-Coupled)

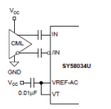


Figure 3b. CML Interface (AC-Coupled)

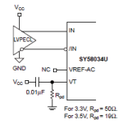


Figure 3c. LVPECL Interface (DC-Coupled)

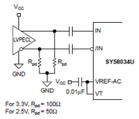


Figure 3d. LVPECL Interface (AC-Coupled)

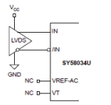


Figure 3e. LVDS Interface

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE
CLOCK_CML_FANOUT6_DC

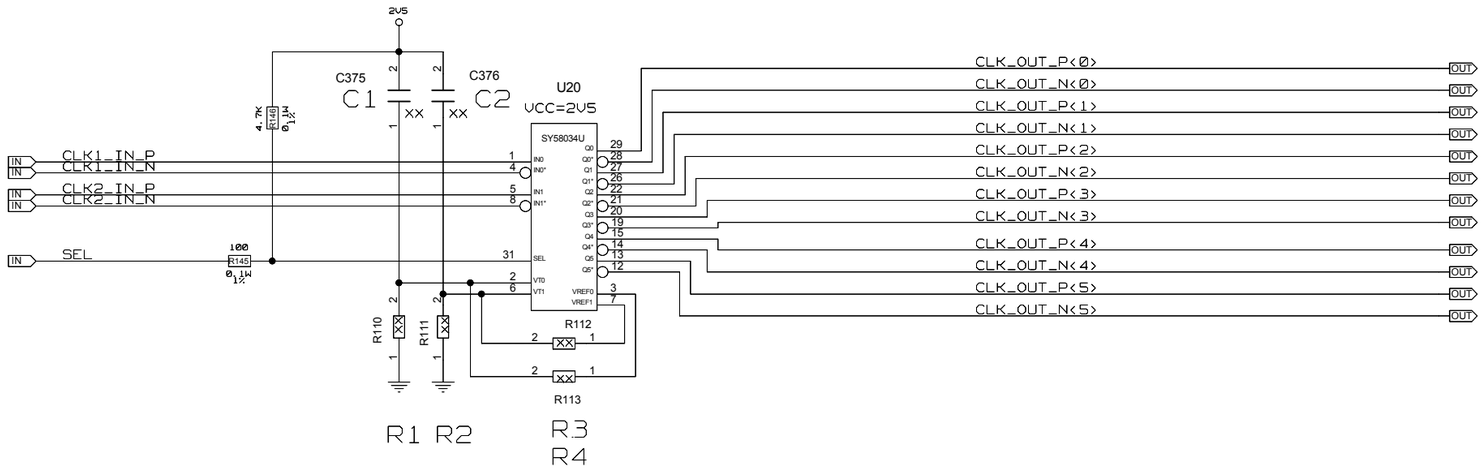
DRAWING TITLE
CLOCK

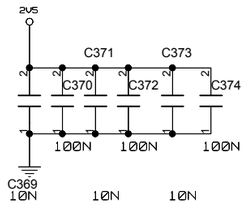
UNIVERSITY OF MAINZ

SIZE	REV.	DRAWING NO.
C		

SCALE | SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.





INPUT INTERFACE APPLICATIONS

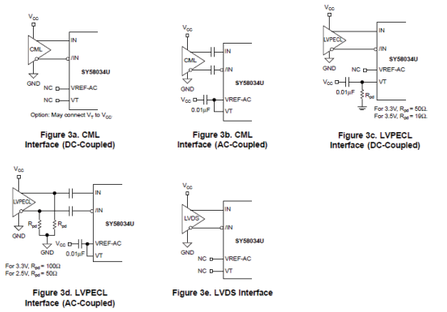


Figure 3a. CML Interface (DC-Coupled)

Figure 3b. CML Interface (AC-Coupled)

Figure 3c. LVPECL Interface (DC-Coupled)

Figure 3d. LVPECL Interface (AC-Coupled)

Figure 3e. LVDS Interface

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE
CLOCK_CML_FANOUT6_DC

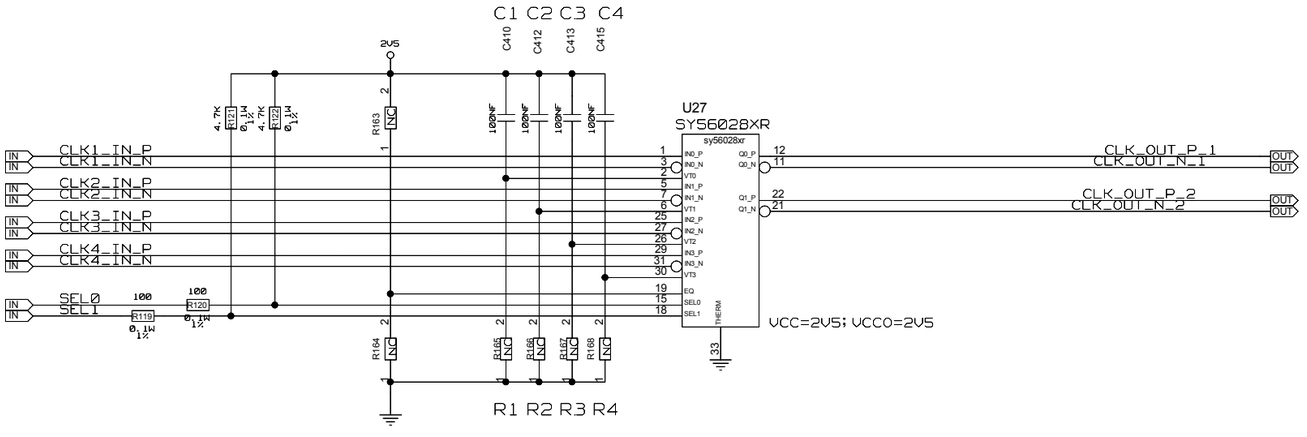
DRAWING TITLE
CLOCK

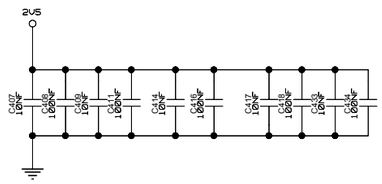
UNIVERSITY OF MAINZ

SIZE	REV.	DRAWING NO.
C		

SCALE | SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.





Input Interface Applications

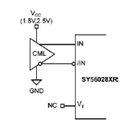


Figure 4a. CML Interface
100 Ω Differential
(DC-Coupled, 1.8V, 2.5V)

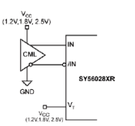


Figure 4b. CML Interface
50 Ω to Vcc
(DC-Coupled, 1.2V,1.8V,2.5V)

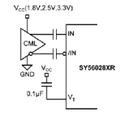


Figure 4c. CML Interface
(AC-Coupled)
*See note in Functional Description
for 1.2V CML driver with AC-Coupling

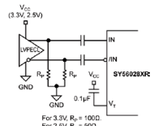


Figure 4d. LVPECL Interface
(AC-Coupled)

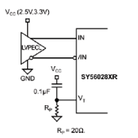


Figure 4e. LVPECL Interface
(DC-Coupled 2.5V and 3.3V)

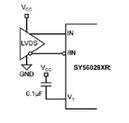


Figure 4f. LVDS Interface

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

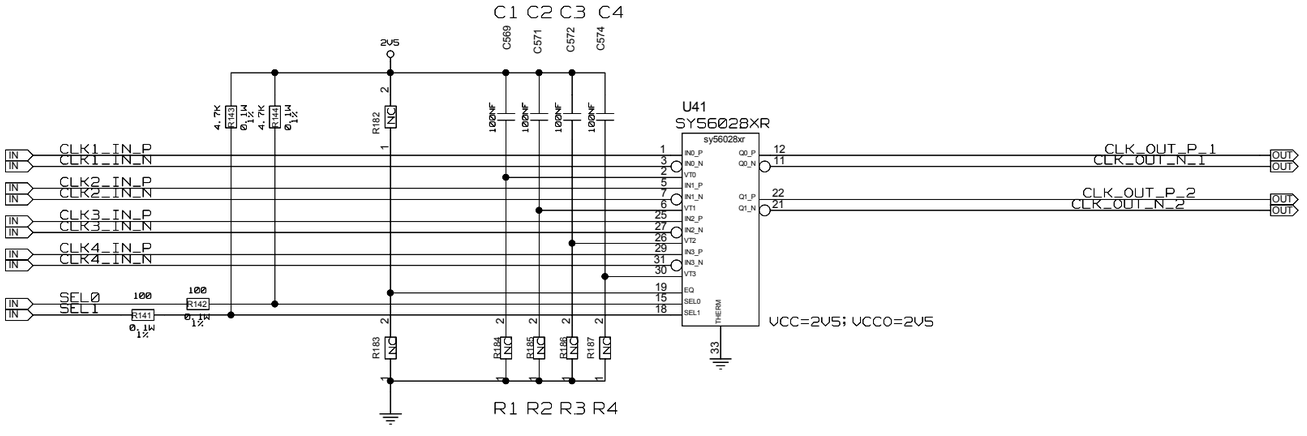
BLOCK TITLE
MUX_CML_4X2_DC

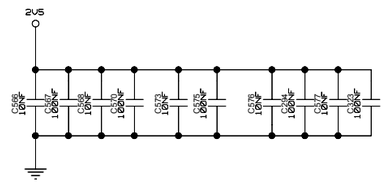
DRAWING TITLE
CLOCK

UNIVERSITY OF MAINZ

SIZE	REV.	DRAWING NO.
C		
SCALE		SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.





Input Interface Applications

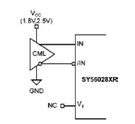


Figure 4a. CML Interface
100 Ω Differential
(DC-Coupled, 1.8V, 2.5V)

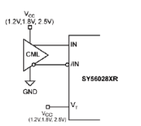


Figure 4b. CML Interface
50 Ω to Vcc
(DC-Coupled, 1.2V,1.8V,2.5V)

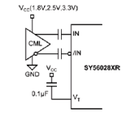


Figure 4c. CML Interface
(AC-Coupled)
*See note in Functional Description
for 1.2V CML driver with AC-Coupling

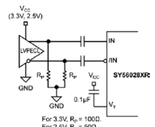


Figure 4d. LVPECL Interface
(AC-Coupled)

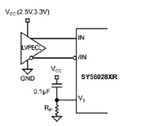


Figure 4e. LVPECL Interface
(DC-Coupled 2.5V and 3.3V)

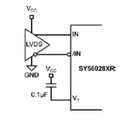


Figure 4f. LVDS Interface

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

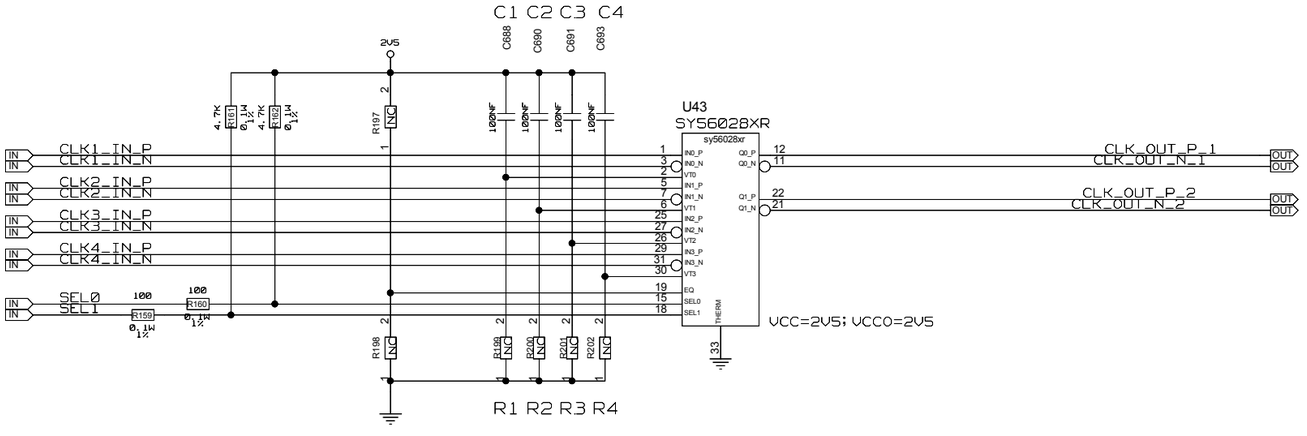
BLOCK TITLE
MUX_CML_4X2_DC

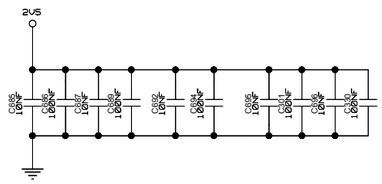
DRAWING TITLE
CLOCK

UNIVERSITY OF MAINZ

SIZE	REV.	DRAWING NO.
C		
SCALE		SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.





Input Interface Applications

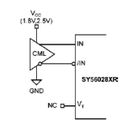


Figure 4a. CML Interface
100 Ω Differential
(DC-Coupled, 1.8V, 2.5V)

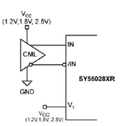


Figure 4b. CML Interface
50 Ω to Vcc
(DC-Coupled, 1.2V,1.8V,2.5V)

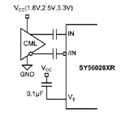


Figure 4c. CML Interface
(AC-Coupled)
*See note in Functional Description
for 1.2V CML driver with AC-Coupling

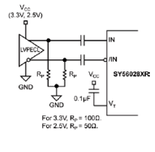


Figure 4d. LVPECL Interface
(AC-Coupled)

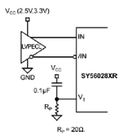


Figure 4e. LVPECL Interface
(DC-Coupled 2.5V and 3.3V)

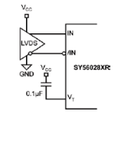


Figure 4f. LVDS Interface

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

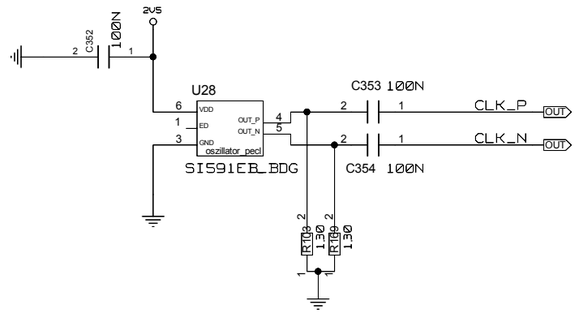
BLOCK TITLE
MUX_CML_4X2_DC

DRAWING TITLE
CLOCK

UNIVERSITY OF MAINZ

SIZE	REV.	DRAWING NO.
C		
SCALE		SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE
CLOCK_LVPECL_AC

DRAWING TITLE
CLOCK

UNIVERSITY OF MAINZ

SIZE C | REV. | DRAWING NO.

SCALE | SHEET OF

REVISIONS				
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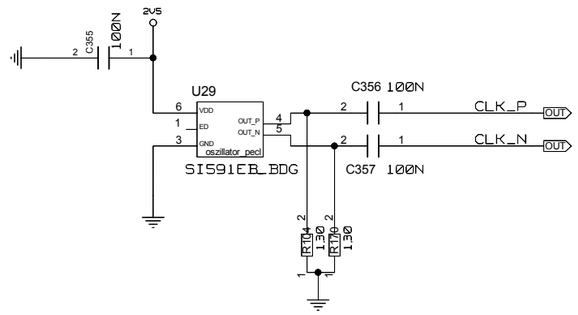

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE
CLOCK_LVPECL_AC

DRAWING TITLE
CLOCK

UNIVERSITY OF MAINZ

SIZE C | REV. | DRAWING NO.

SCALE | SHEET OF

REVISIONS				
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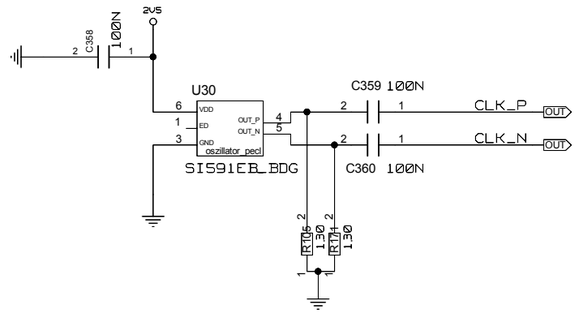


	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE
CLOCK_LVPECL_AC

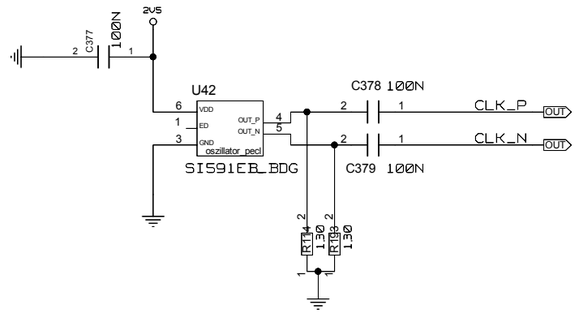
DRAWING TITLE
CLOCK

UNIVERSITY OF MAINZ

SIZE C | REV. | DRAWING NO.

SCALE | SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE
CLOCK_LVPECL_AC

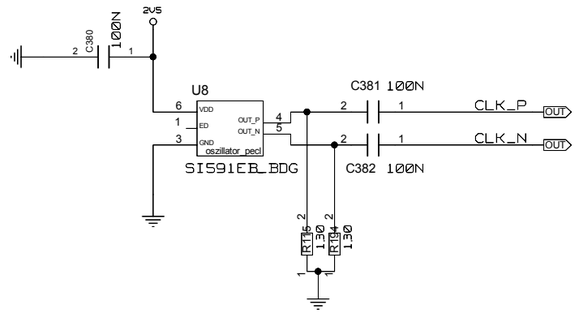
DRAWING TITLE
CLOCK

UNIVERSITY OF MAINZ

SIZE C | REV. | DRAWING NO.

SCALE | SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE
CLOCK_LVPECL_AC

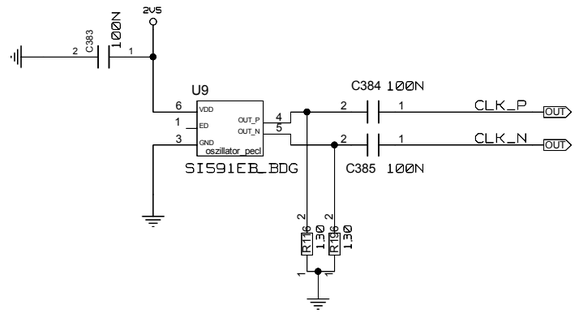
DRAWING TITLE
CLOCK

UNIVERSITY OF MAINZ

SIZE C | REV. | DRAWING NO.

SCALE | SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE
CLOCK_LVPECL_AC

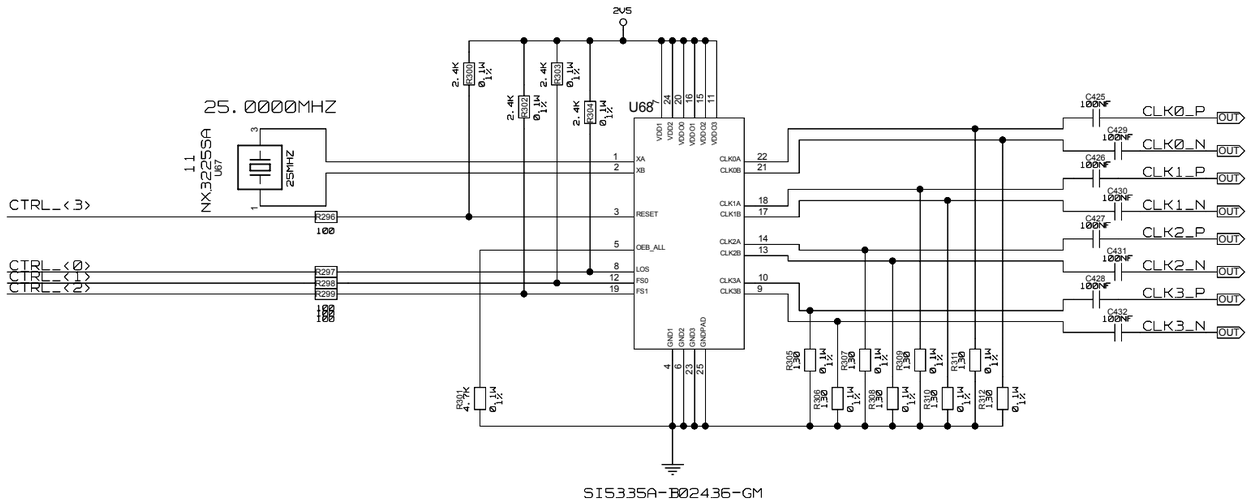
DRAWING TITLE
CLOCK

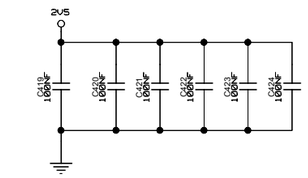
UNIVERSITY OF MAINZ

SIZE C | REV. | DRAWING NO.

SCALE | SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.





TO CTRL_<3..0>

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE: QUAD_CLOCK_GEN

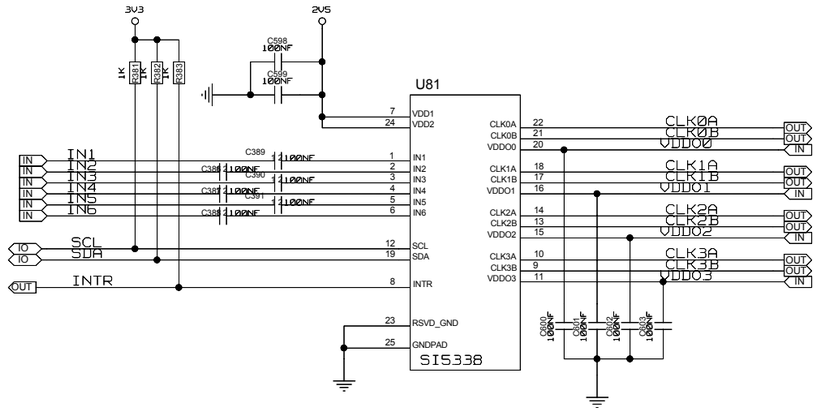
DRAWING TITLE: CLOCK

UNIVERSITY OF MAINZ

SIZE: C | REV. | DRAWING NO.

SCALE | SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	02.12.2015	

BLOCK TITLE: JITTERCLEANER_SI5338

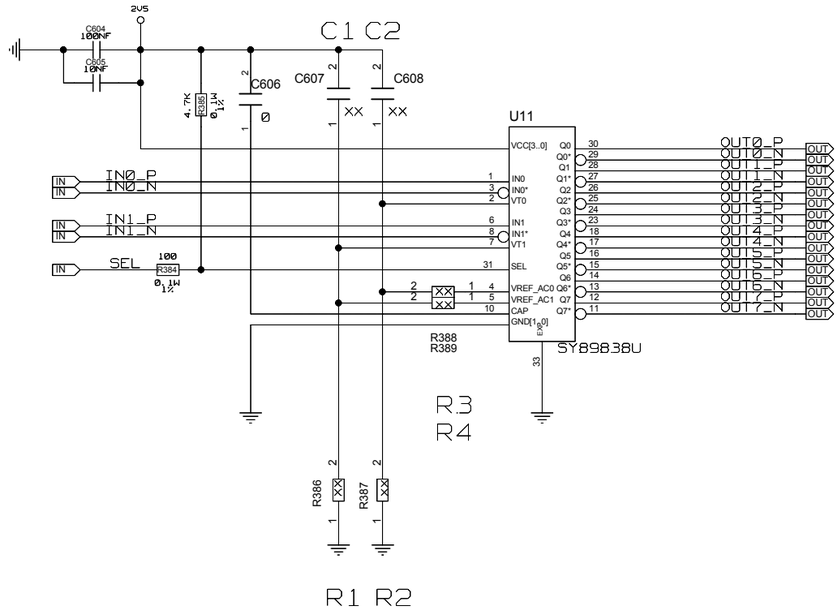
DRAWING TITLE: CLOCK

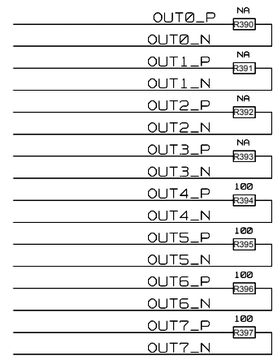
UNIVERSITY OF MAINZ

SIZE: C | REV. | DRAWING NO.

SCALE | SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.





	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE
CLOCK_LVDS_FANOUT8

DRAWING TITLE
CLOCK

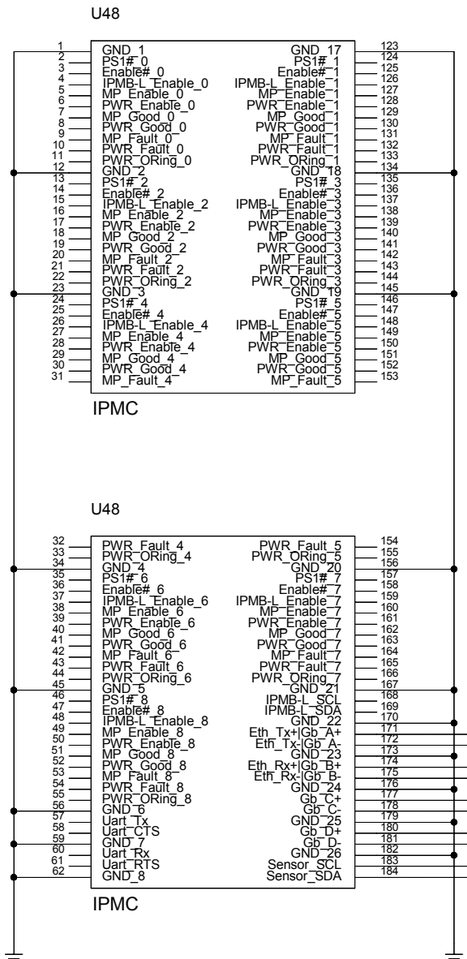
UNIVERSITY OF MAINZ

SIZE C | REV. | DRAWING NO.

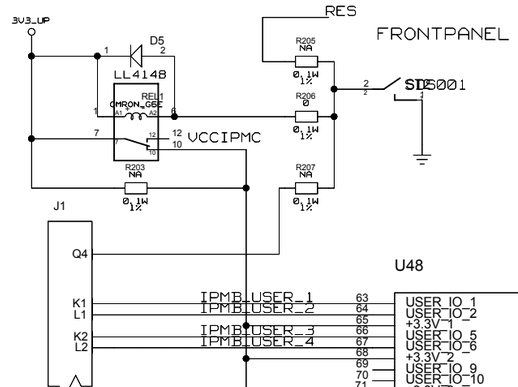
SCALE | SHEET OF

IPMC CONNECTOR MOLEX_87782-3003

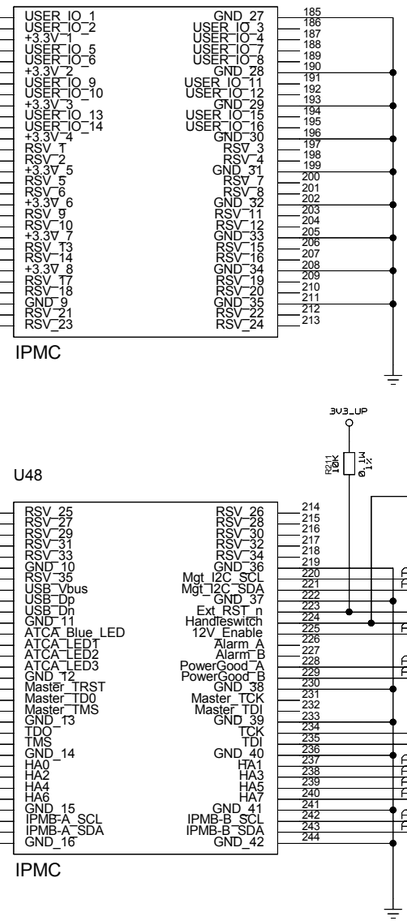
REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



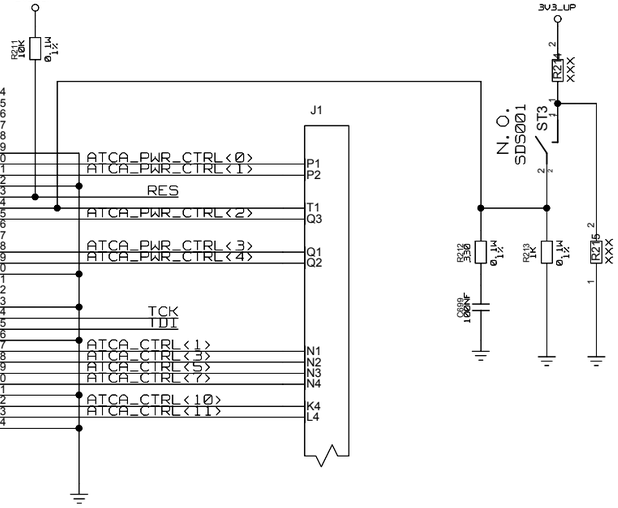
GB_A_P
GB_A_N
GB_B_P
GB_B_N
GB_C_P
GB_C_N
GB_D_P
GB_D_N
IPMC_SENS_SCL
IPMC_SENS_SDA



IPMB_USER_1
IPMB_USER_2
IPMB_USER_3
IPMB_USER_4

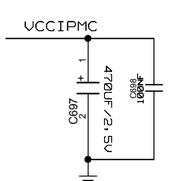


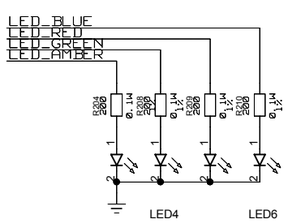
LED_BLUE
LED_RED
LED_GREEN
LED_AMBER
TDO
TMS
ATCA_CTRL<0>
ATCA_CTRL<2>
ATCA_CTRL<4>
ATCA_CTRL<6>
ATCA_CTRL<8>
ATCA_CTRL<9>



ATCA_HA<0..7> BACKPLANE + MEZZANINE
ATCA_A/B_SCL/SDA BACKPLANE + MEZZANINE

ATCA_CTRL<11..0>
ATCA_PWR_CTRL<4..0>





CHANGED 17.12.2015

FRONTPANEL

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	30.10.2015	

BLOCK TITLE: IPMC

DRAWING TITLE: IPMC CONTROL

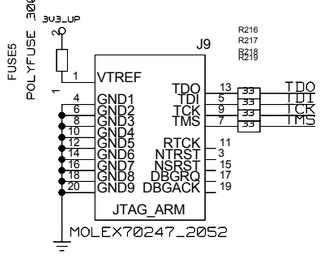
UNIVERSITY OF MAINZ

SIZE: C | REV. | DRAWING NO.

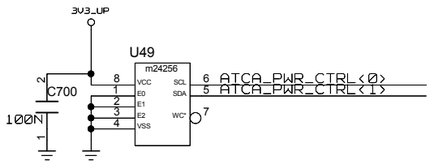
SCALE | SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

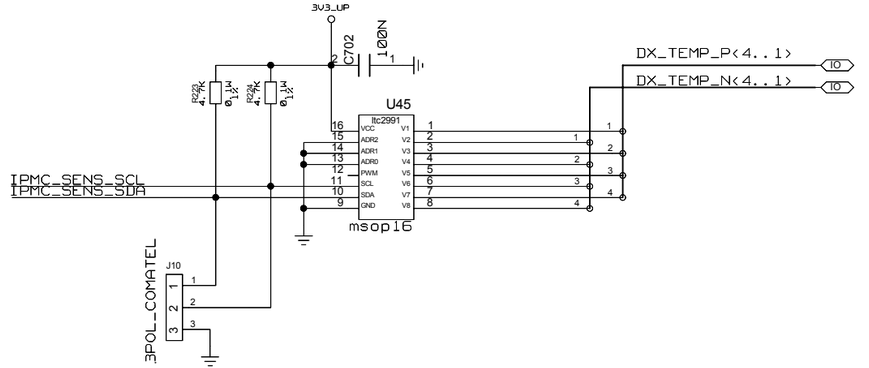
IPMC JTAG PORT FRONTPANEL



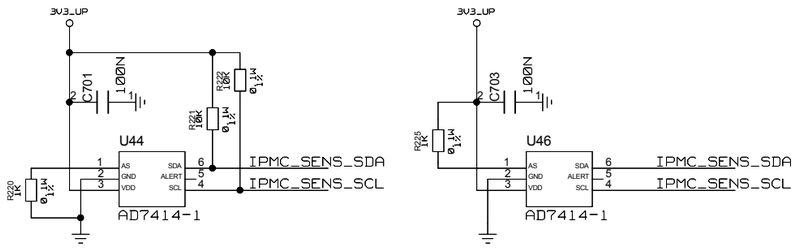
MEMORY



I2C FPGA TEMPERATURE



I2C BOARDTEMP



	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	23.07.2015	

BLOCK TITLE: IPMC

DRAWING TITLE: IPMC CONTROL

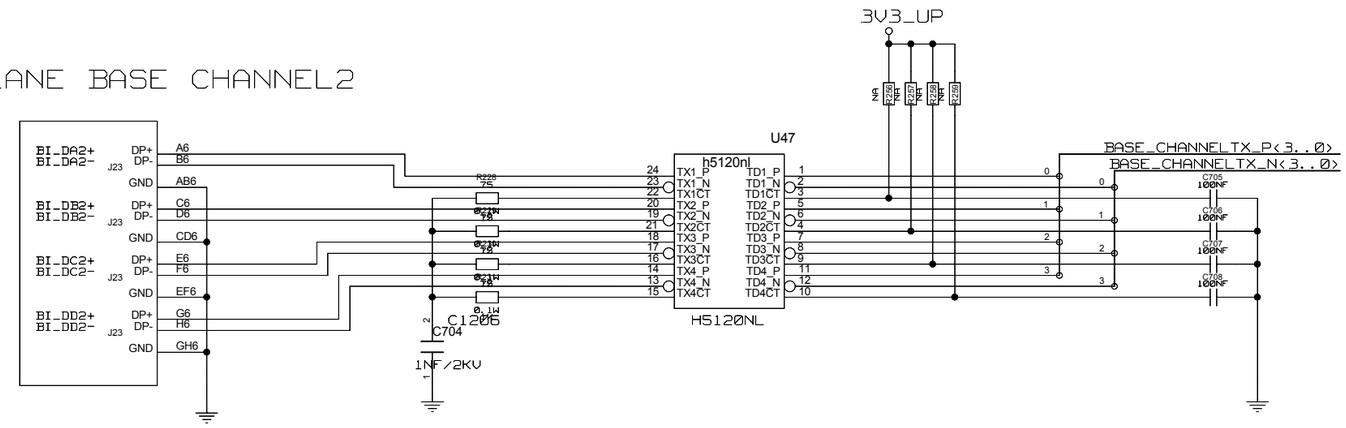
UNIVERSITY OF MAINZ

SIZE: C | REV. | DRAWING NO.

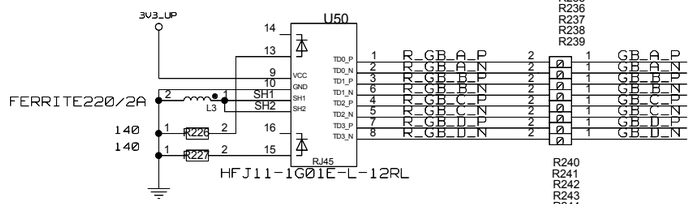
SCALE | SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

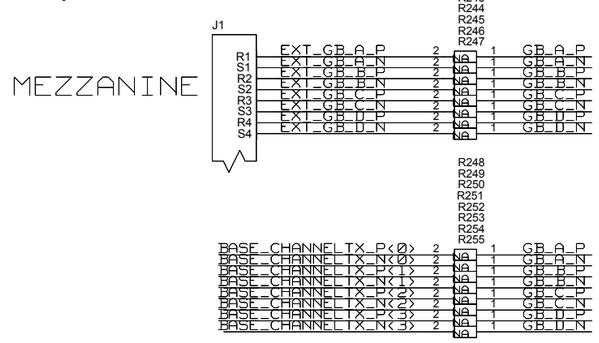
BACKPLANE BASE CHANNEL2



RJ45 FRONTPANEL



MEZZANINE



TO IPMC MODUL

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE: IPMC

DRAWING TITLE: IPMC CONTROL

UNIVERSITY OF MAINZ

SIZE: C | REV. | DRAWING NO.

SCALE | SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

U51

1KHZ
CPLD_CLK1
GCK_SINGLE

CPLD_IO_U1<11..0>
CPLD_IO_U2<11..0>
CPLD_IO_U3<11..0>
CPLD_IO_U4<11..0>

J1
MEZZANINE
CPLD_EXT<0>
CPLD_EXT<1>
CPLD_EXT<2>
CPLD_EXT<3>
CPLD_EXT<4>
CPLD_EXT<5>
CPLD_EXT<6>
CPLD_EXT<7>
CPLD_EXT<8>
CPLD_EXT<9>
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CPLD_EXT<13>
CPLD_EXT<14>
CPLD_EXT<15>
CPLD_EXT<16>
CPLD_EXT<17>
CPLD_EXT<18>
CPLD_EXT<19>

GCK0 GCK1 GCK2
IO_R1_1 IO_N4_1 IO_N2_1 IO_P1_1 IO_L3_1 IO_N1_1 IO_L4_1 IO_M1_1 IO_L5_1 IO_N3_1 IO_P2_1 IO_P4_1 IO_R2_1 IO_T1_1 IO_T2_1 IO_N5_1 IO_R4_1 IO_M5_1 IO_R5_1 IO_K4_1 IO_L2_1 IO_K3_1 IO_L1_1 IO_K5_1 IO_K2_1 IO_J4_1 IO_K1_1 IO_J3_1 IO_J2_1 IO_J5_1 IO_J1_1 IO_R6_1 IO_N6_1 IO_R3_1 IO_M6_1 IO_T3_1 IO_P6_1 IO_T4_1 IO_P7_1 IO_T5_1 IO_N7_1 IO_R7_1 IO_M7_1 IO_R15_1 IO_T16_1 IO_N14_1 IO_R16_1 IO_N15_1 IO_M15_1 IO_M13_1 IO_P16_1 IO_N16_1 IO_L14_1 IO_M14_1 IO_P15_1 IO_P14_1 IO_P13_1 IO_R13_1 IO_N13_1 IO_R14_1 IO_T15_1 IO_R12_1 IO_N11_1 IO_M11_1 IO_N10_1 IO_L15_1 IO_L13_1 IO_M12_1 IO_M16_1 IO_K14_1 IO_L16_1 IO_K15_1 IO_L12_1 IO_K16_1 IO_J14_1 IO_J15_1 IO_J13_1 IO_P10_1 IO_R10_1 IO_T10_1 IO_R9_1 IO_N9_1 IO_M8_1 IO_T8_1 IO_P8_1 IO_R8_1 IO_T7_1 IO_N8_1 IO_T6_1

IO_B3_2 IO_B4_2 IO_C4_2 IO_A2_2 IO_A3_2 IO_A4_2 IO_B5_2 IO_A5_2 IO_E8_2 IO_B6_2 IO_C7_2 IO_D3_2 IO_C3_2 IO_E3_2 IO_B2_2 IO_D4_2 IO_D2_2 IO_E5_2 IO_B1_2 IO_E4_2 IO_C1_2 IO_E2_2 IO_A6_2 IO_D7_2 IO_B7_2 IO_E9_2 IO_A7_2 IO_D8_2 IO_B8_2 IO_C8_2 IO_A8_2 IO_E11_2 IO_E10_2 IO_F2_2 IO_F3_2 IO_G4_2 IO_G3_2 IO_F5_2 IO_G5_2 IO_H2_2 IO_H4_2 IO_H3_2 IO_H1_2 IO_H5_2 IO_B13_2 IO_B14_2 IO_C13_2 IO_A15_2 IO_C12_2 IO_B12_2 IO_D13_2 IO_A14_2 IO_E13_2 IO_A13_2 IO_C11_2 IO_A12_2 IO_A16_2 IO_B15_2 IO_C14_2 IO_G11_2 IO_B16_2 IO_D15_2 IO_E14_2 IO_C16_2 IO_E14_2 IO_E13_2 IO_E15_2 IO_G13_2 IO_B11_2 IO_D11_2 IO_A11_2 IO_D10_2 IO_B10_2 IO_E12_2 IO_F12_2 IO_B9_2 IO_C9_2 IO_C10_2 IO_A9_2 IO_D9_2 IO_F15_2 IO_G14_2 IO_E16_2 IO_H12_2 IO_F16_2 IO_H16_2 IO_G15_2 IO_H13_2 IO_G16_2 IO_H14_2 IO_H15_2 IO_J12_2

AVAGO_CTRL<47..0>
CLK_CTRL<14..0>

ST5
LED MEZZA.

XC2C256
XC2C256_FG256
xc2c256_FT256

J11
JTAG_USB
3V3
FUSE2
POLYFUSE 300MA
VREF TDI TDO TMS TCK CONNECTOR
R11 A10 N12 P12

1V8_CPLD 3V3
F4
VCCAUX VCC[3..0] VCCIO1[7..0] VCCIO2[6..0] GND[19..0]

IN CPLD_CLK1

CPLD CLOCK

1V8_CPLD
U55
C721 100NF
C728 100NF
GCK_P
GCK_N
IN
IN
GCK_SINGLE
SN65LVDS4

3V3
VCC=3V3
C260
NC
U54
SN74AHC1G14
C710 100NF
R261 51
R259 162
1KHZ
C722
XX

1V8_CPLD
C711 C712 C713 C714 C715 C716 C717 C718 C720 C723 C724
3V3
C725 C726 C727 C729 C730 C731 C732 C733 C734
10N U37

3V3
D6
RS3J_13_F
C709 100NF
U53
mic68400
1V8_CPLD
1.8
C710 10UF

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	22.07.2015	

BLOCK TITLE: CPLD

DRAWING TITLE: JFEX

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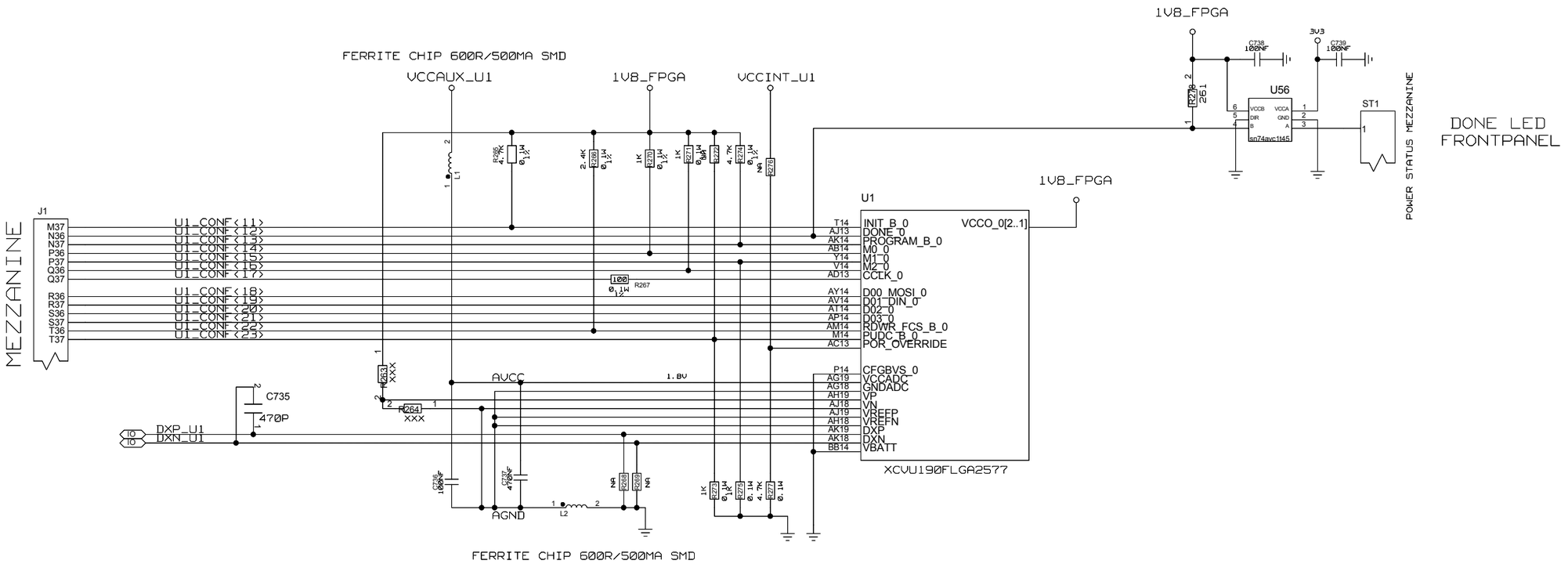
SIZE: C | REV. | DRAWING NO.

SCALE | SHEET 1 OF 1

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	17.09.2015	

BLOCK TITLE: CONFIGURATION PROCESSOR U1

DRAWING TITLE: JFEX

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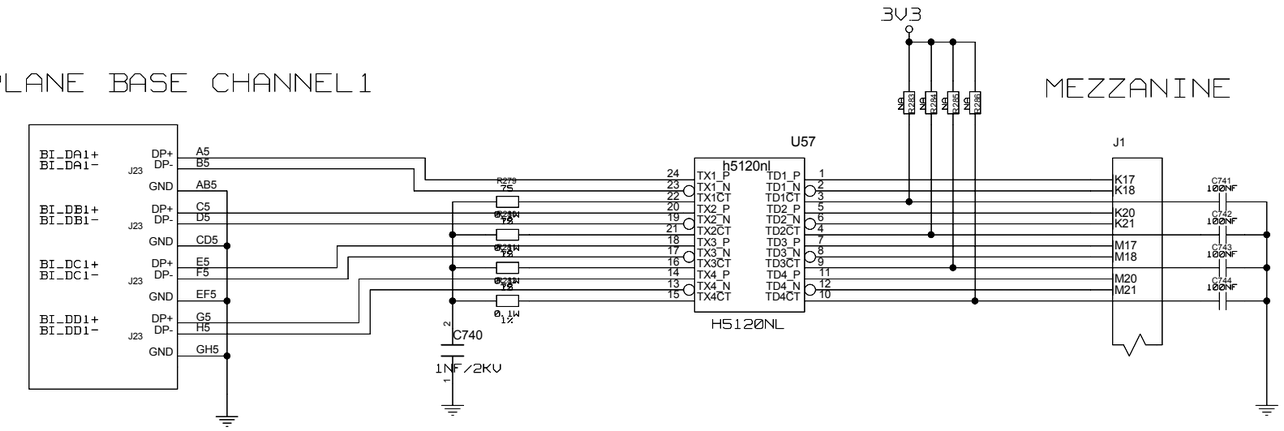
SIZE: C | REV. | DRAWING NO.

SCALE | SHEET 1 OF 1

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

BACKPLANE BASE CHANNEL1

MEZZANINE



	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	05.11.2015	

BLOCK TITLE: IPBUS

DRAWING TITLE: IPBUS

UNIVERSITY OF MAINZ

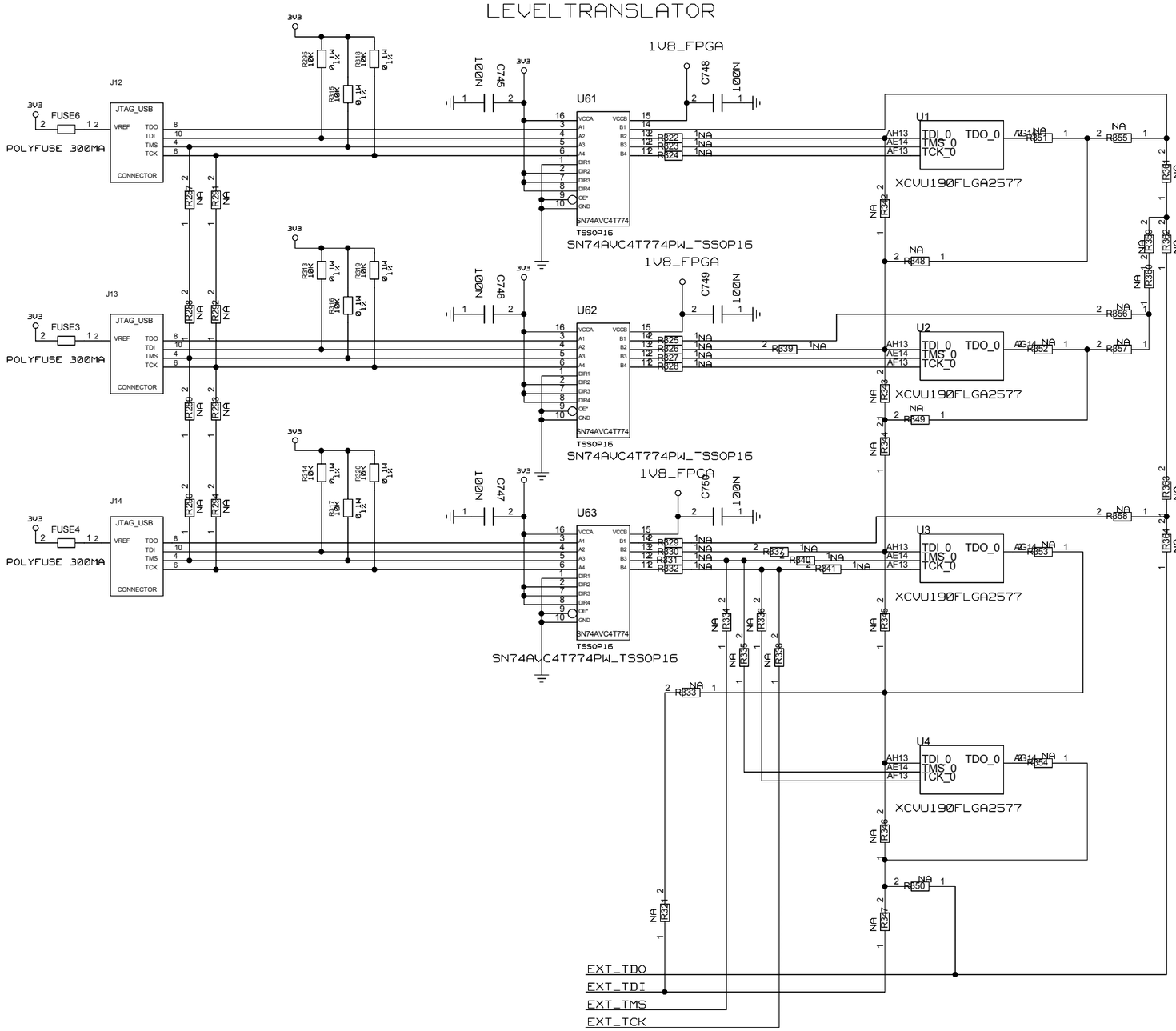
SIZE: C | REV. | DRAWING NO.

SCALE | SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

CLOSE TOGETHER
NEAR FRONTPANEL

LEVELTRANSLATOR



	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	15.07.2015	

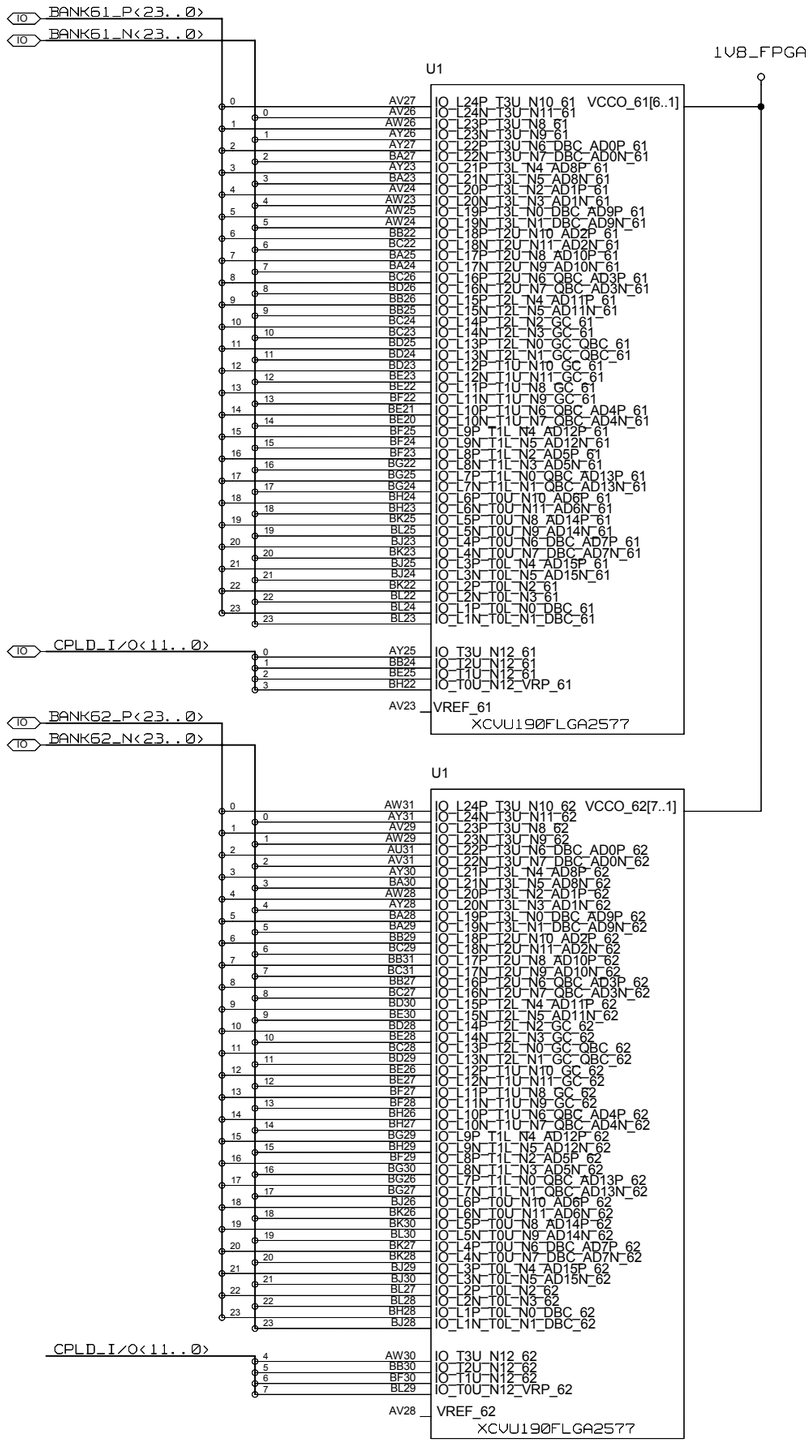
BLOCK TITLE
JTAG

DRAWING TITLE
JTAG CHAIN

UNIVERSITY OF MAINZ

SIZE	REV.	DRAWING NO.
C		
SCALE		SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



BANK
61
62
63

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	25. 08. 2015	

BLOCK TITLE: U1_PARALLEL_I/O

DRAWING TITLE: JFEX

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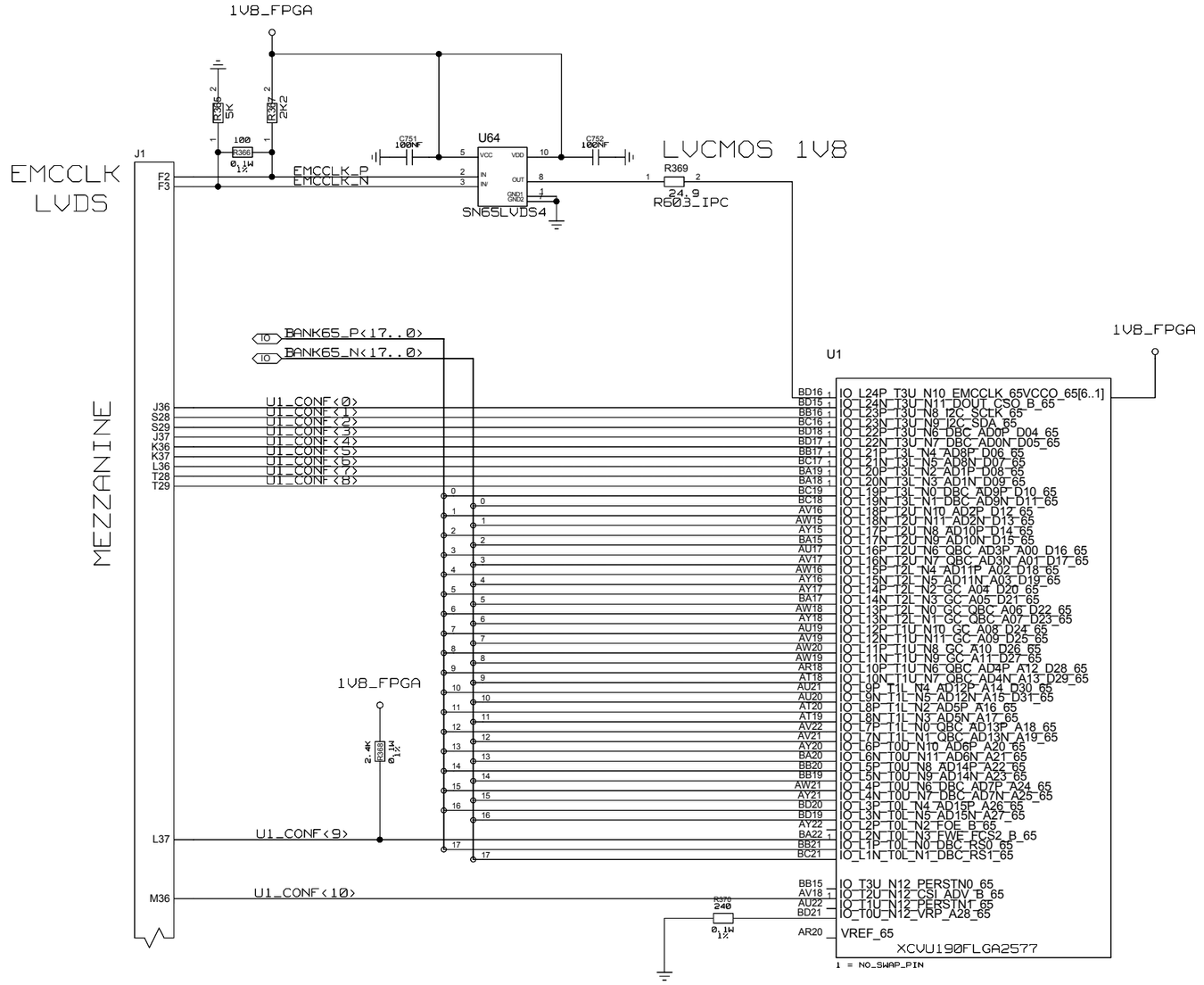
SIZE: C | REV. | DRAWING NO.

SCALE | SHEET 1 OF 5

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



BANK
65

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	25.08.2015	

BLOCK TITLE
U1_PARALLEL_I/O

DRAWING TITLE
JFEX

UNIVERSITY OF MAINZ

SIZE C | REV. | DRAWING NO.

SCALE | SHEET 2 OF 5

REVISIONS				
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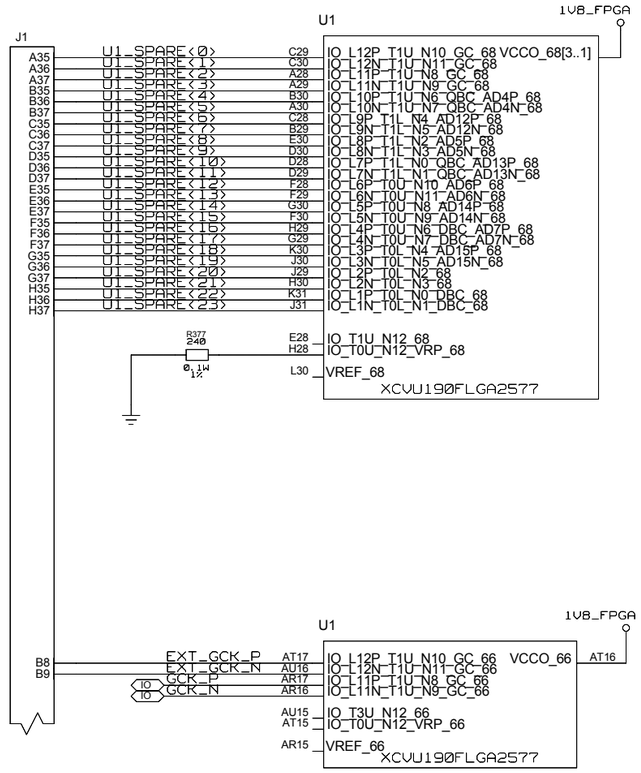


BANK
66
68

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	25.08.2015	

BLOCK TITLE
U1_PARALLEL_I/O

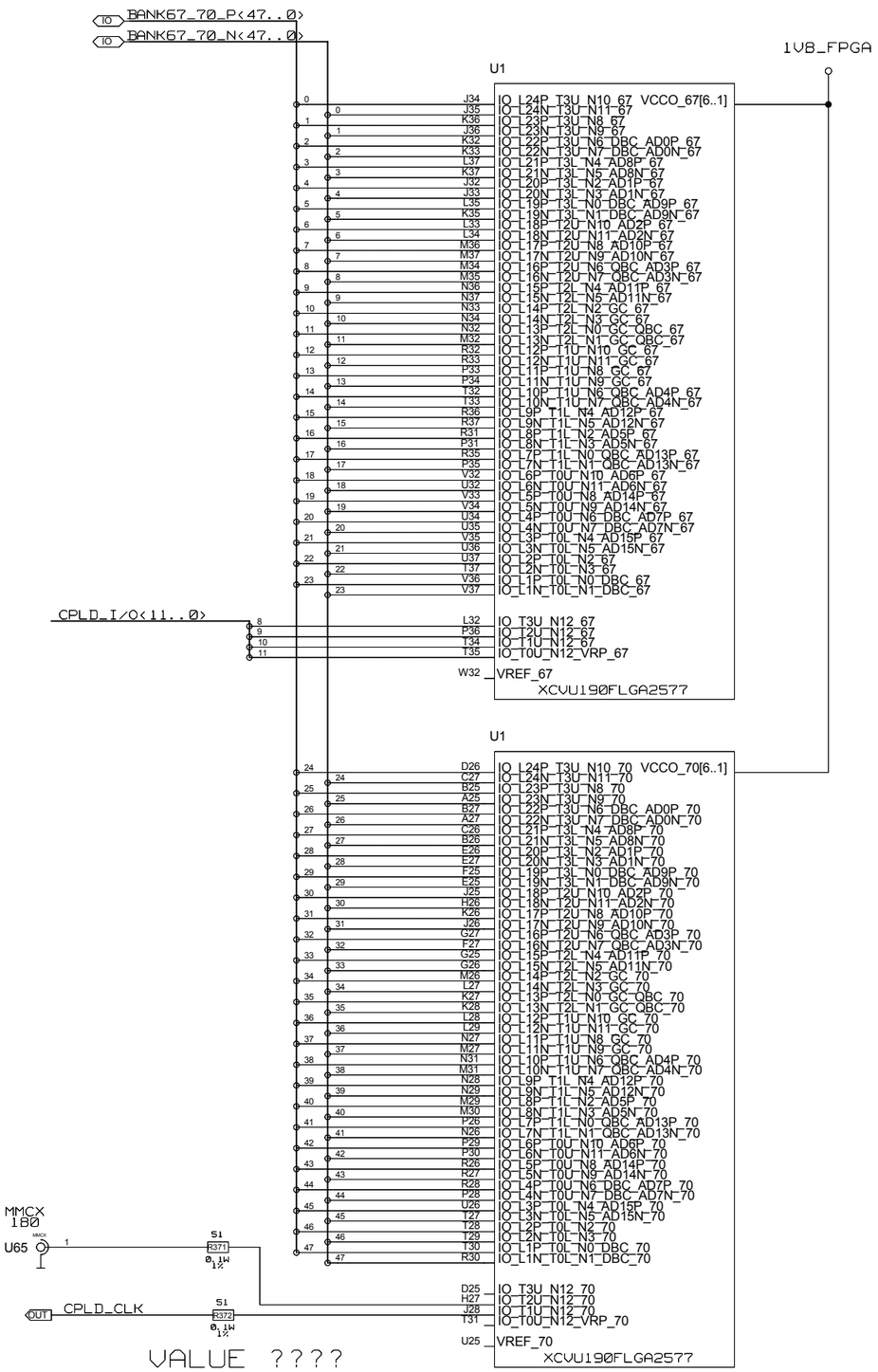
DRAWING TITLE
JFEX

UNIVERSITY OF MAINZ

SIZE C | REV. | DRAWING NO.

SCALE | SHEET 3 OF 5

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



BANK
67
70

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	25.08.2015	

BLOCK TITLE: U1_PARALLEL_I/O

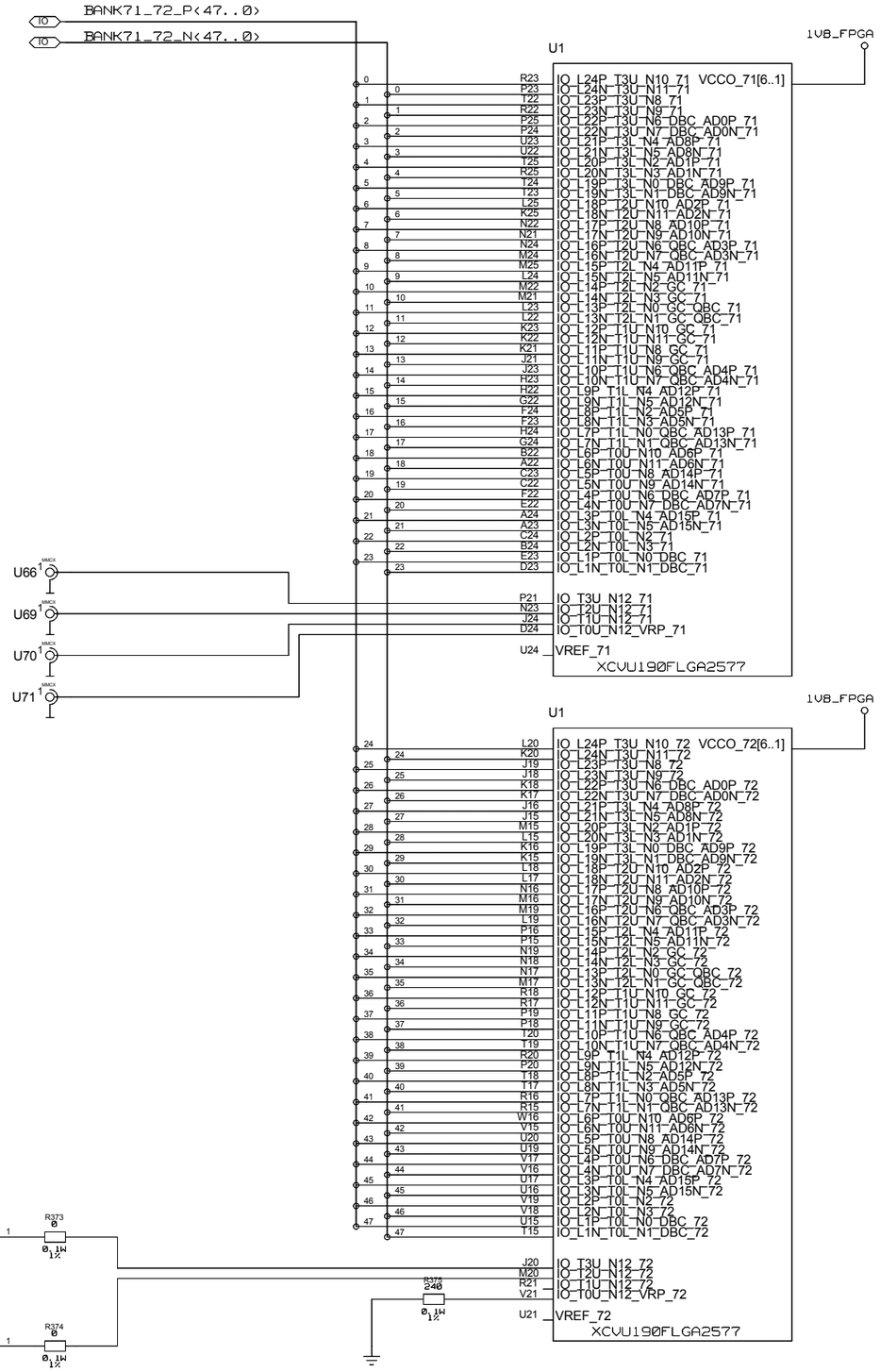
DRAWING TITLE: JFEX

UNIVERSITY OF MAINZ

SIZE: C | REV. | DRAWING NO.

SCALE | SHEET 4 OF 5

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



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	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	25.08.2015	

BLOCK TITLE: U1_PARALLEL_I/O

DRAWING TITLE: JFEX

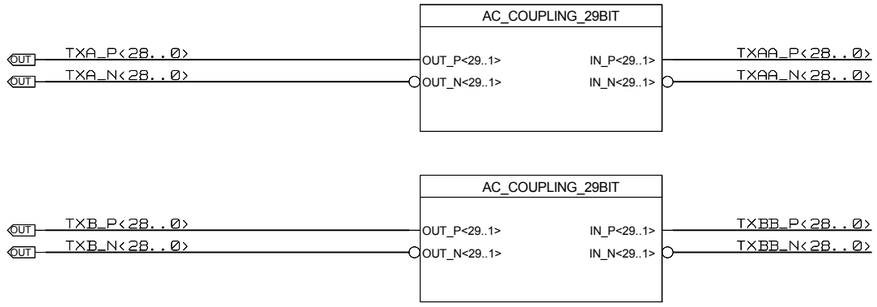
UNIVERSITY OF MAINZ

SIZE: C | REV. | DRAWING NO.

SCALE | SHEET 5 OF 5

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

AC COUPLING



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CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	07.08.2015	

BLOCK TITLE
U1_MGT_REAL_TIME

UNIVERSITY OF MAINZ

DRAWING TITLE
I/O + AC COUPLING

SIZE C | REV. | DRAWING NO.

SCALE | SHEET 1 OF 7

REVISIONS				
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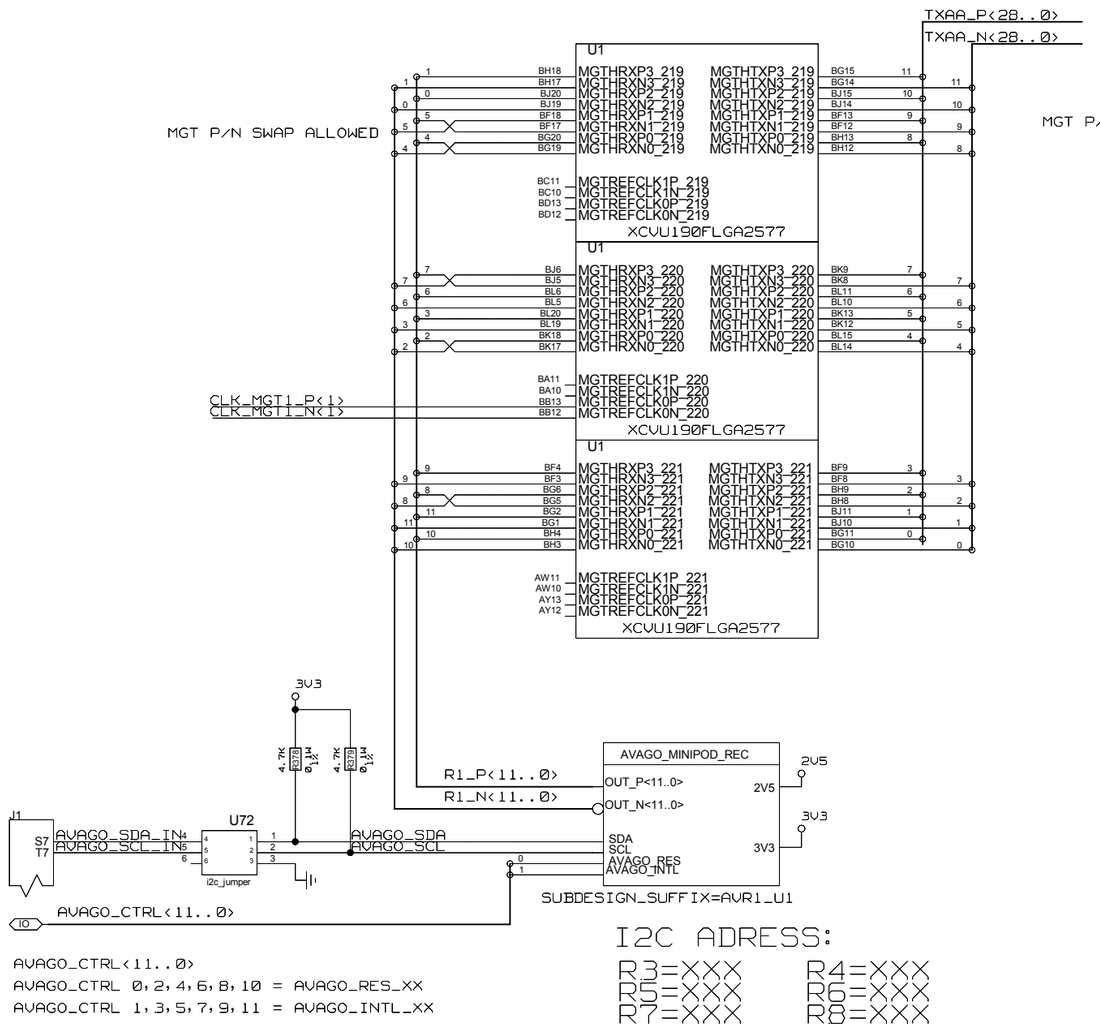

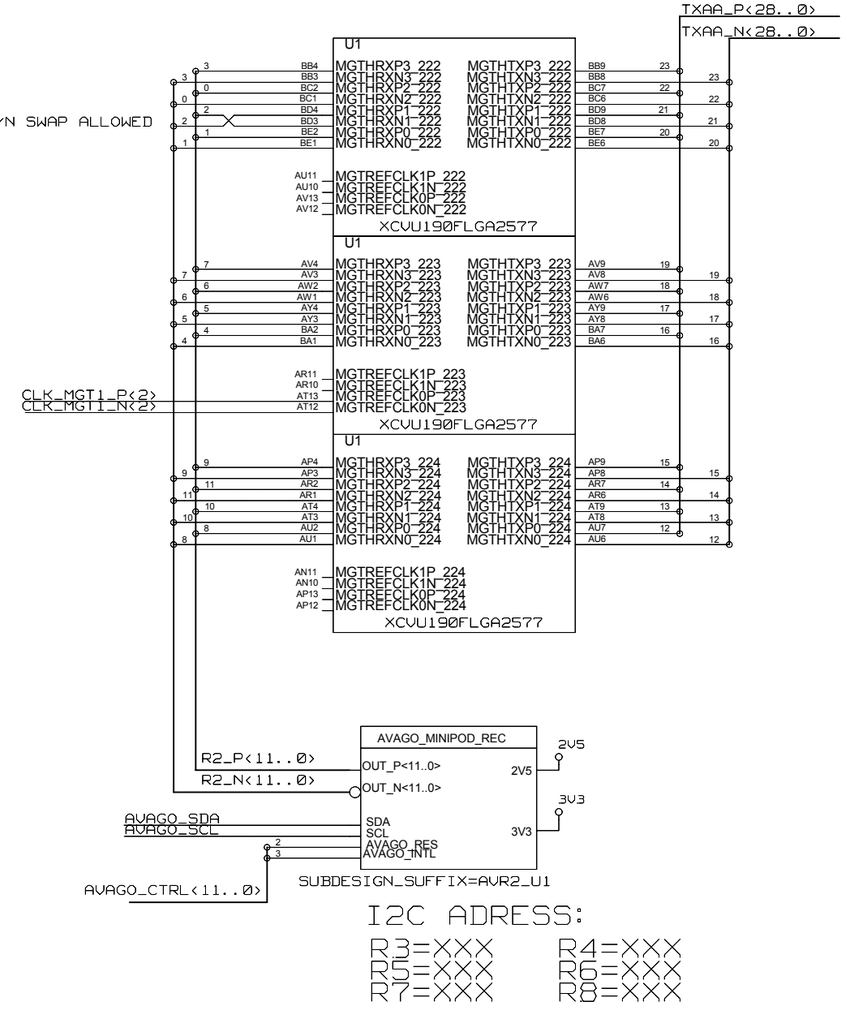

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	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	07.08.2015	

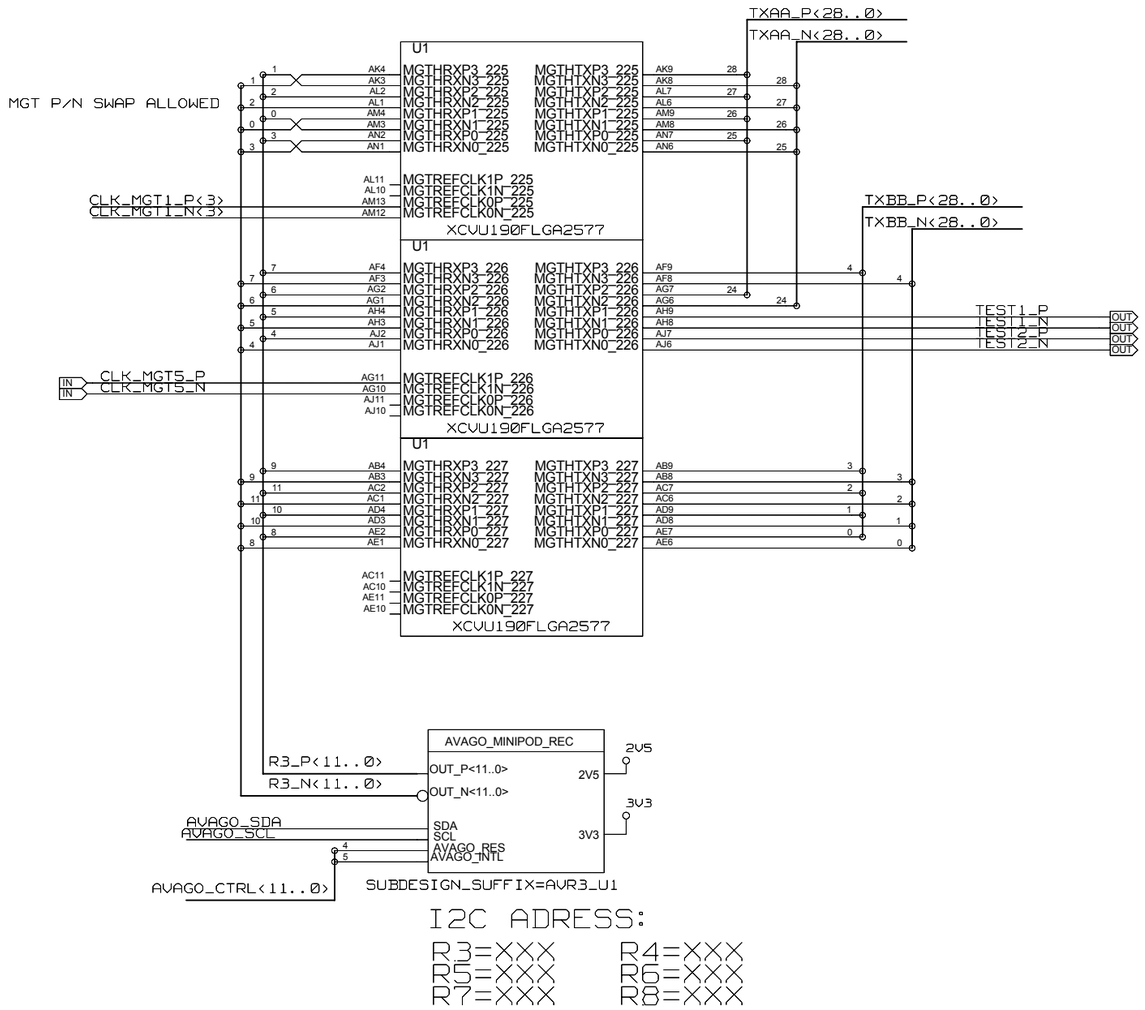
BLOCK TITLE: U1_MGT_REAL_TIME

DRAWING TITLE: AVAGO TXA

UNIVERSITY OF MAINZ

SIZE	REV.	DRAWING NO.
C		
SCALE		SHEET 2 OF 7

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



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CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	07.08.2015	

BLOCK TITLE: U1_MGT_REAL_TIME

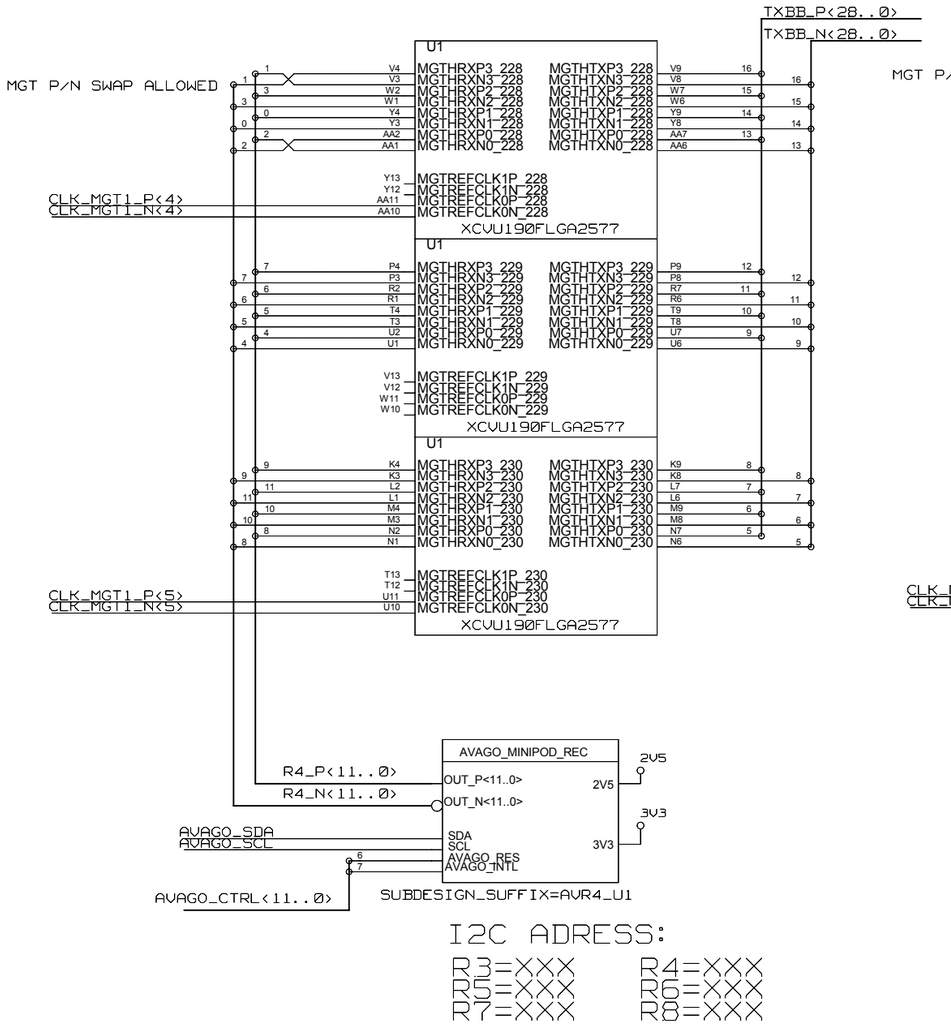
DRAWING TITLE: AVAGO TXA + TXB

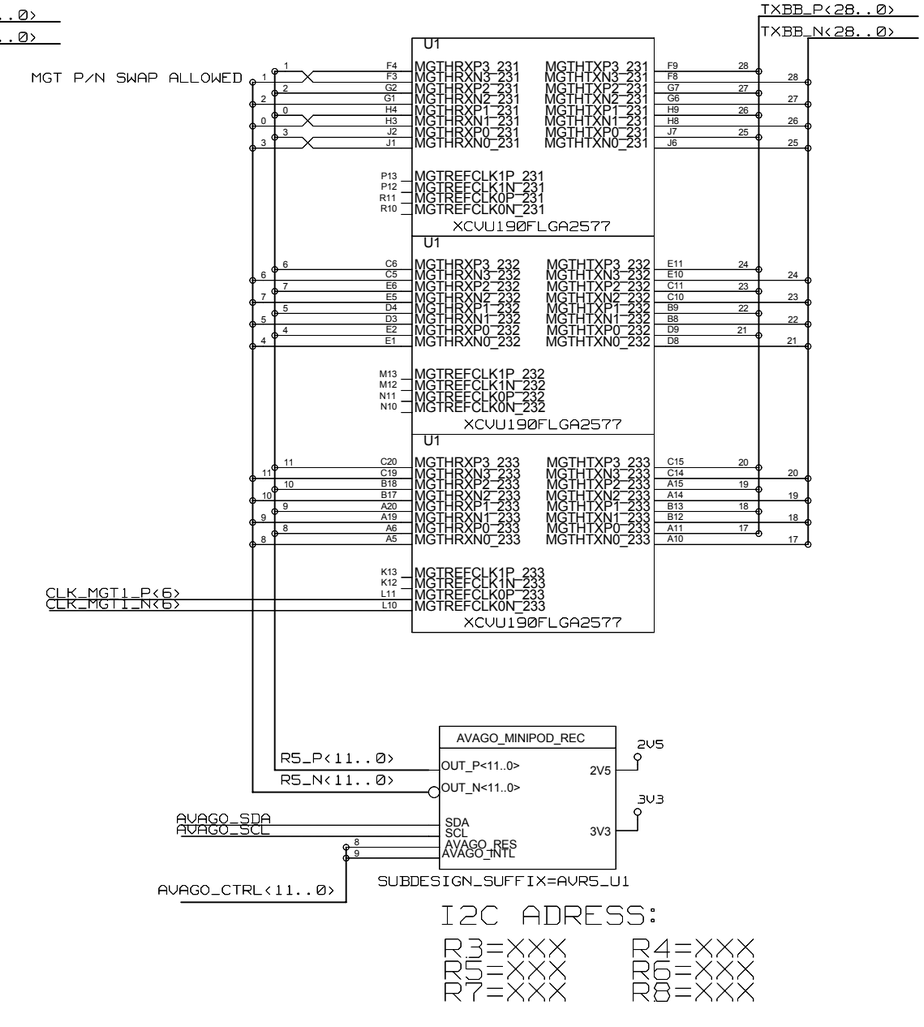
UNIVERSITY OF MAINZ

SIZE: C | REV. | DRAWING NO.

SCALE | SHEET 3 OF 7

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.





I2C ADRESS:

R3=XXX R4=XXX
R5=XXX R6=XXX
R7=XXX R8=XXX

BANK

228 229 230 231 232 233

	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	07.08.2015	

BLOCK TITLE: U1_MGT_REAL_TIME

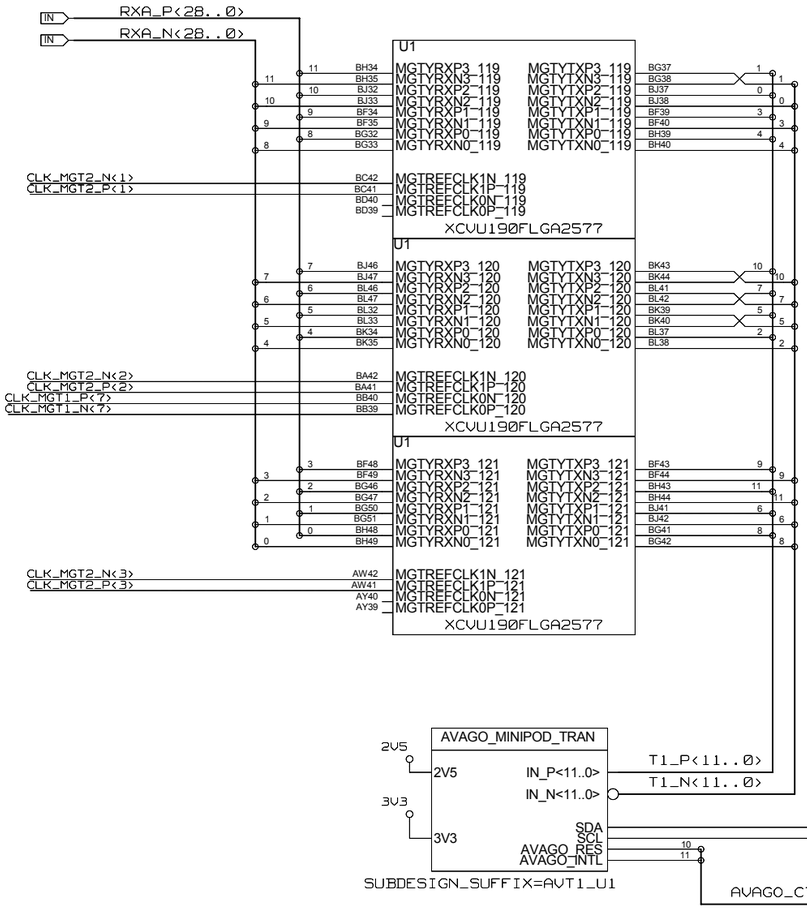
DRAWING TITLE: AVAGO TXB

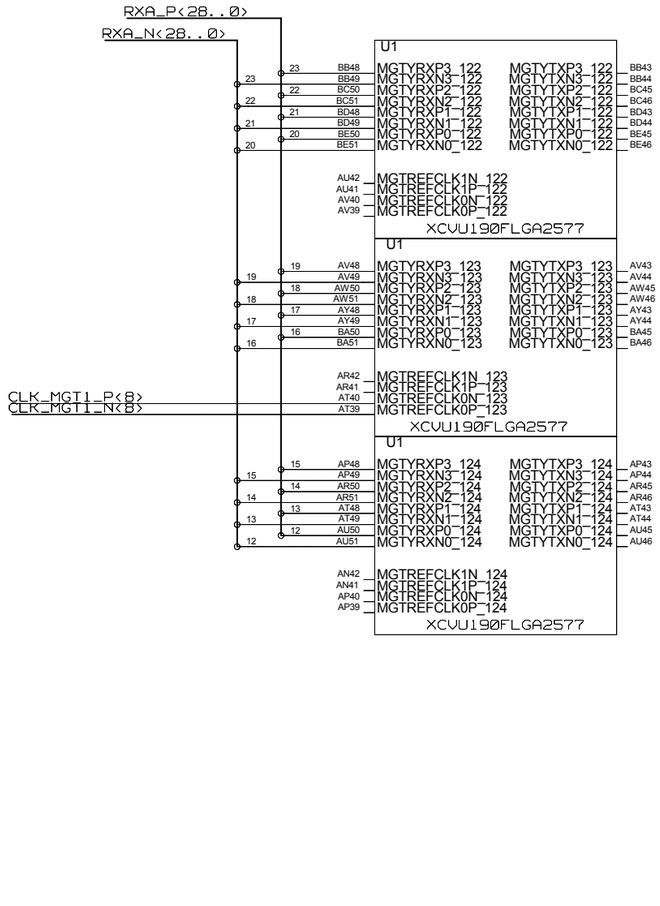
UNIVERSITY OF MAINZ

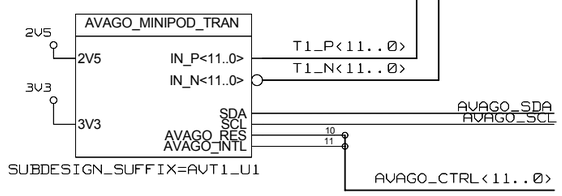
SIZE C | REV. | DRAWING NO.

SCALE | SHEET 4 OF 7

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.







I2C ADRESS:

R3=XXX R4=XXX
R5=XXX R6=XXX
R7=XXX R8=XXX

BANK
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	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	07.08.2015	

BLOCK TITLE: U1_MGT_REAL_TIME

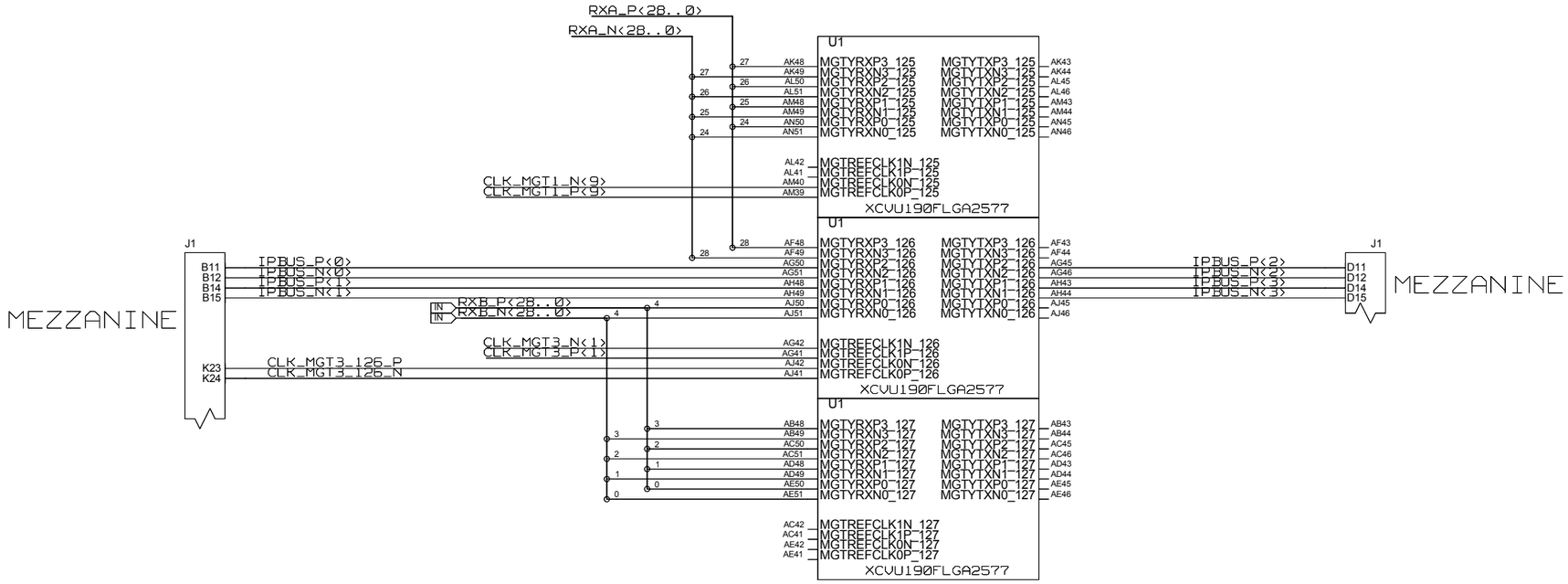
DRAWING TITLE: AVAGO RXA

UNIVERSITY OF MAINZ

SIZE: C | REV. | DRAWING NO.

SCALE | SHEET 5 OF 7

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



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CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	07.08.2015	

BLOCK TITLE: U1_MGT_REAL_TIME

DRAWING TITLE: RXA + RXB TXD + IPBUS

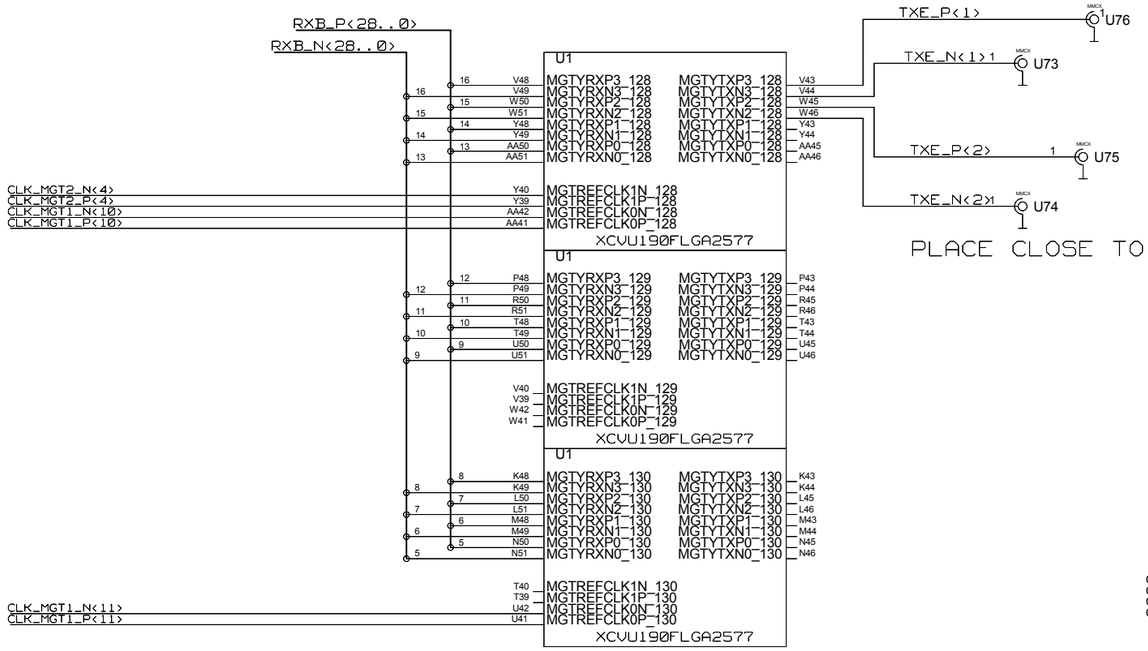
UNIVERSITY OF MAINZ

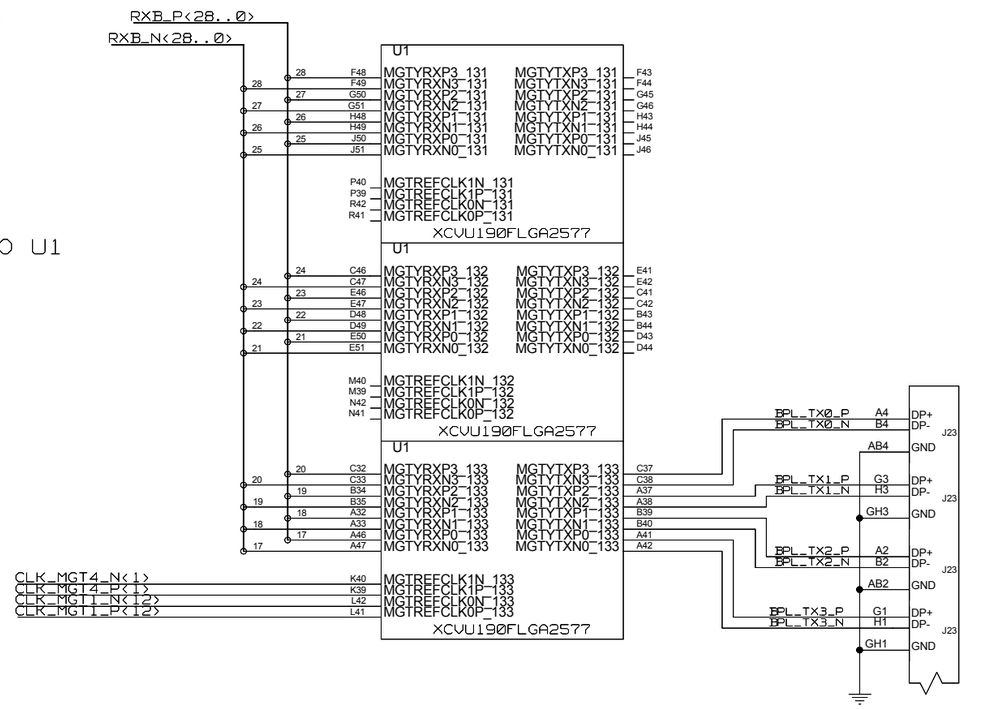
SIZE	REV.	DRAWING NO.
C		
SCALE		SHEET 6 OF 7

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

PLACE CLOSE TO U2

PLACE CLOSE TO U1





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	NAME	DATE	SIGN
CHECKED	E.ROCCO		
CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS	07.08.2015	

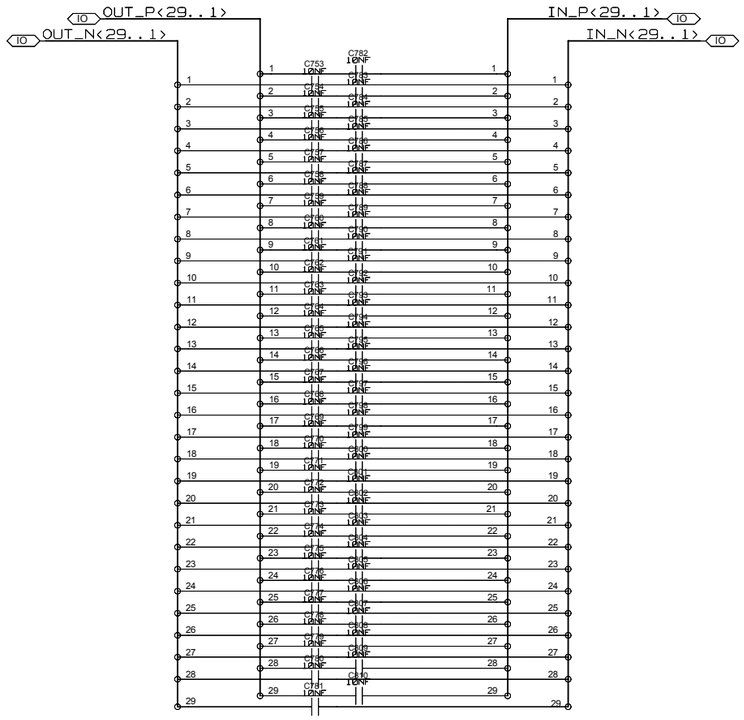
BLOCK TITLE: U1_MGT_REAL_TIME

DRAWING TITLE: RXB + TXC

UNIVERSITY OF MAINZ

SIZE	REV.	DRAWING NO.
C		
SCALE		SHEET 7 OF 7

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



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CHECKED	U.SCHAEFER		
CHECKED	S.RAVE		
DRAWN	BAUSS		

BLOCK TITLE

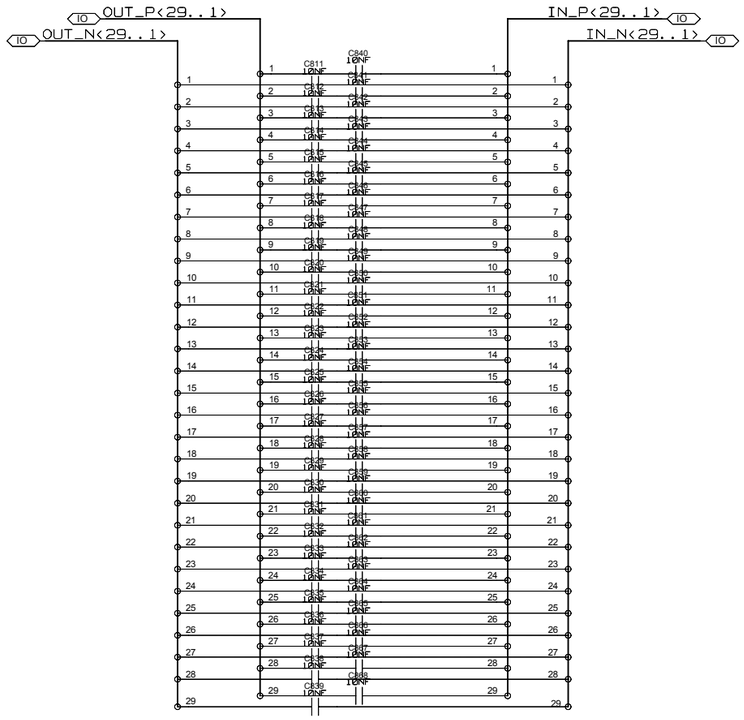
DRAWING TITLE

UNIVERSITY OF MAINZ

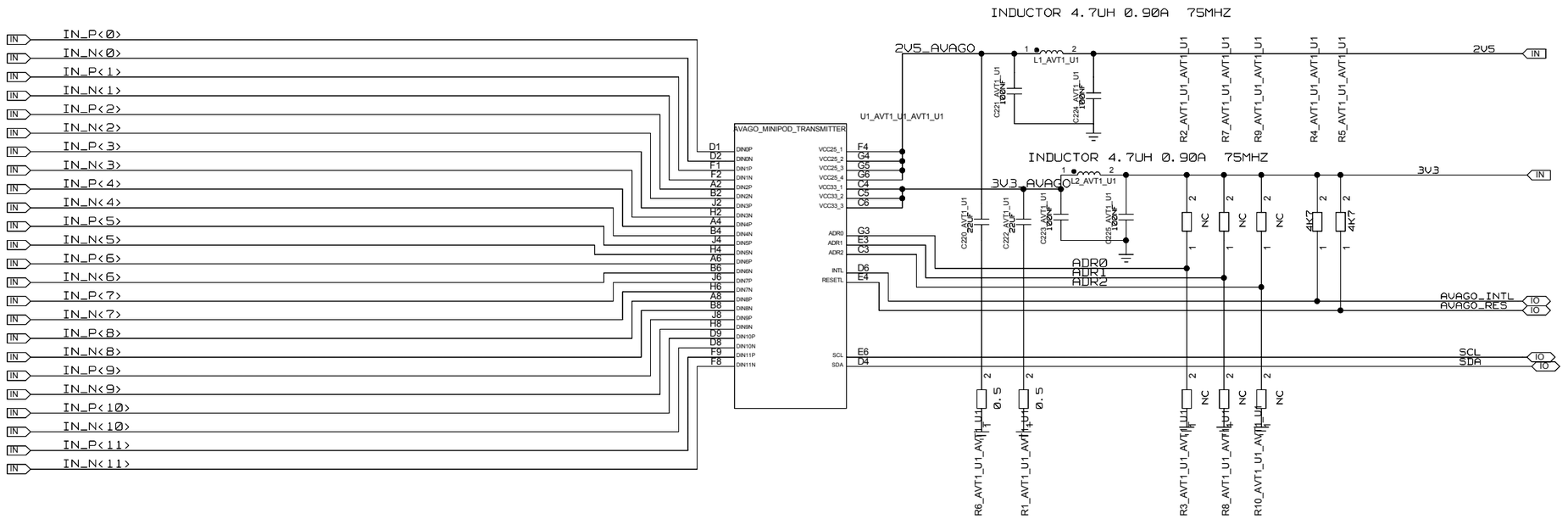
SIZE C | REV. | DRAWING NO.

SCALE | SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



UNIVERSITY OF MAINZ	DRAWING TITLE: AVAGO TRANSMITTER		
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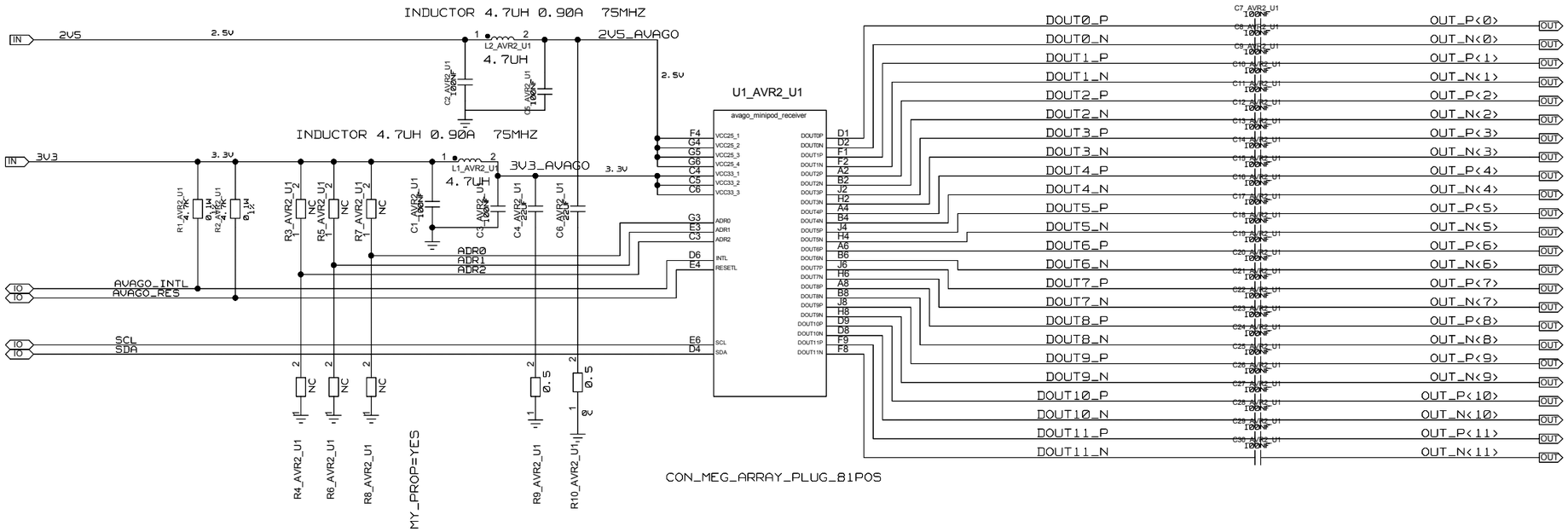
	SCALE		SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



UNIVERSITY OF MAINZ	DRAWING TITLE: AVAGO RECEIVER		
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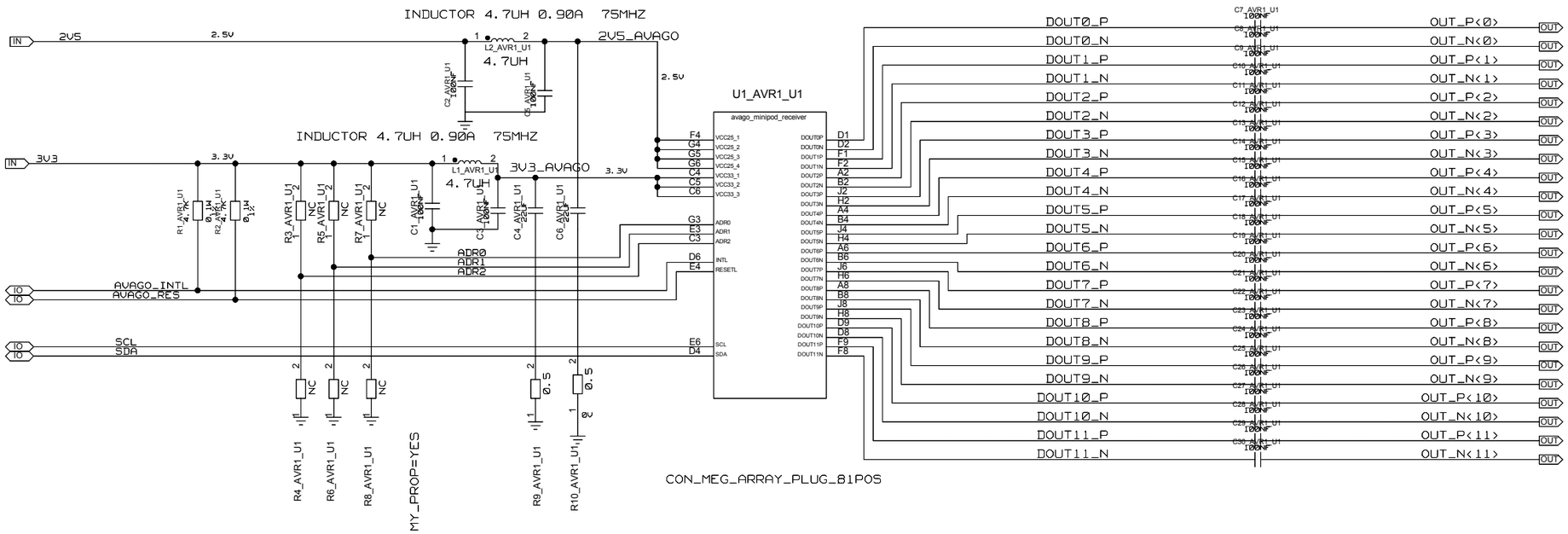
	SCALE		SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



UNIVERSITY OF MAINZ	DRAWING TITLE: AVAGO RECEIVER		
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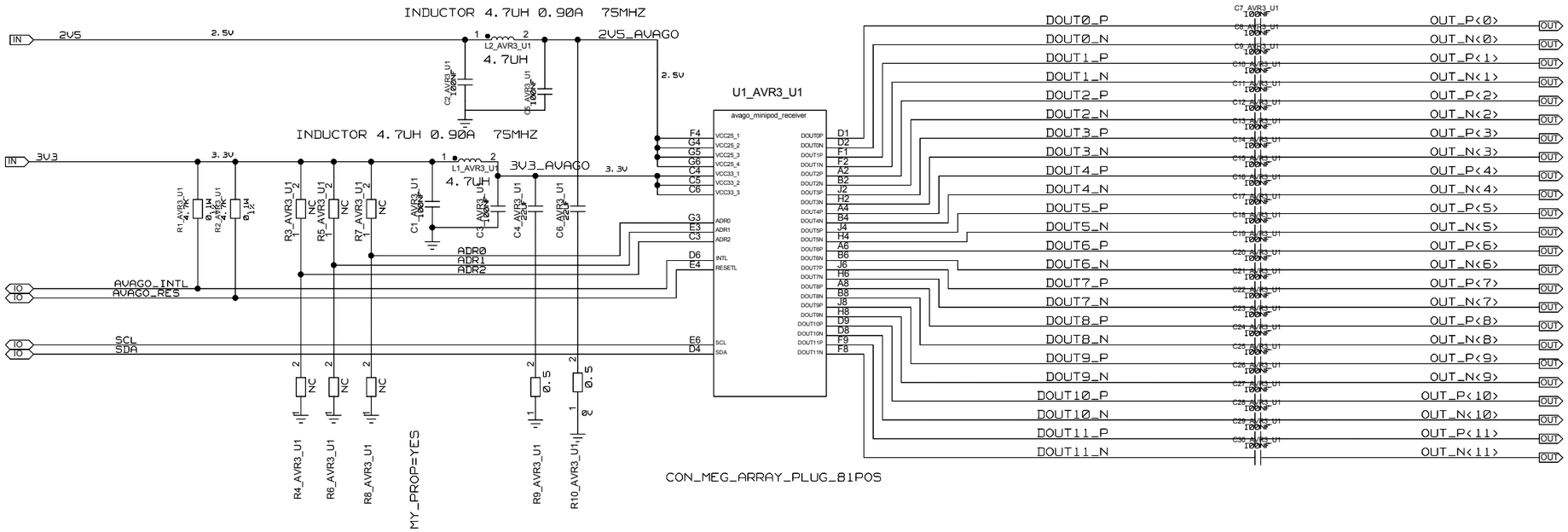
	SCALE		SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



UNIVERSITY OF MAINZ	DRAWING TITLE: AVAGO RECEIVER		
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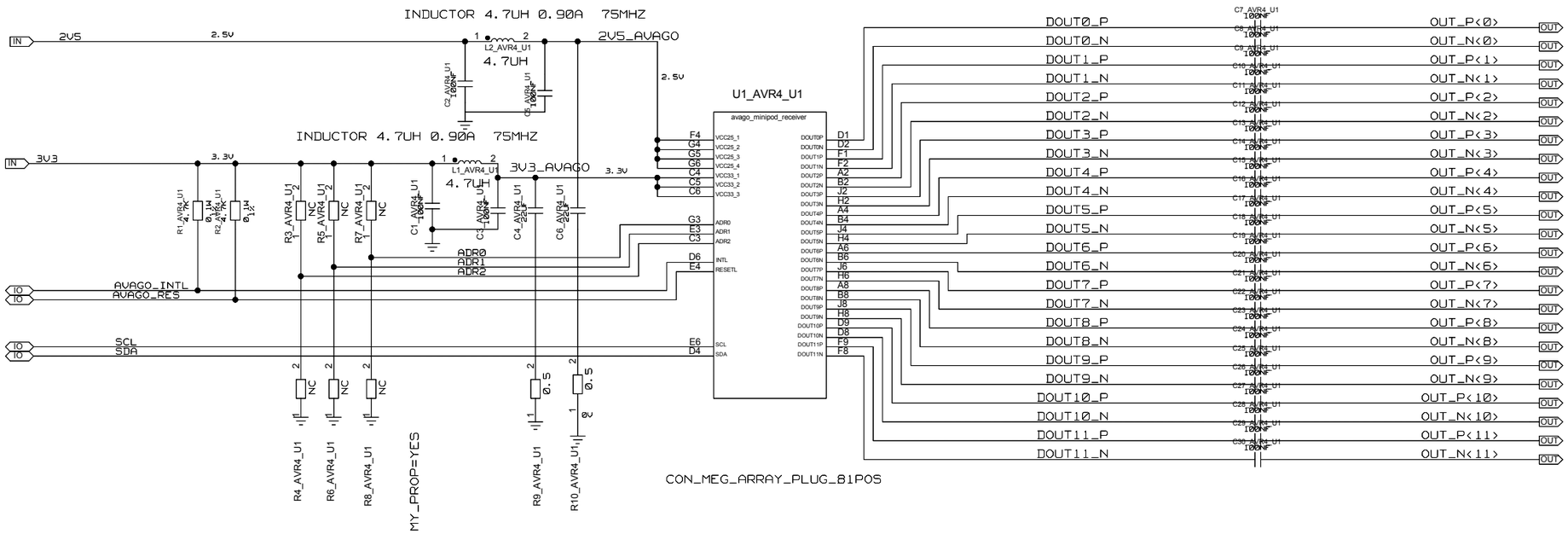
	SCALE		SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



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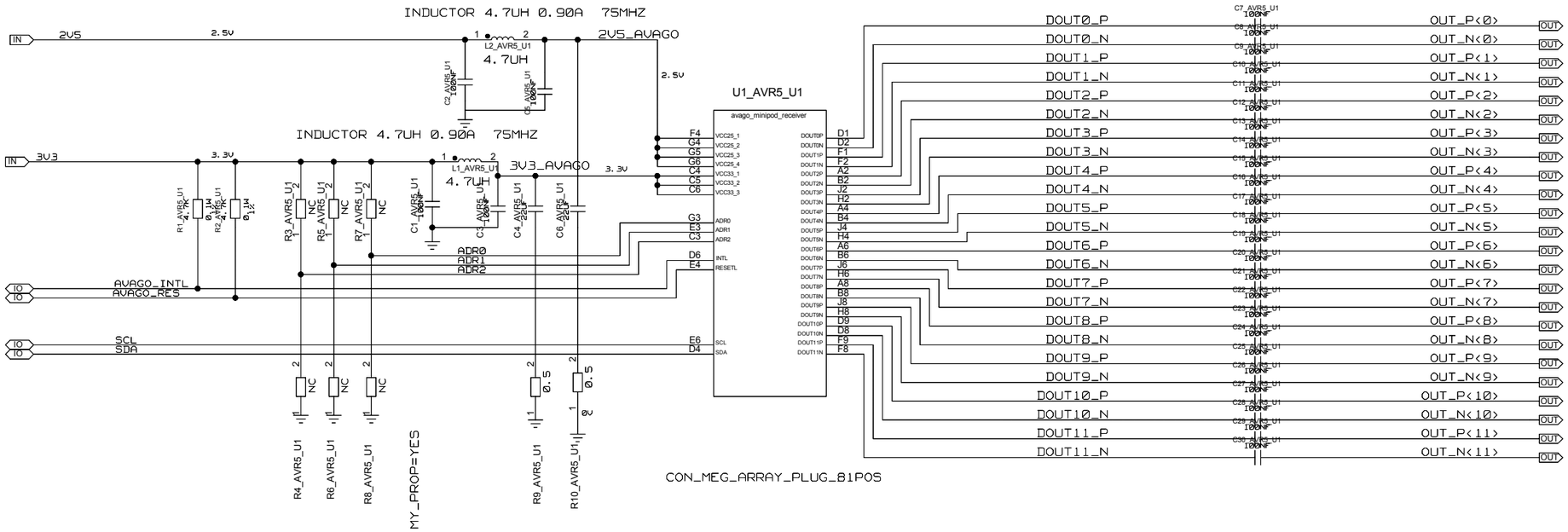
	SCALE		SHEET OF

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



UNIVERSITY OF MAINZ	DRAWING TITLE: AVAGO RECEIVER		
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	SCALE		SHEET OF