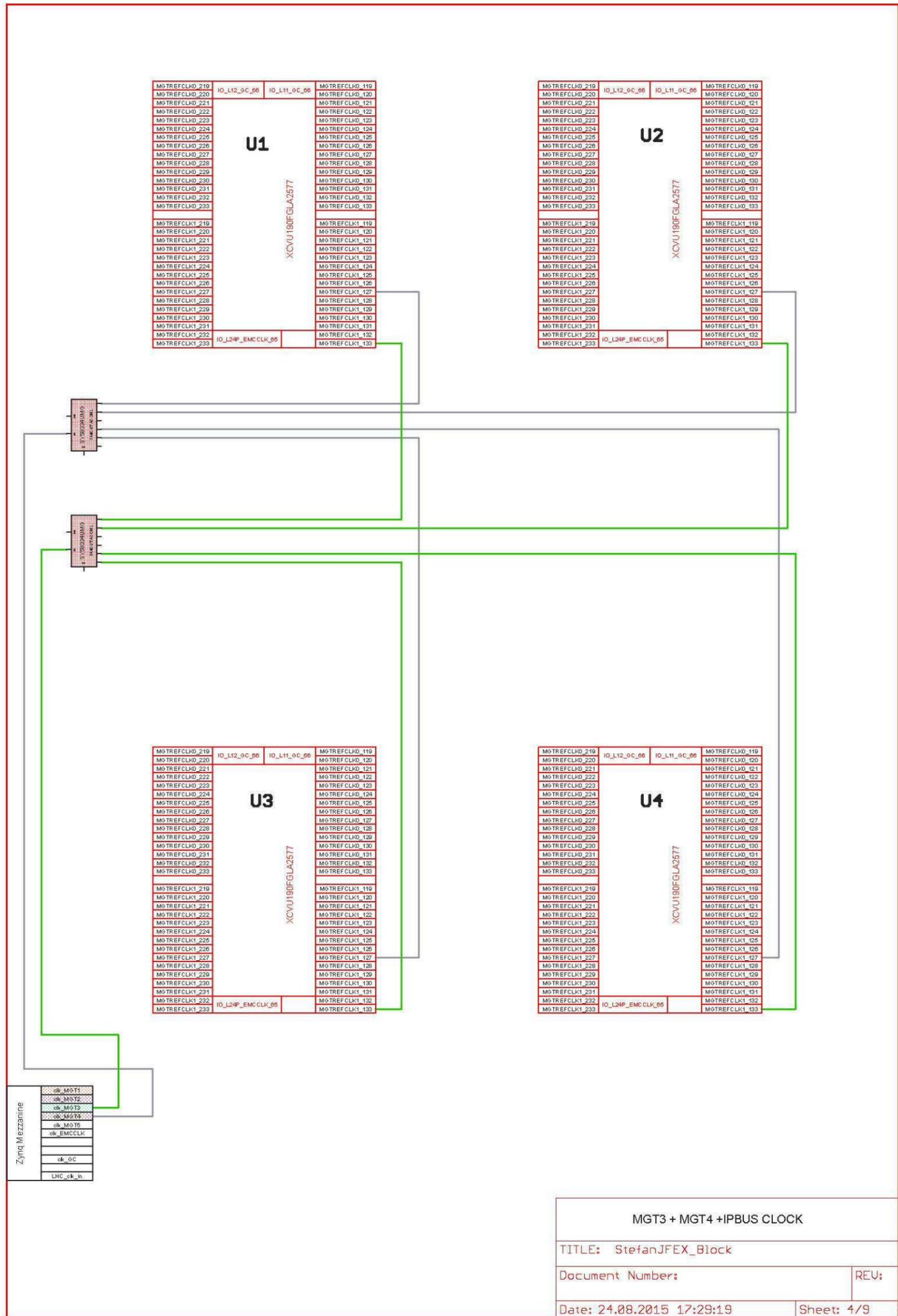
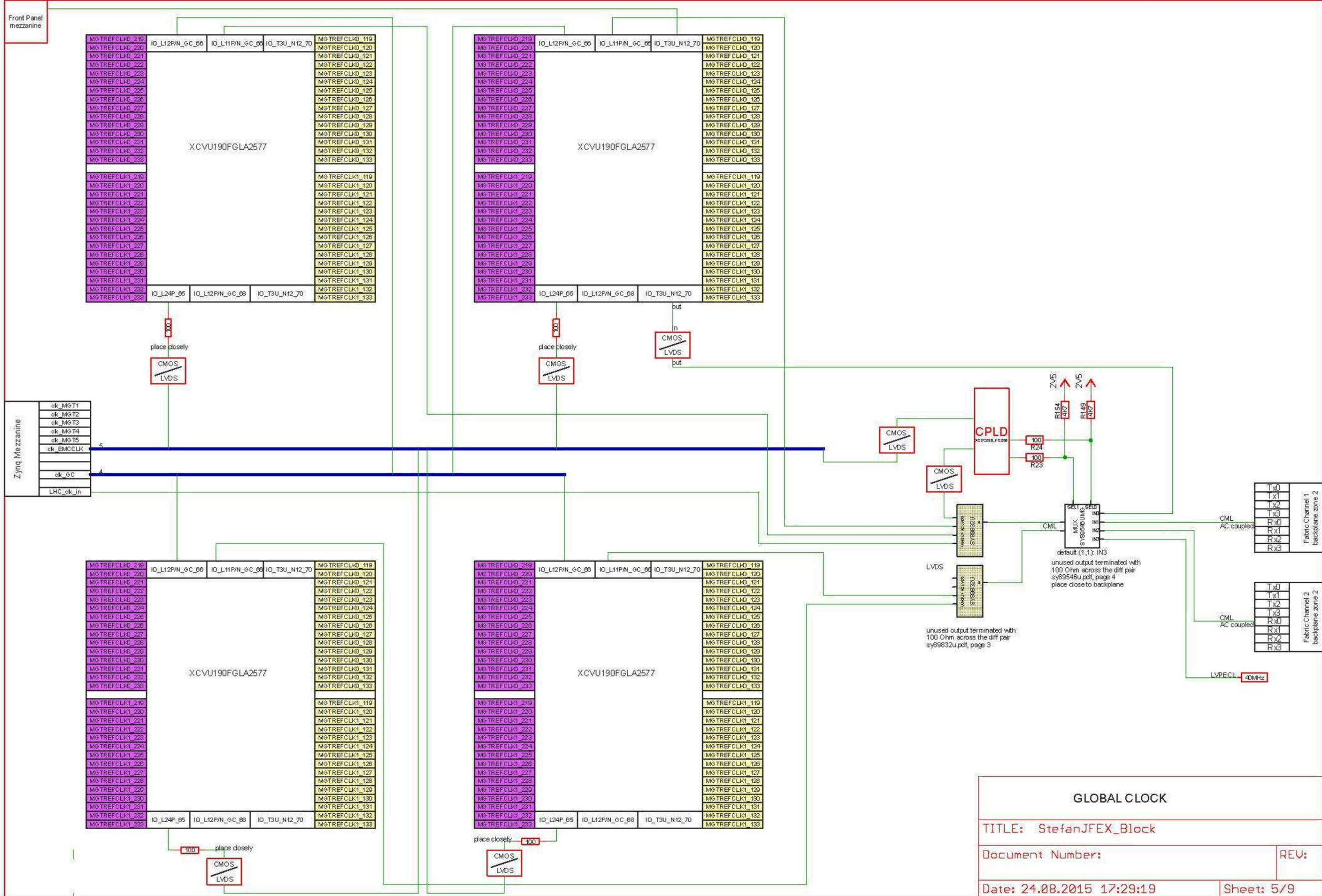


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Date: 24.08.2015 17:29:19	Sheet: 3/9

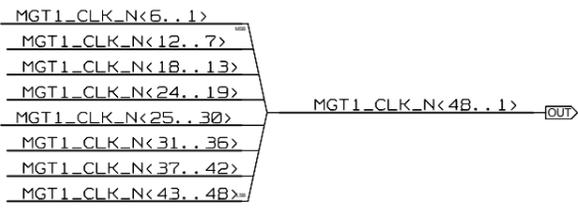
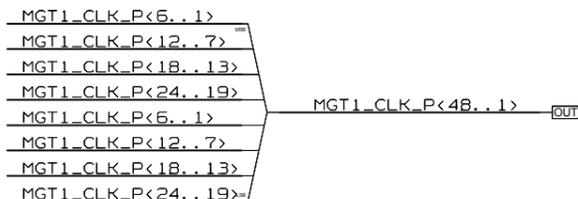
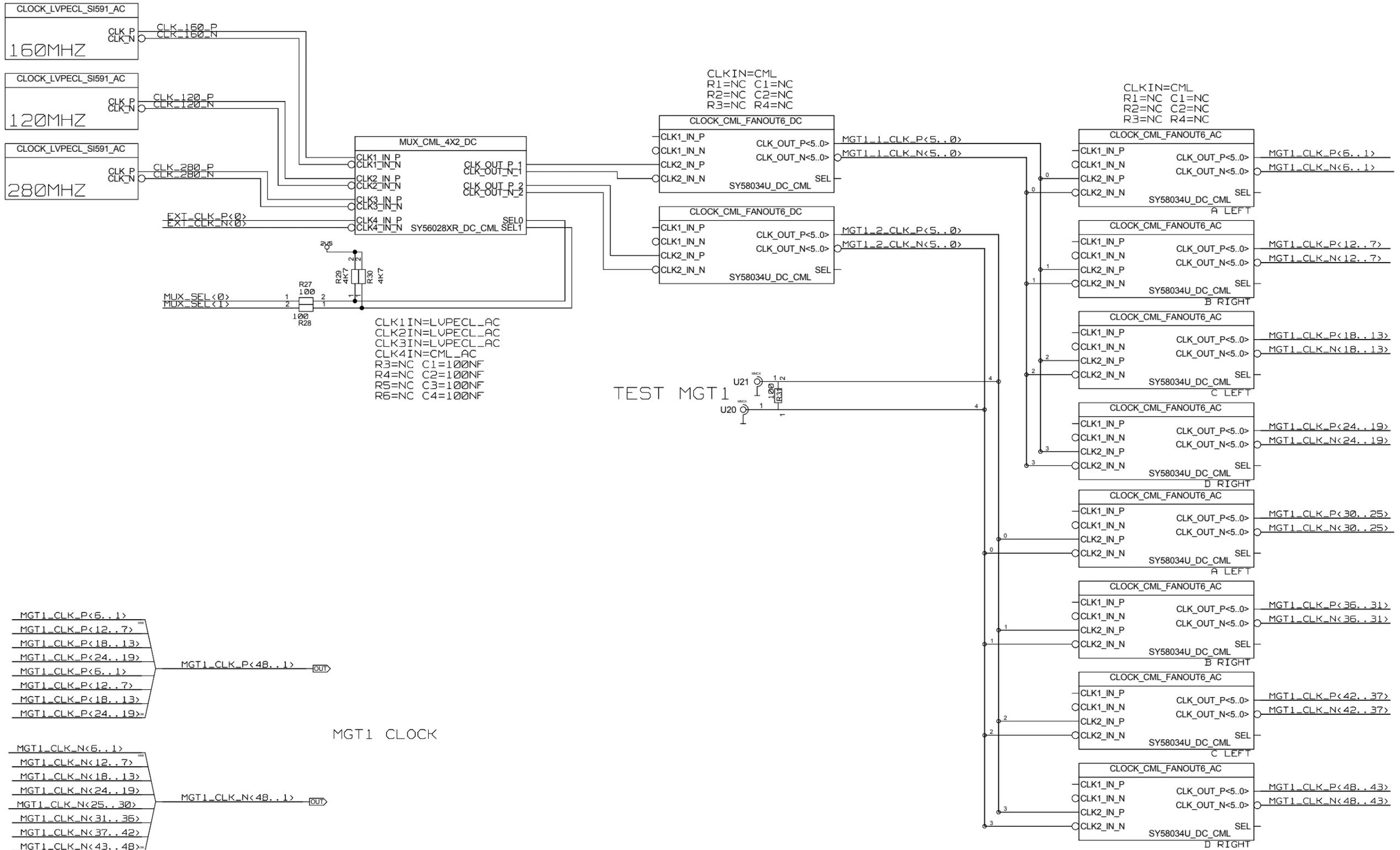




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MGT1 CLOCK

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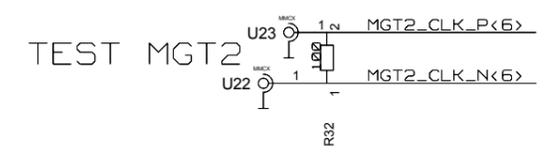
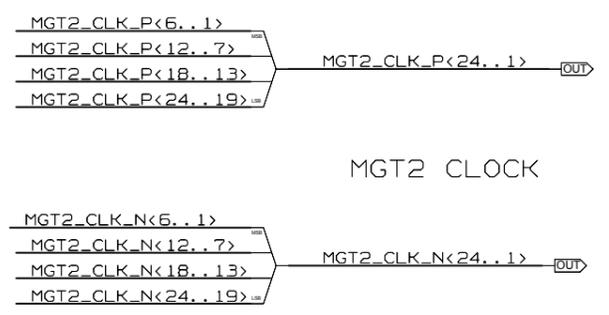
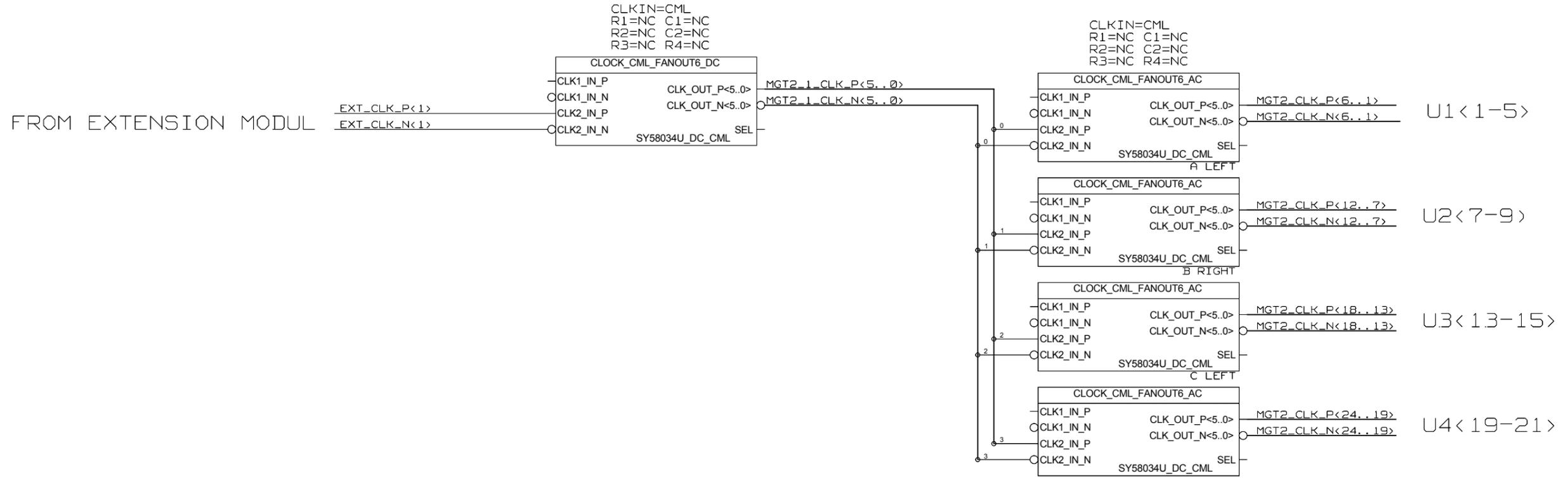


UPDATE PICK & PLACE DATA FOR FANOUT AND MUX

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CHECKED	S.RAVE			
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UNIVERSITY OF MAINZ				REV.
				DRAWING NO.
				SHEET 1 of 5

MGT2 CLOCK

REVISIONS				
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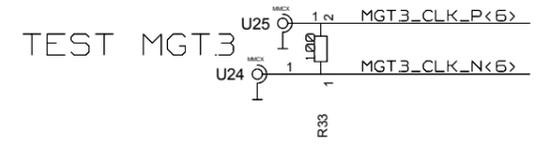
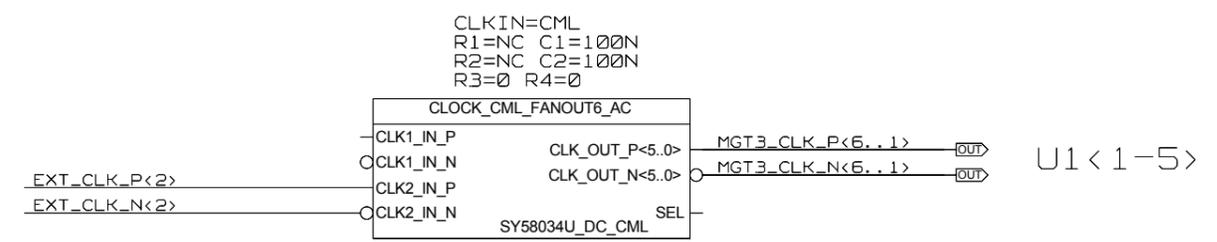


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MGT.3 CLOCK

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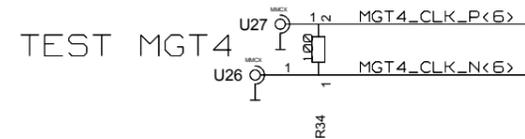
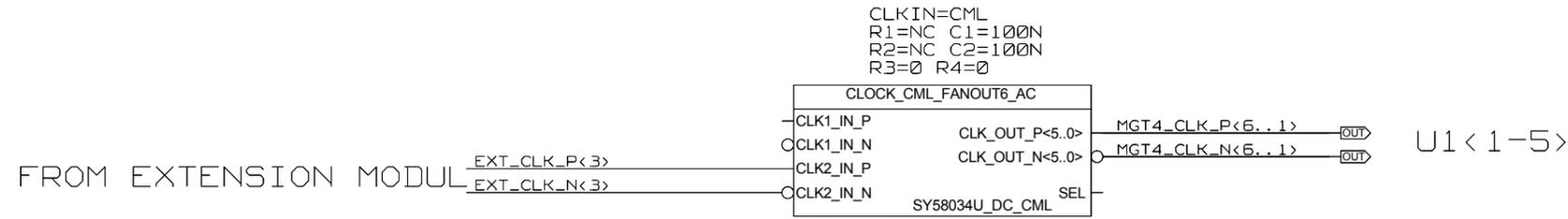


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MGT4 CLOCK

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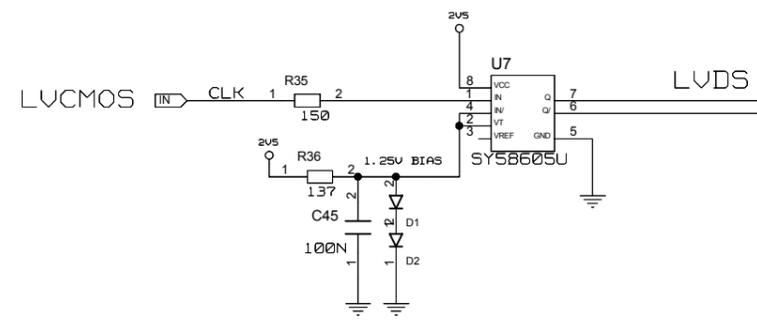


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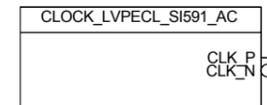
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REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

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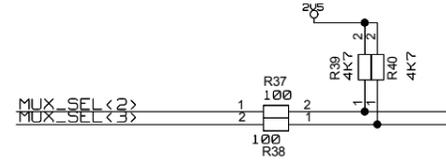


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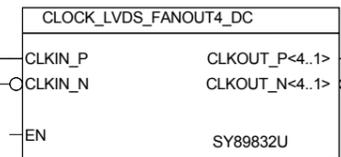
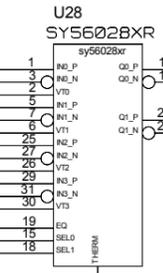


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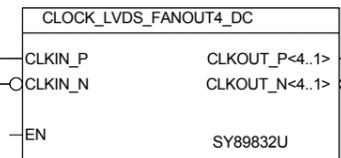
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FAB1_CLK_N
FAB2_CLK_P
FAB2_CLK_N



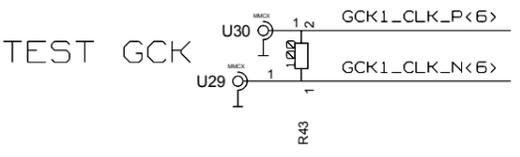
MUX



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R2=NC C6=NC

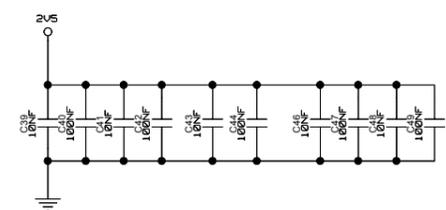
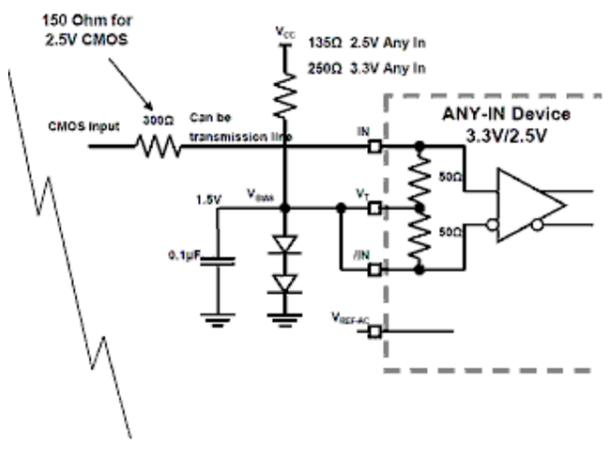


CLKIN=CML
R1=NC C5=NC
R2=NC C6=NC



LVDS OUT GCK1_CLK_P<B..1>
OUT GCK1_CLK_N<B..1>

CMOS to Any-In



CHECKED	NAME	DATE	SIGN
	E.ROCCO		
	U.SCHAEFER		
	S.RAVE		
	BAUSS	17.08.2015	

DRAWING TITLE		
JFEX GLOBAL CLOCK		
SIZE	REV.	DRAWING NO.
C		
SCALE		

CLOCK
UNIVERSITY OF MAINZ

SHEET 5 OF 5