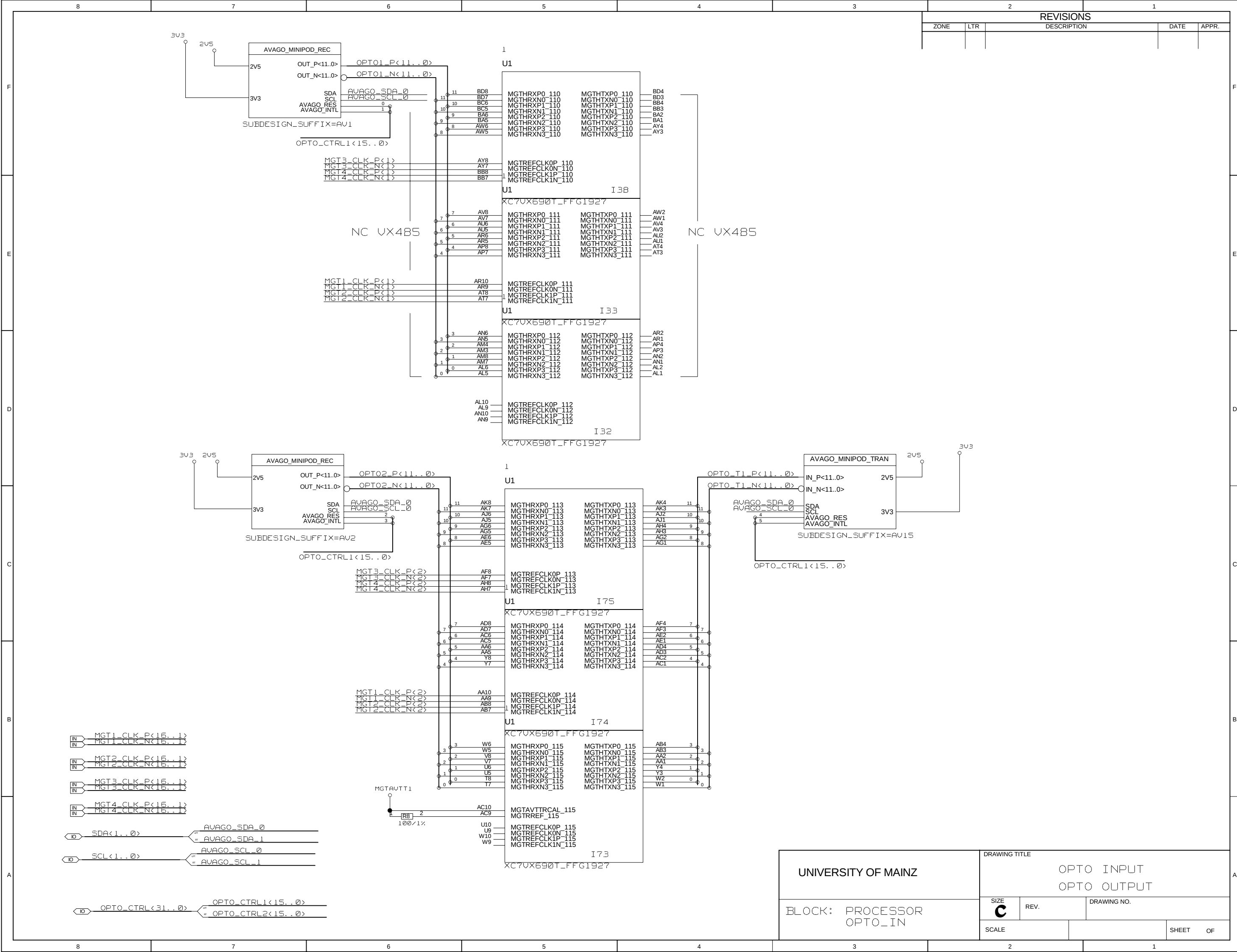
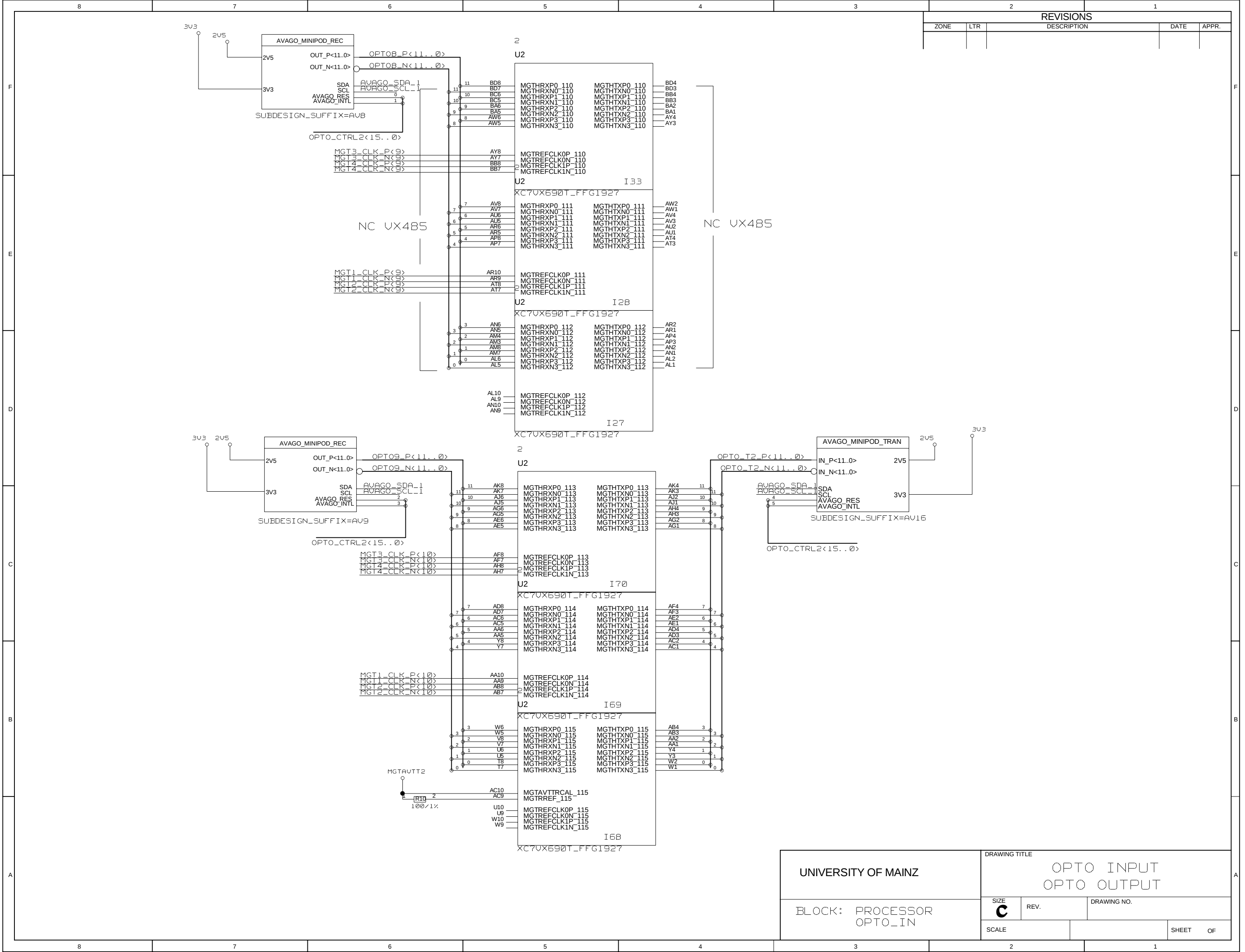
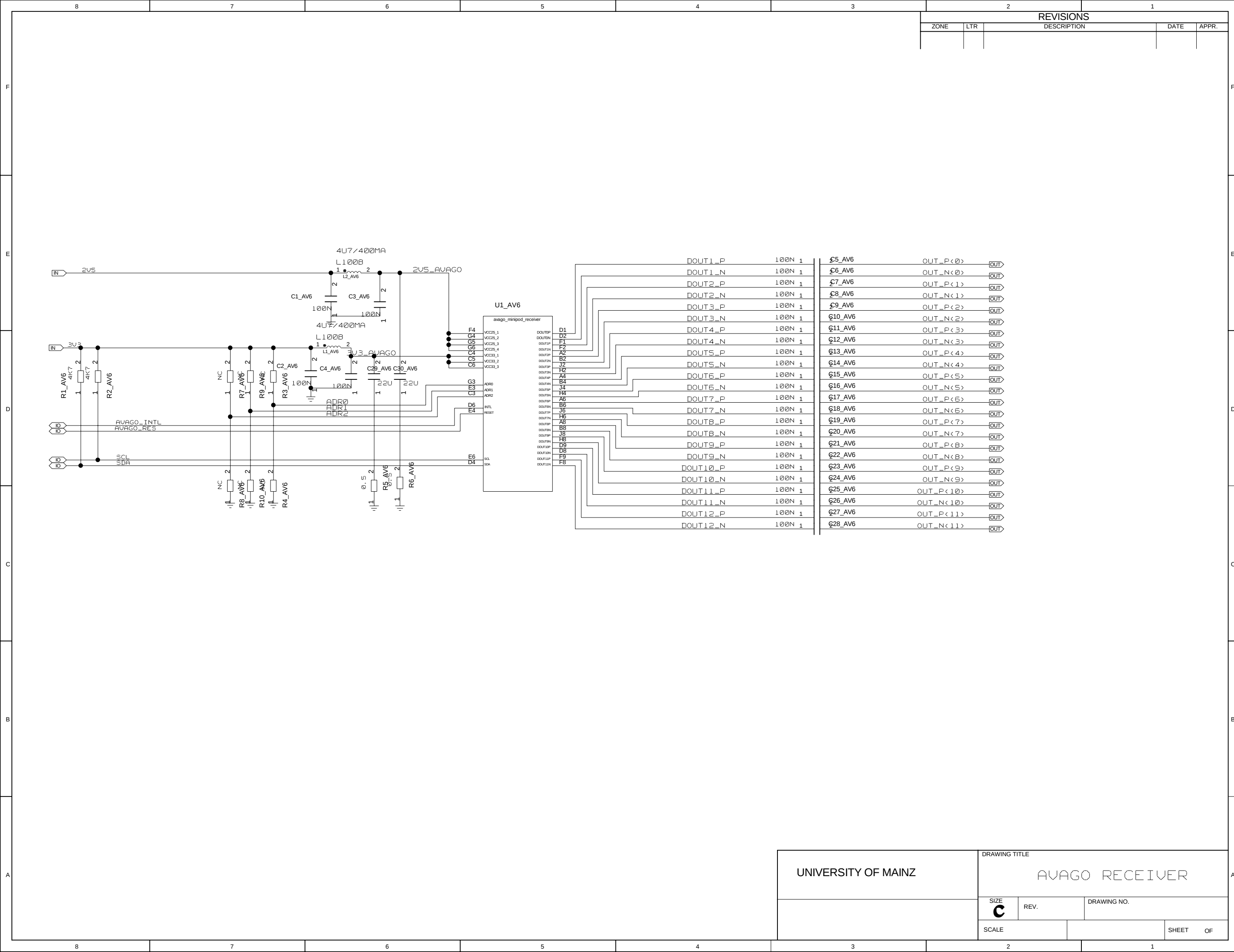
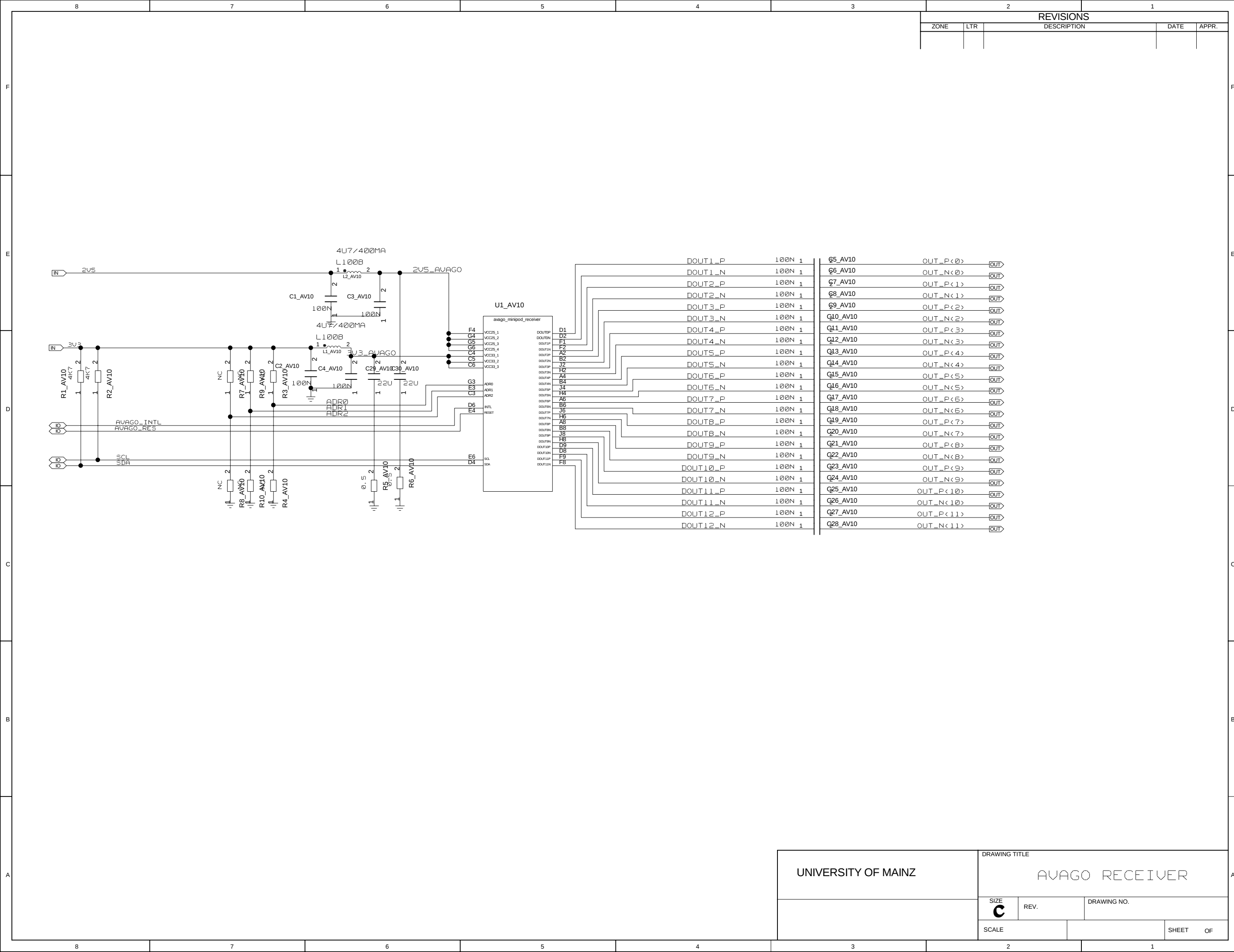
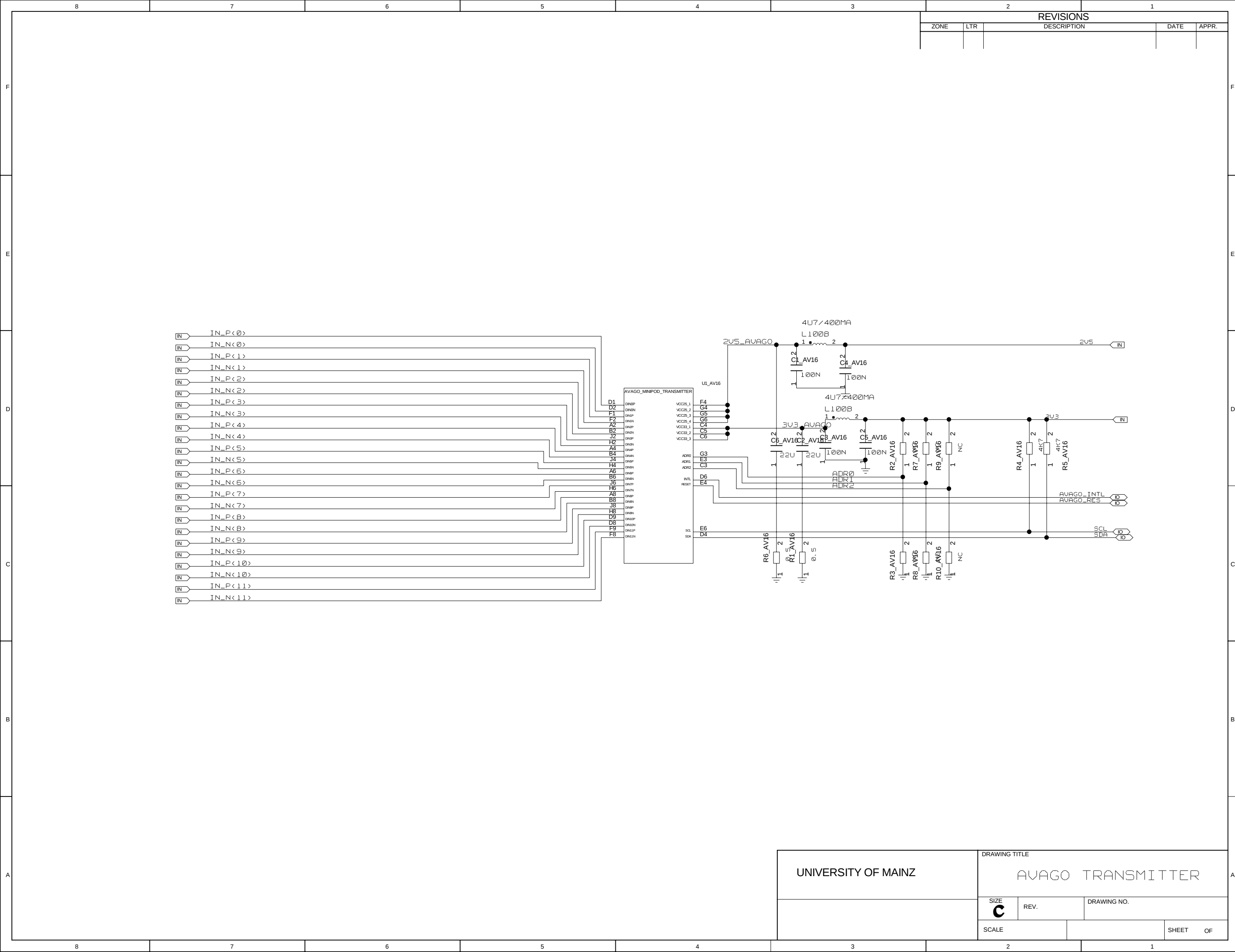


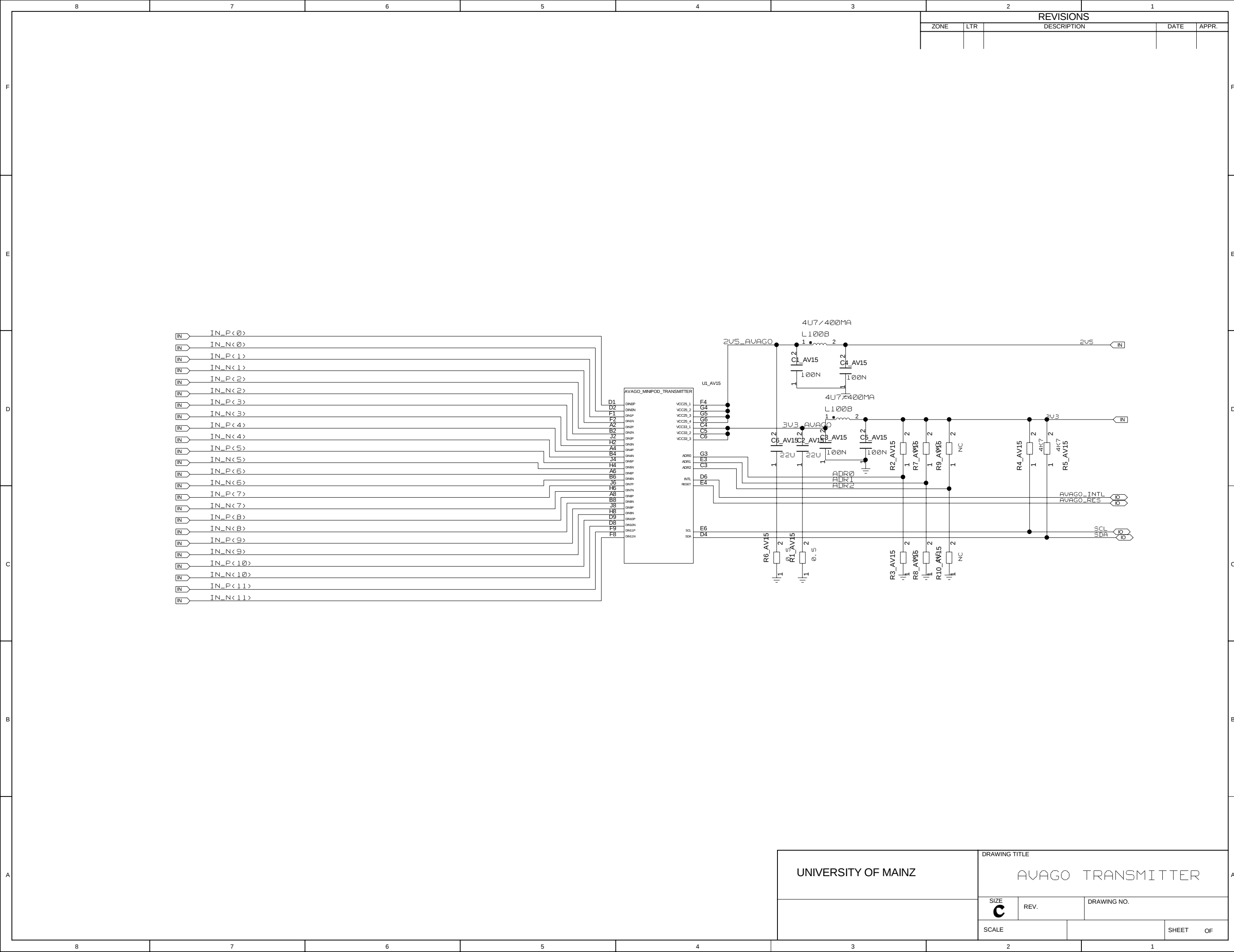












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DRAWING TITLE

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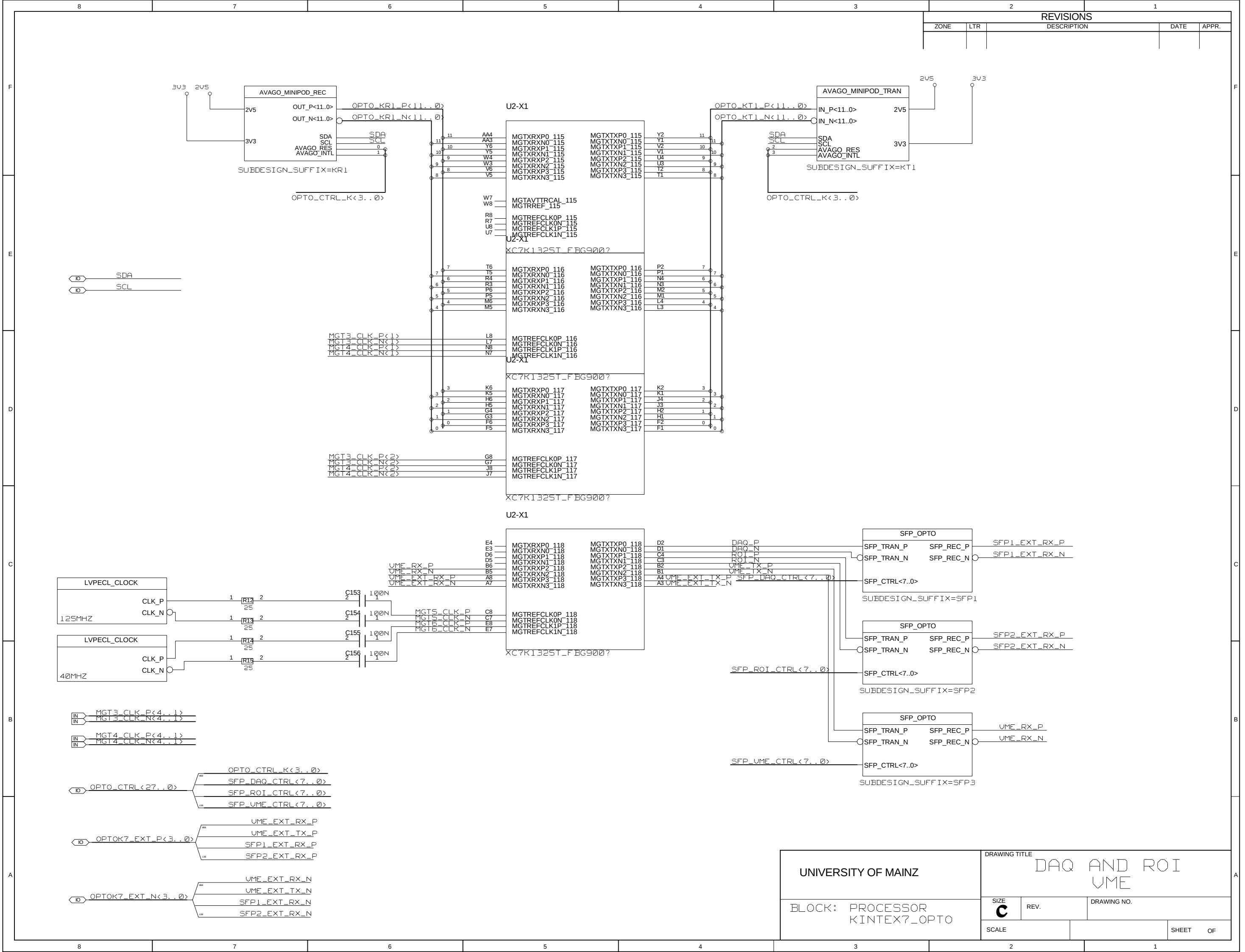
SHEET OF

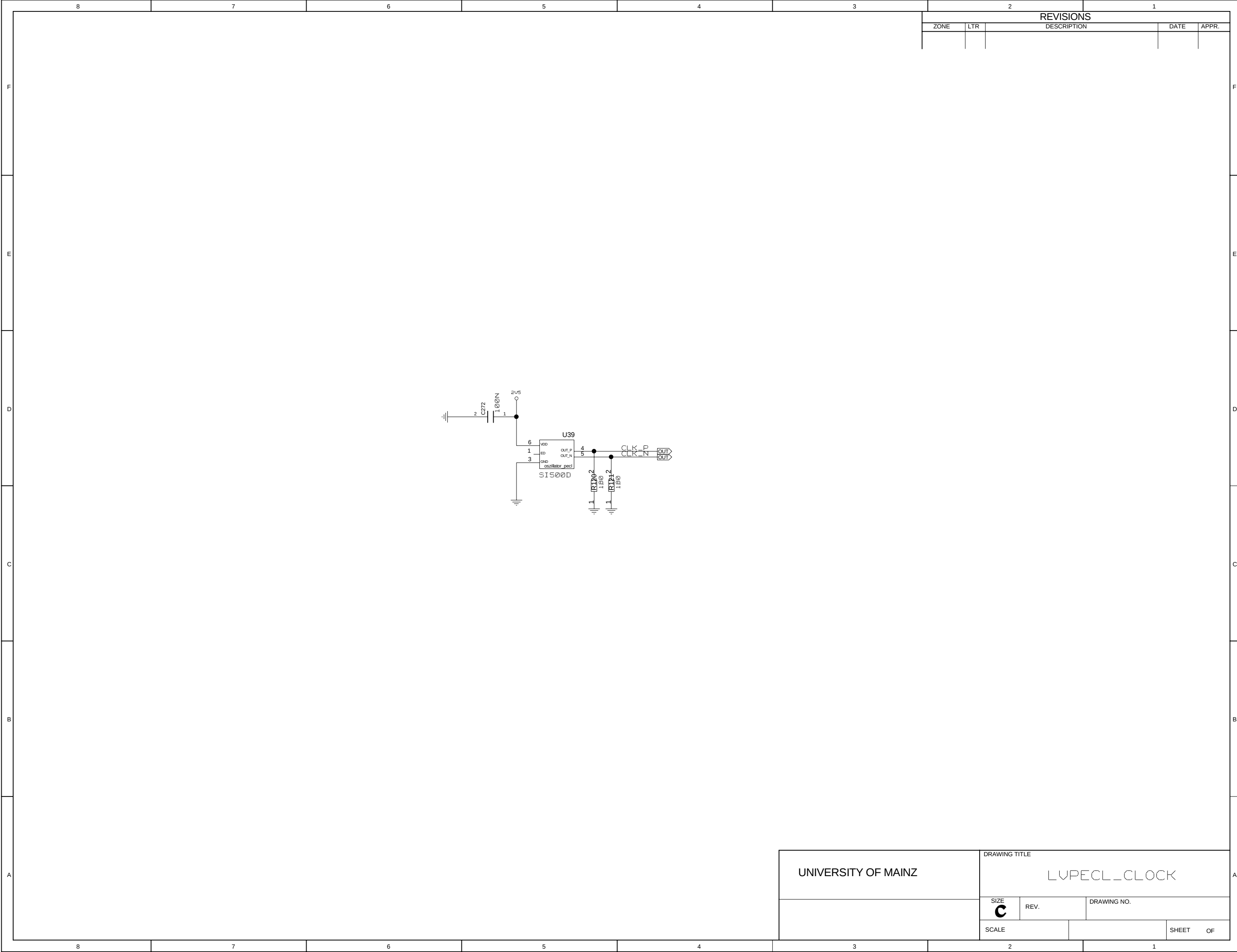


REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

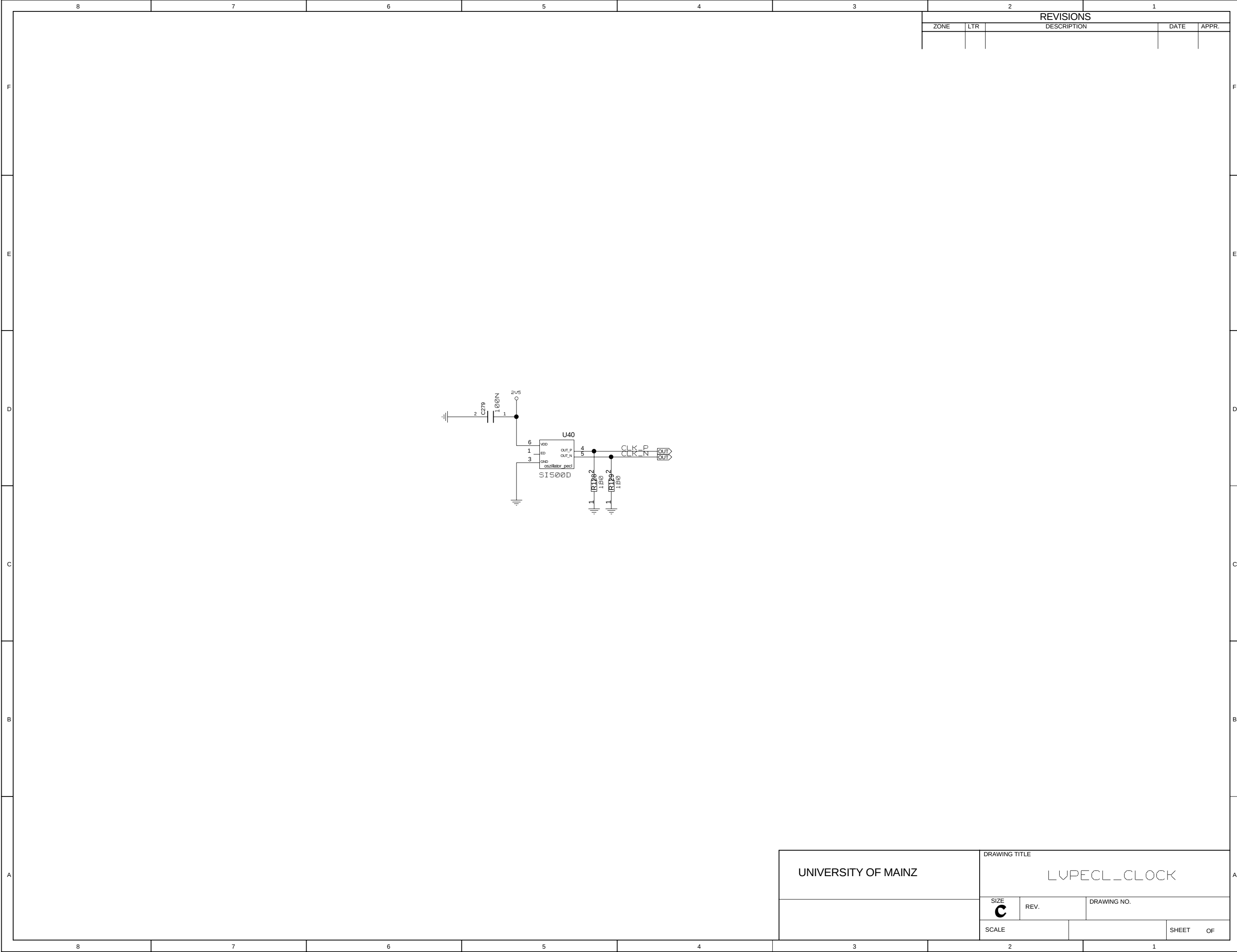
UNIVERSITY OF MAINZ		DRAWING TITLE		
BLOCK: PROCESSOR DATA_BUS		DATABUS		
SIZE		REV.	DRAWING NO.	
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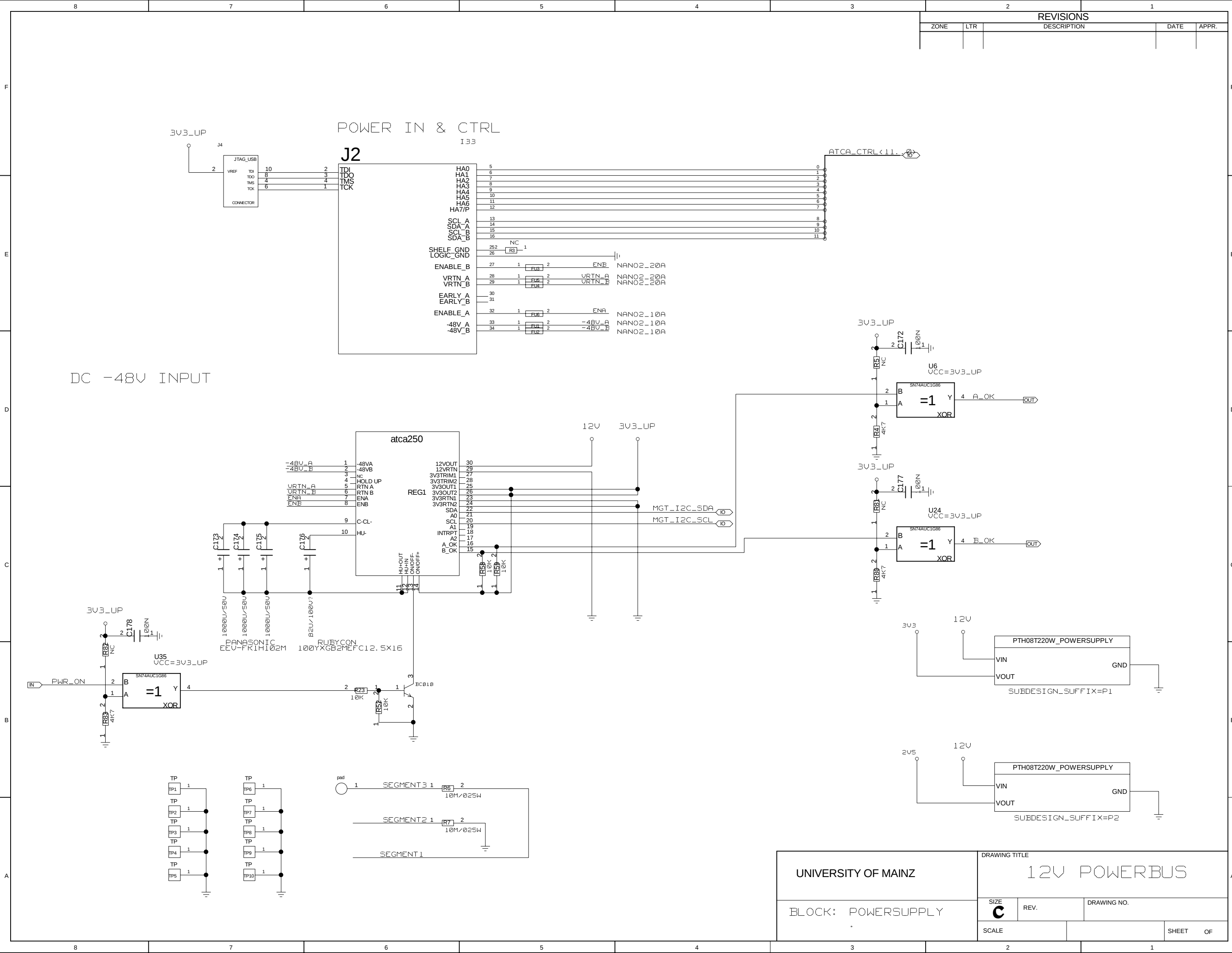


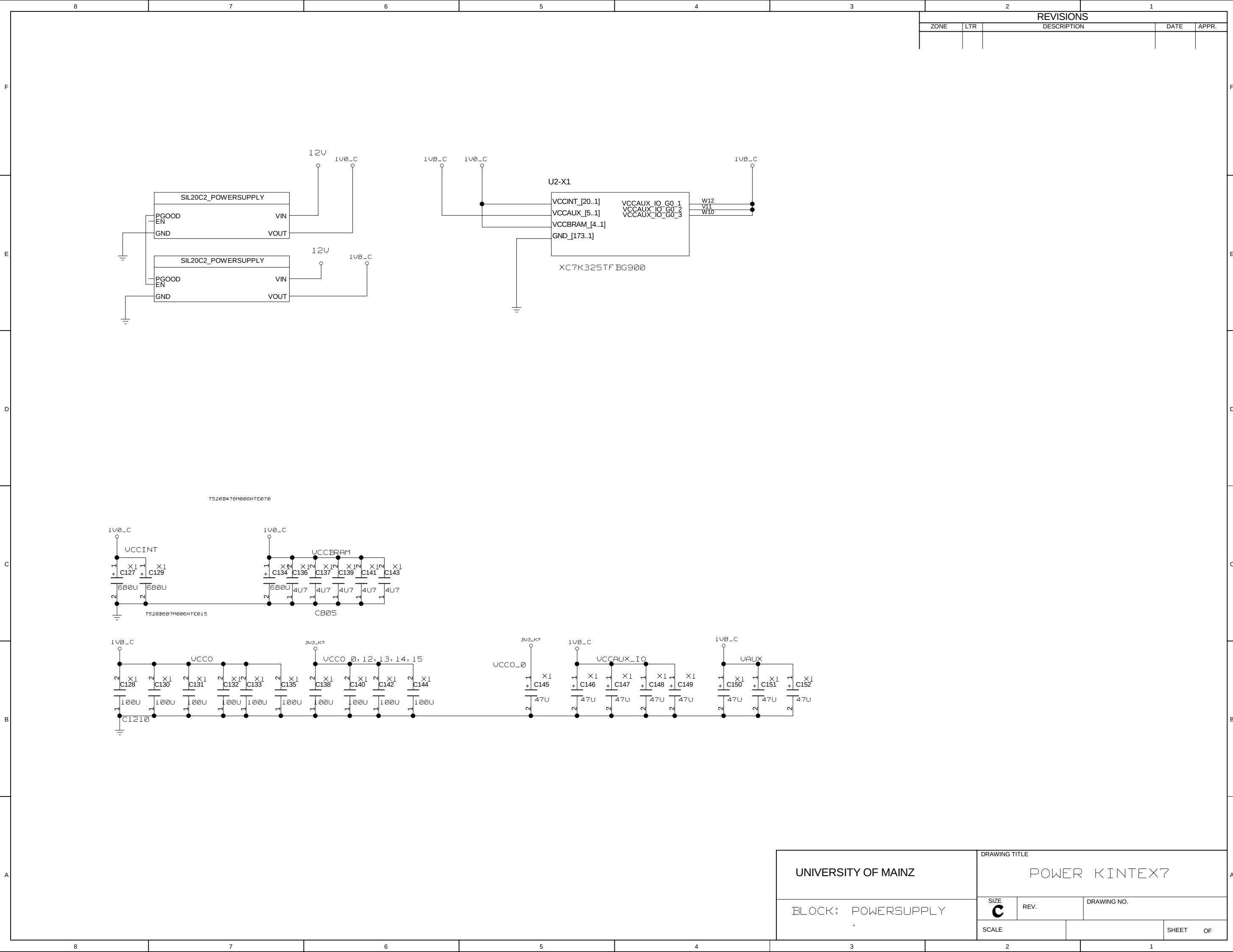


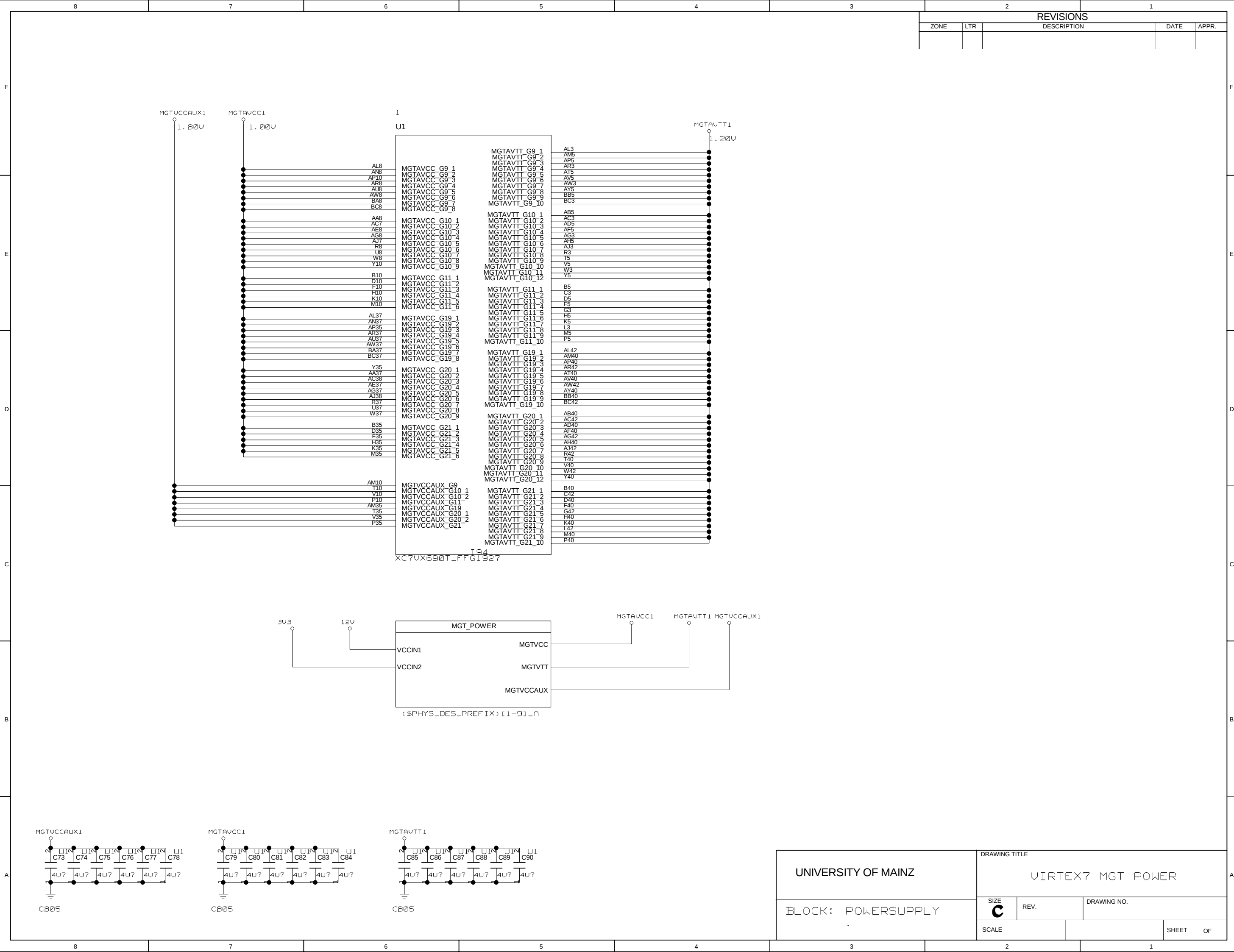


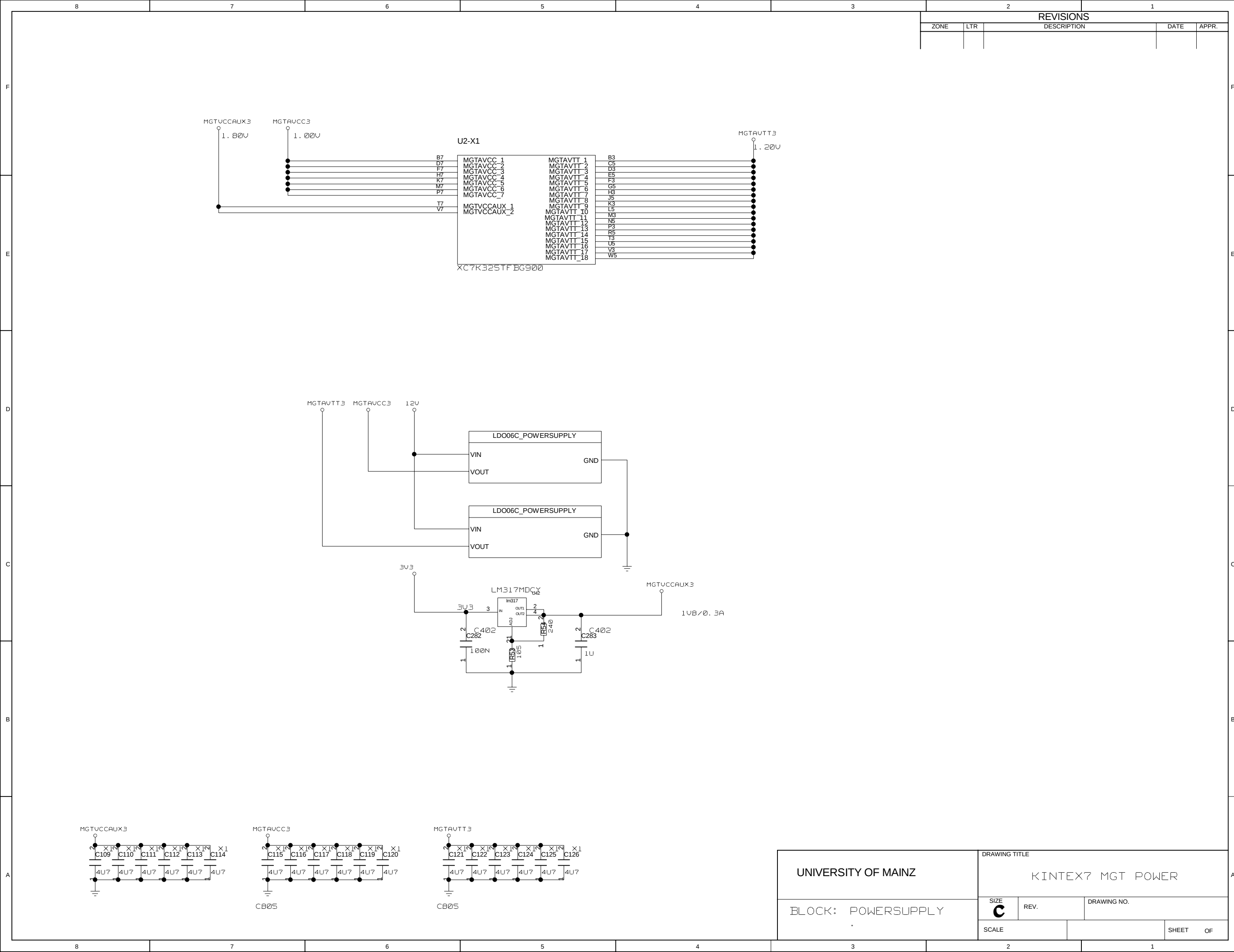
8		7		6		5		4		3		2		1	











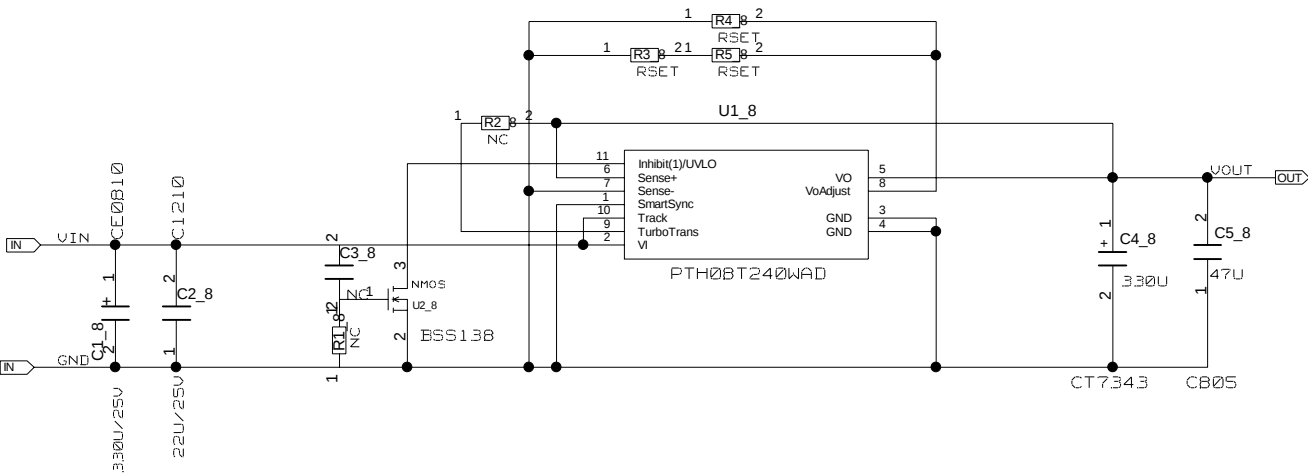


Table 2. Output Voltage Set-Point Resistor Values (Standard Values)			
V _O Required (V)	R _{SET} (kΩ)	V _O Required (V)	R _{SET} (Ω)
0.70 ⁽¹⁾	681	2.50	2.37 k
0.75 ⁽¹⁾	113	2.60	2.15 k
0.80 ⁽¹⁾	61.9	2.70	2.00 k
0.85 ⁽¹⁾	41.2	2.80	1.82 k
0.90 ⁽¹⁾	31.8	2.90	1.69 k
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1.00 ⁽¹⁾	20.5	3.10	1.43 k
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1.15 ⁽¹⁾	13.3	3.40	1.10 k
1.20 ⁽¹⁾	12.1	3.50	1.02 k
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1.30	9.88	3.70 ⁽²⁾	866
1.35	9.09	3.80 ⁽²⁾	787
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1.45	7.68	4.00 ⁽²⁾	649
1.50	6.98	4.10 ⁽²⁾	590
1.55	6.49	4.20 ⁽²⁾	536
1.60	6.04	4.30 ⁽²⁾	475
1.65	5.78	4.40 ⁽²⁾	432
1.70	5.36	4.50 ⁽²⁾	383
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2.00	3.83	5.10 ⁽²⁾	133
2.10	3.40	5.20 ⁽²⁾	100
2.20	3.09	5.30 ⁽²⁾	66.5
2.30	2.67	5.40 ⁽²⁾	34.8
2.40	2.61	5.50 ⁽²⁾	4.99

(1) For output voltages ≤ 1.2V, at nominal operating frequency, the output ripple may increase (typically 2x) when operating at input voltages greater than (V_O × 11). When using the SmartSync feature, review the SmartSync application section for further guidance.
(2) For V_O > 3.6 V, the minimum input voltage is (V_O + 2) V.

UNIVERSITY OF MAINZ

DRAWING TITLE

PTH08T240W_POWERSUPPLY

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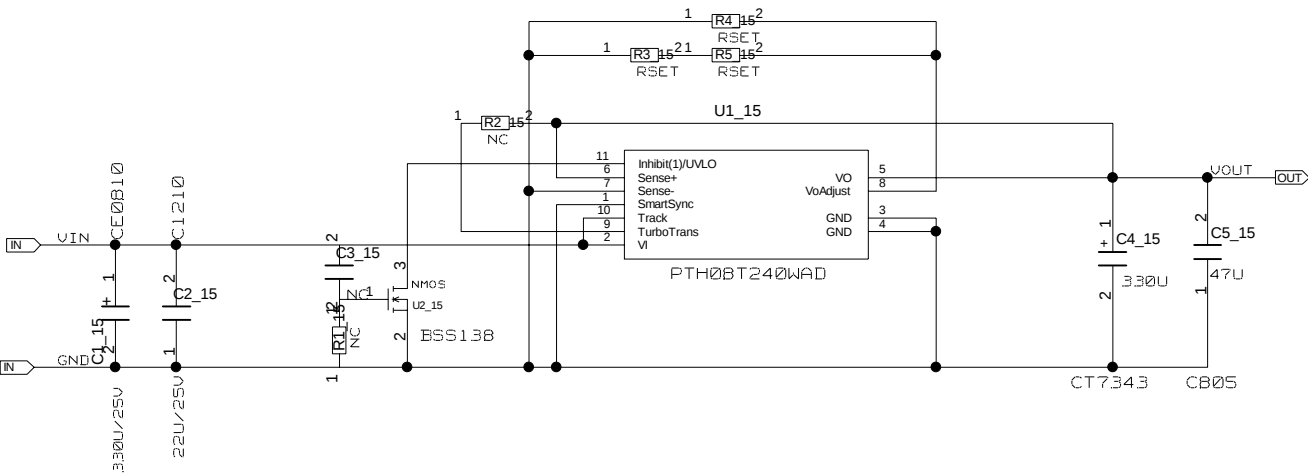


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UNIVERSITY OF MAINZ

DRAWING TITLE

PTH08T240W_POWERSUPPLY

SIZE

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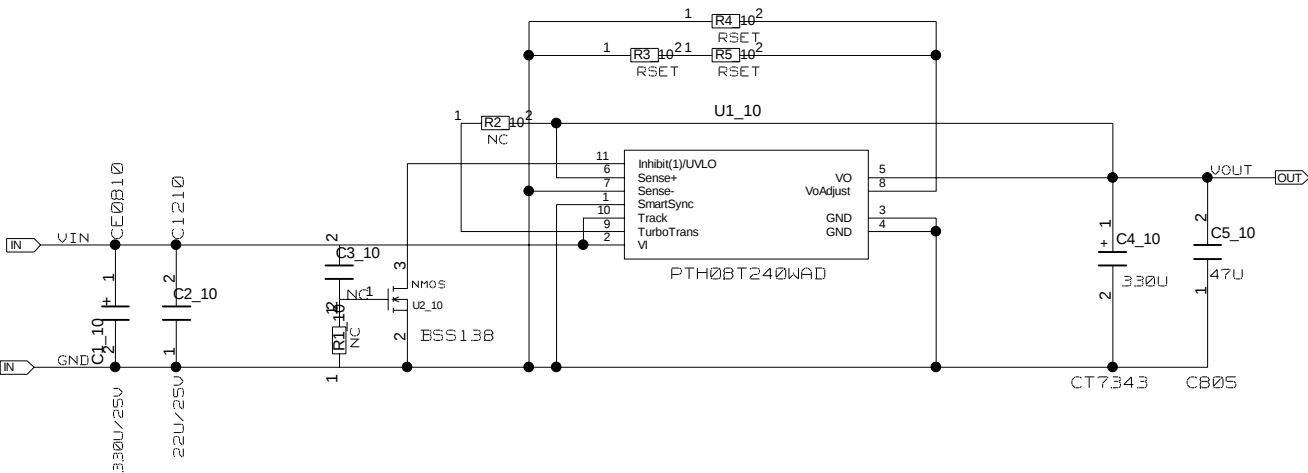


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REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

UNIVERSITY OF MAINZ		DRAWING TITLE		
		PTH08T240W_POWERSUPPLY		
		SIZE C	REV.	DRAWING NO.
SCALE				SHEET OF

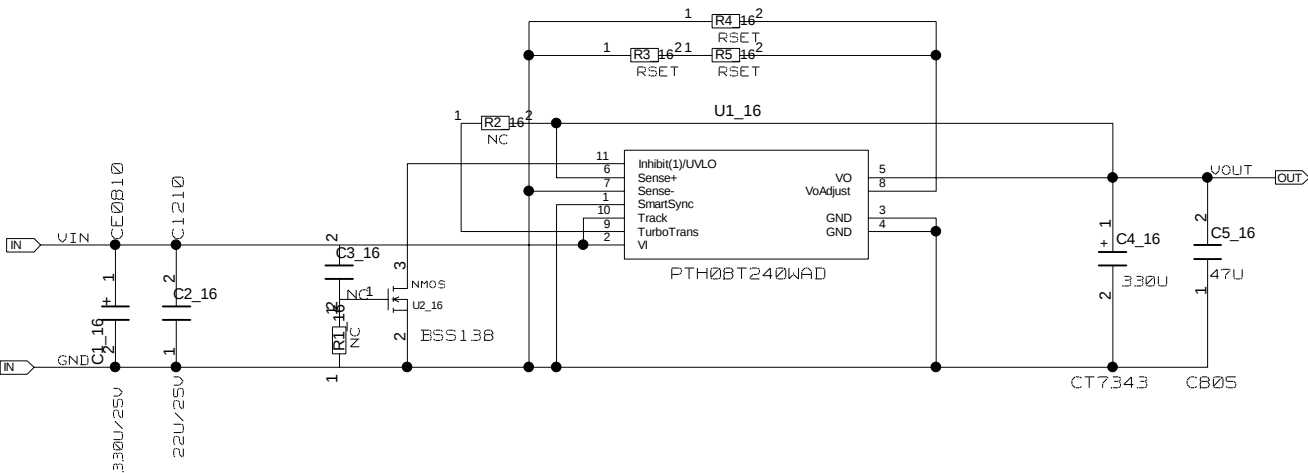


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DRAWING TITLE

PTH08T240W_POWERSUPPLY

SIZE

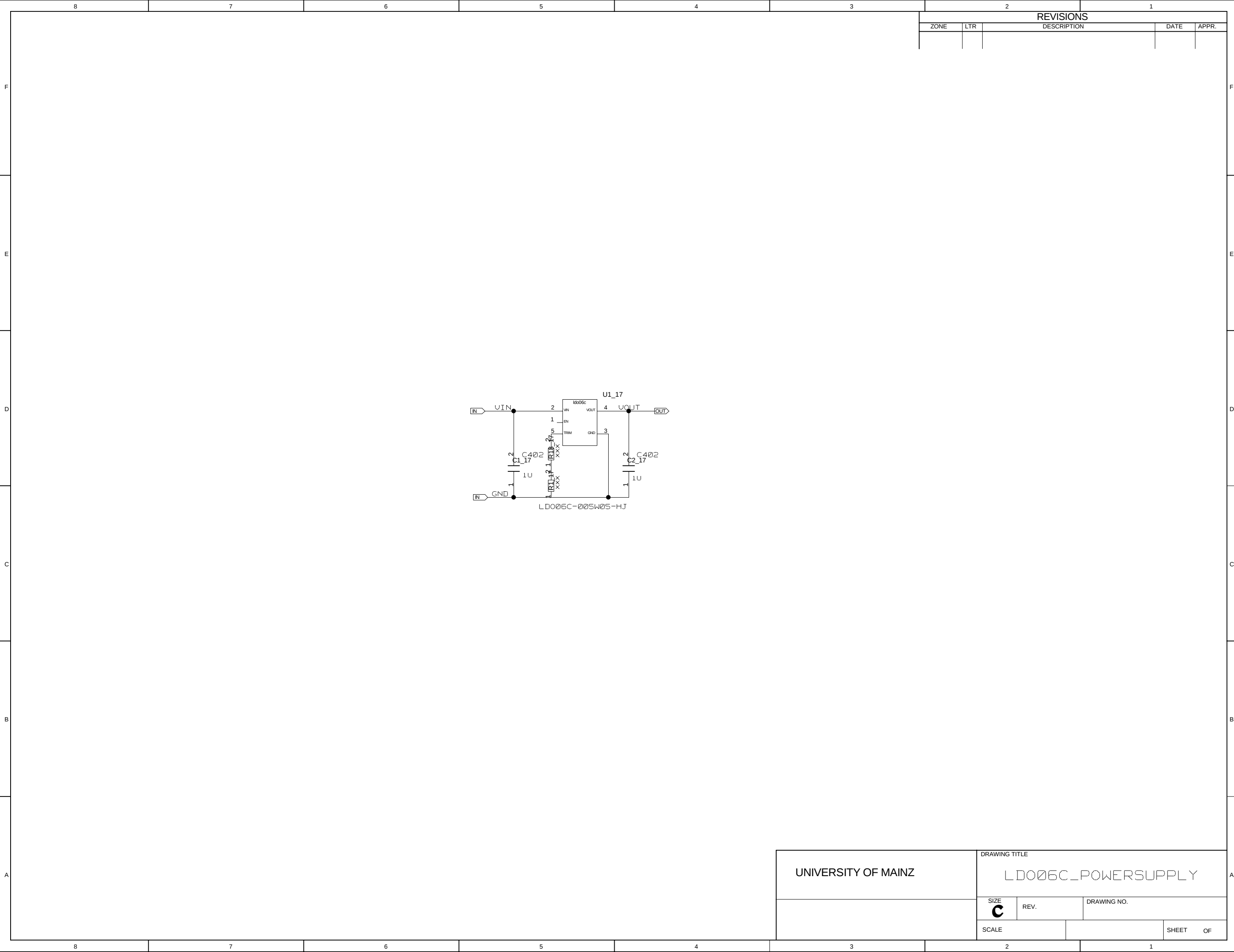
REV.

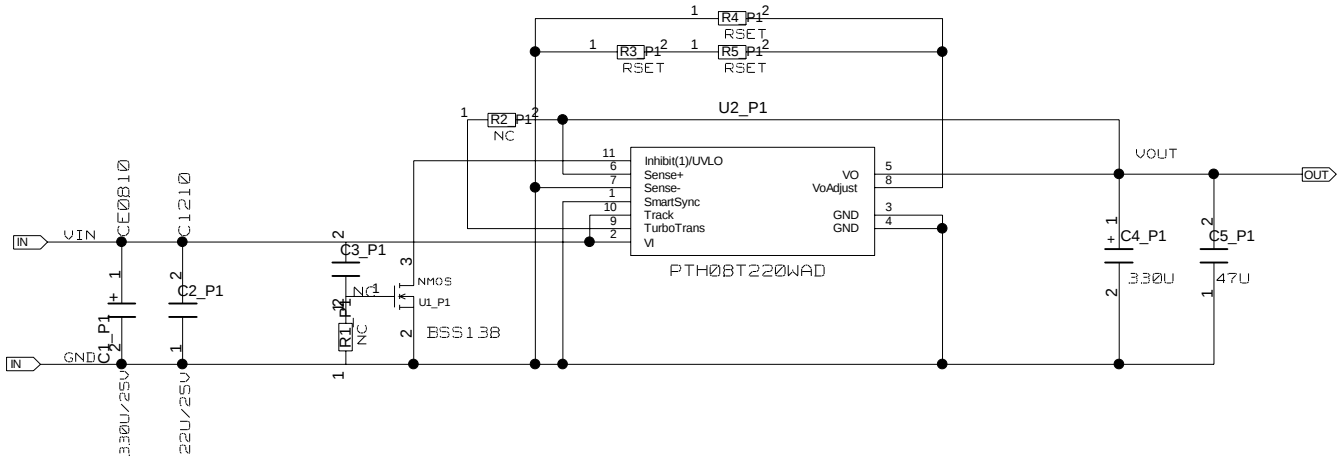
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$$R_{SET} = 10\text{ k}\Omega \times \frac{0.69}{V_O - 0.69} - 1.43\text{ k}\Omega$$

Table 1. Standard Values of R_{SET} for Standard Output Voltages

V _O (Standard)	R _{SET} (Standard Value)	V _O (Actual)
5.0 V ⁽¹⁾	169 Ω	5.005 V
3.3 V	1.21 kΩ	3.304 V
2.5 V	2.37 kΩ	2.506 V
1.8 V	4.75 kΩ	1.807 V
1.5 V	6.98 kΩ	1.510 V
1.2 V ⁽²⁾	12.1 kΩ	1.200 V
1.0 V ⁽²⁾	20.5 kΩ	1.004 V
0.7 V ⁽²⁾	681 kΩ	0.700 V

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PTH08T220W_POWER_SUPPLY

SIZE

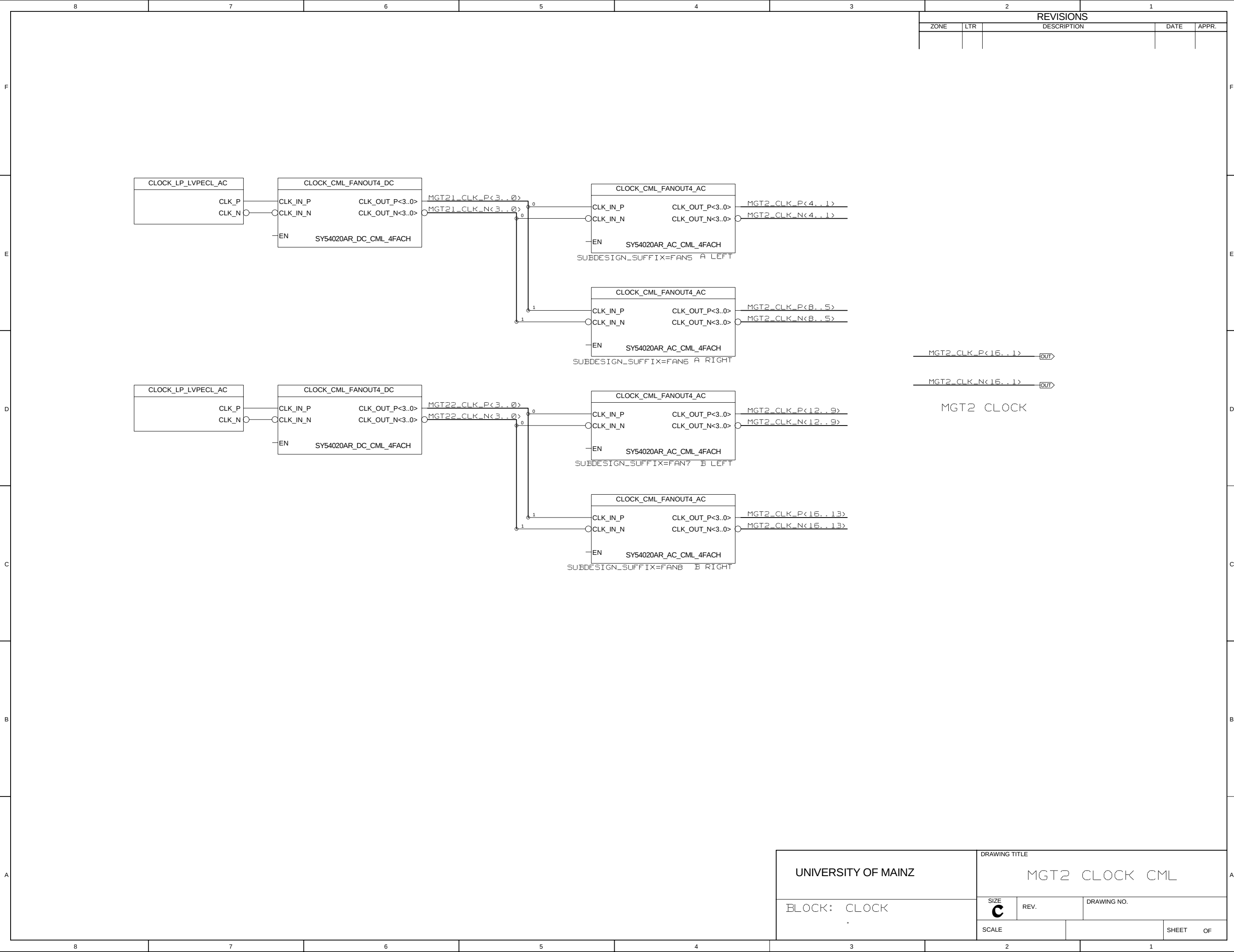
C

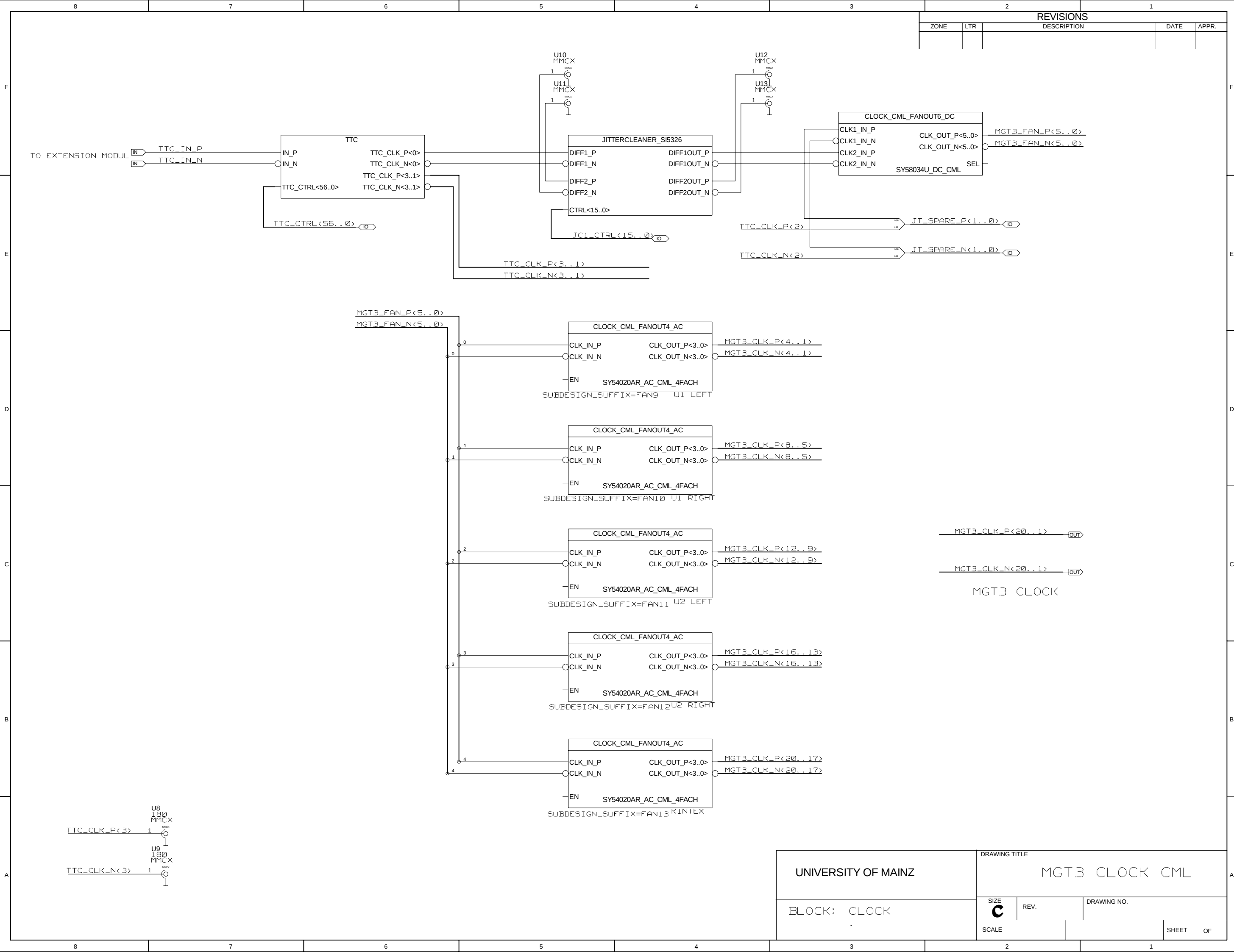
SCALE

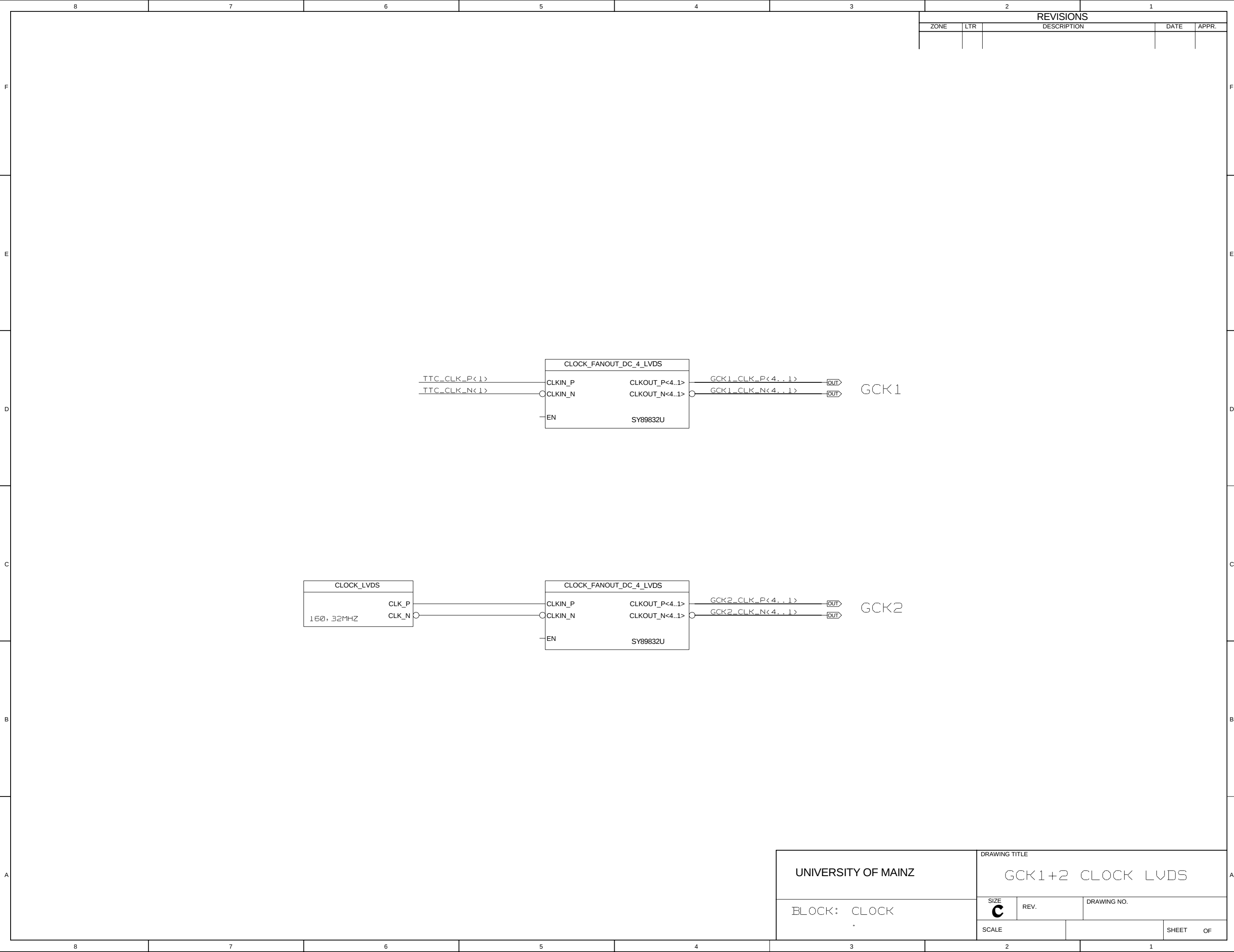
REV.

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BLOCK: CLOCK

DRAWING TITLE

GCK1+2 CLOCK LVDS

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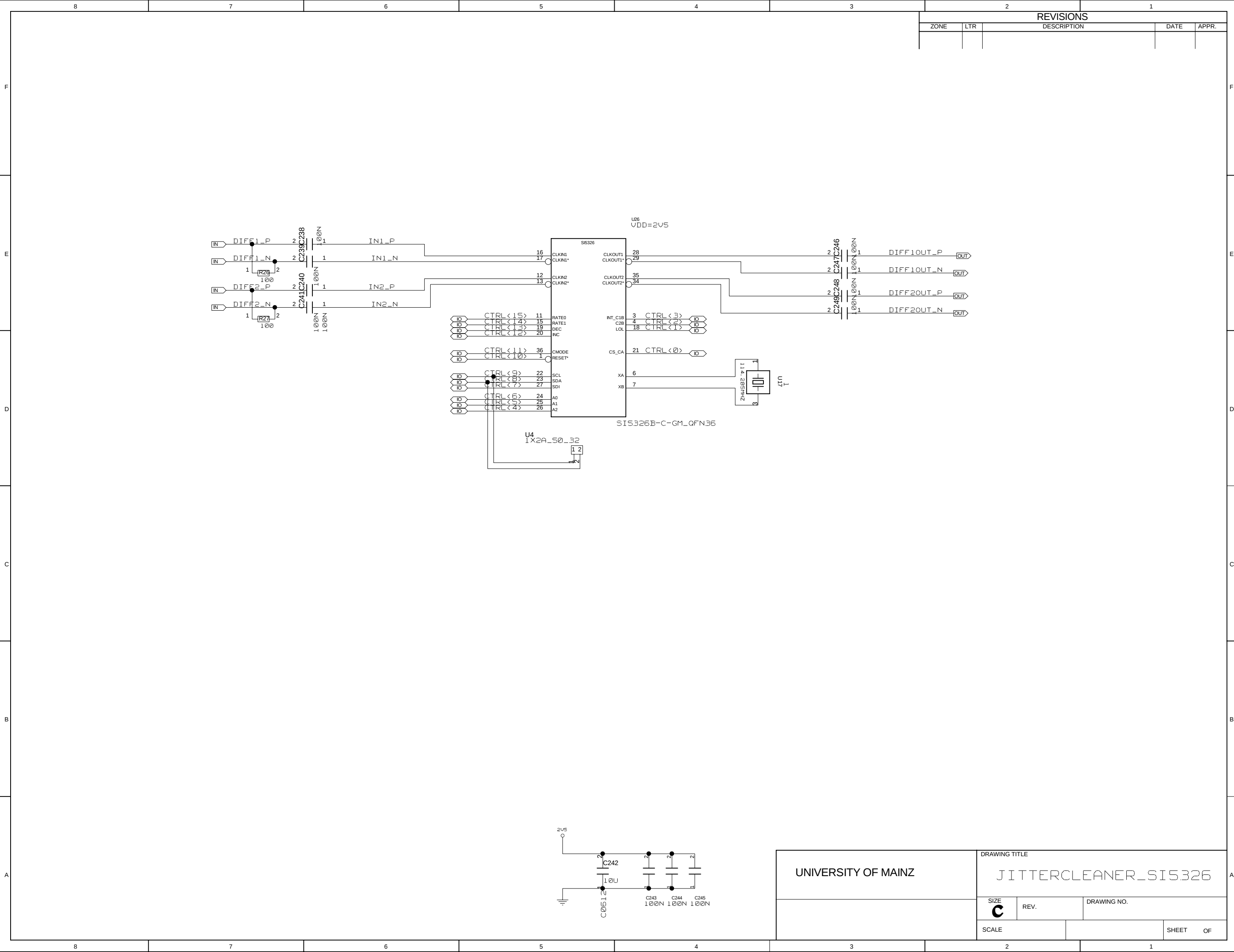
REV.

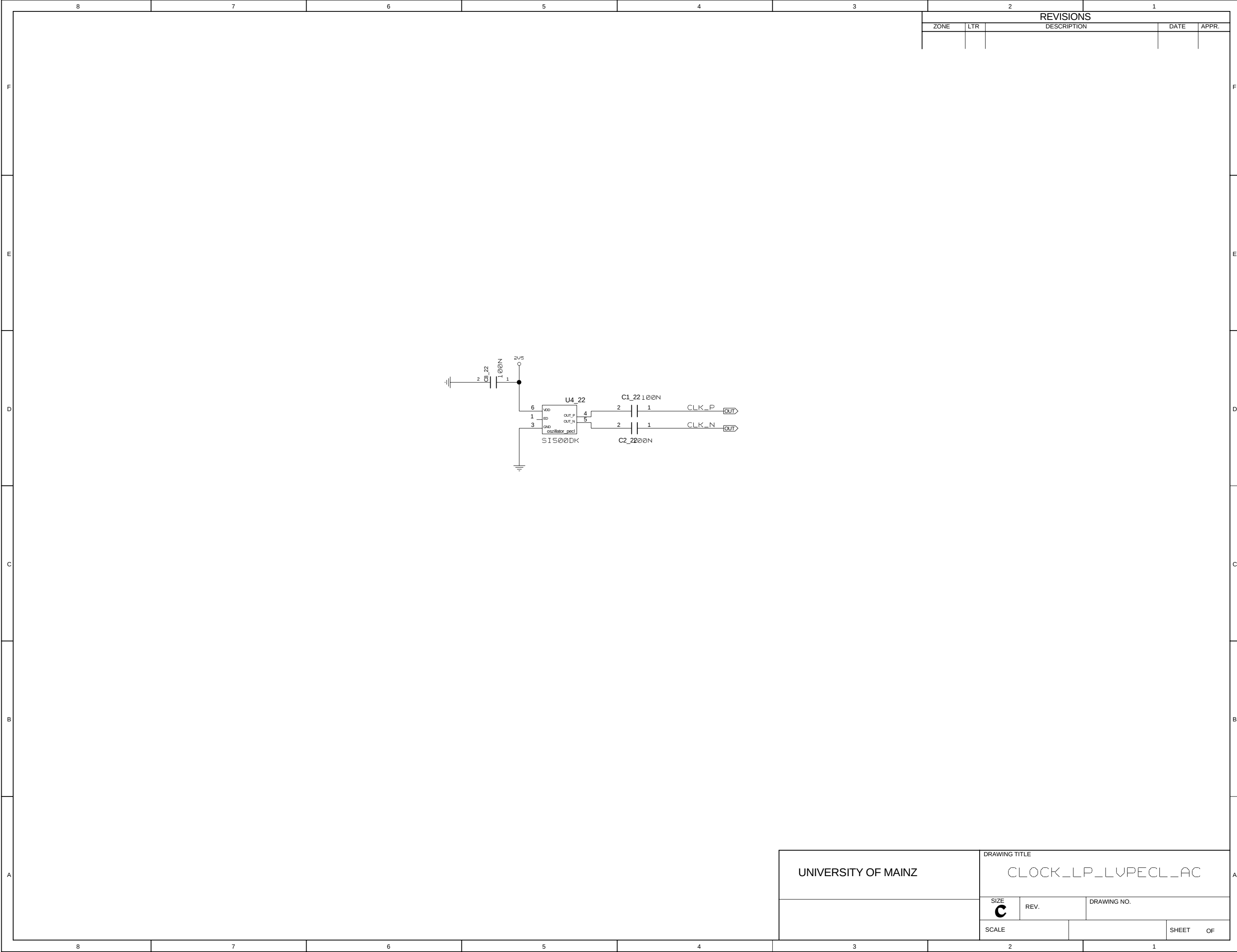
DRAWING NO.

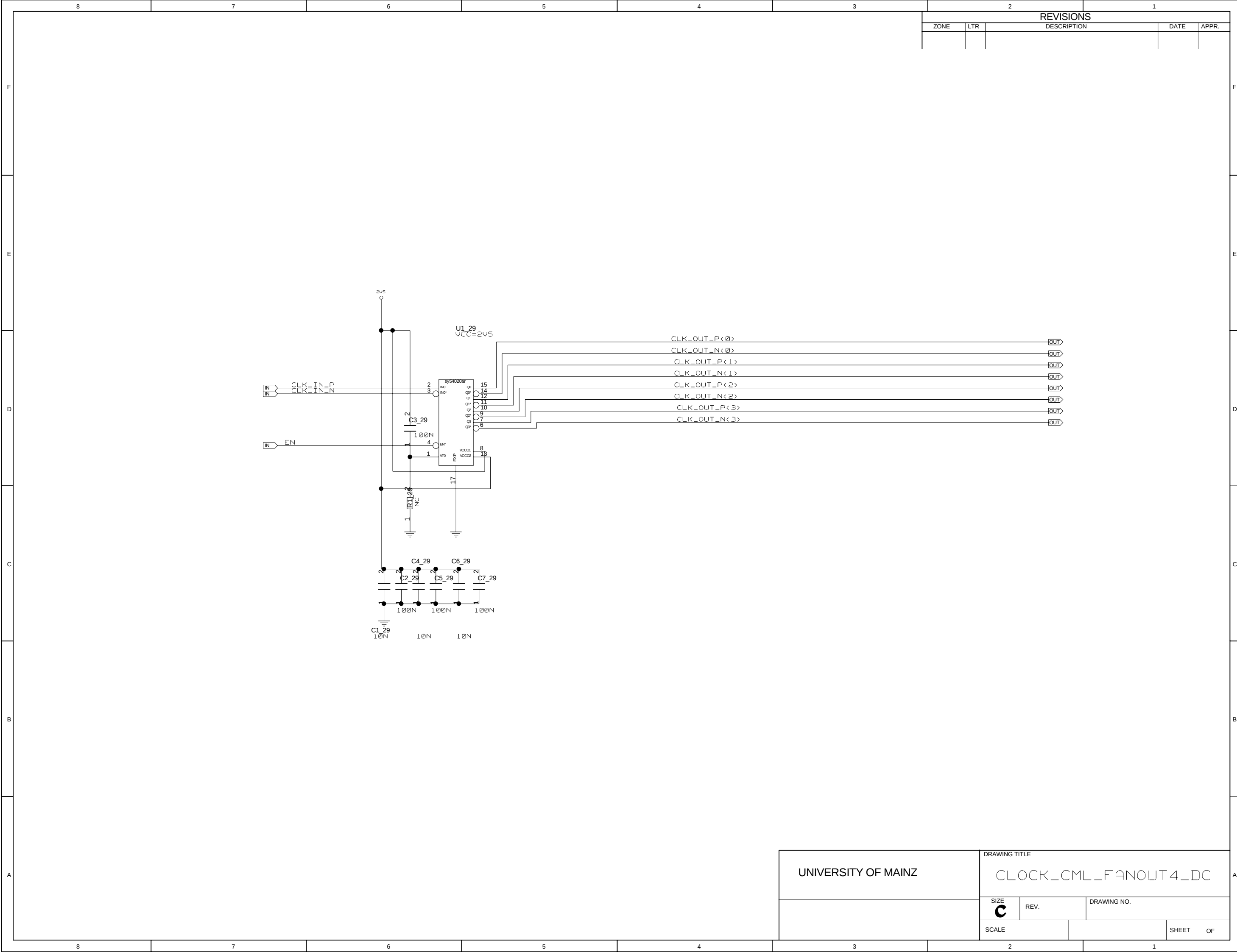
SCALE

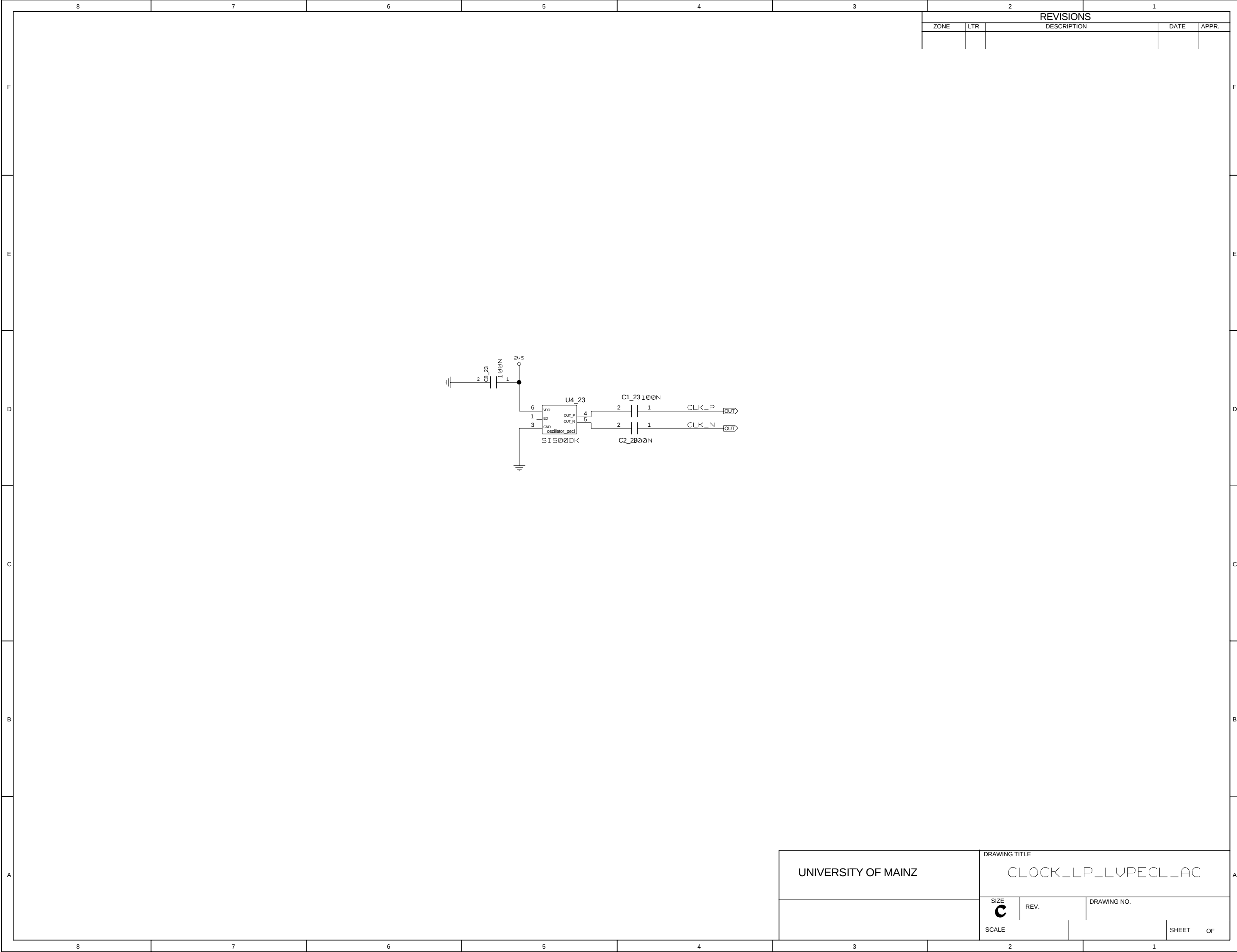
SHEET

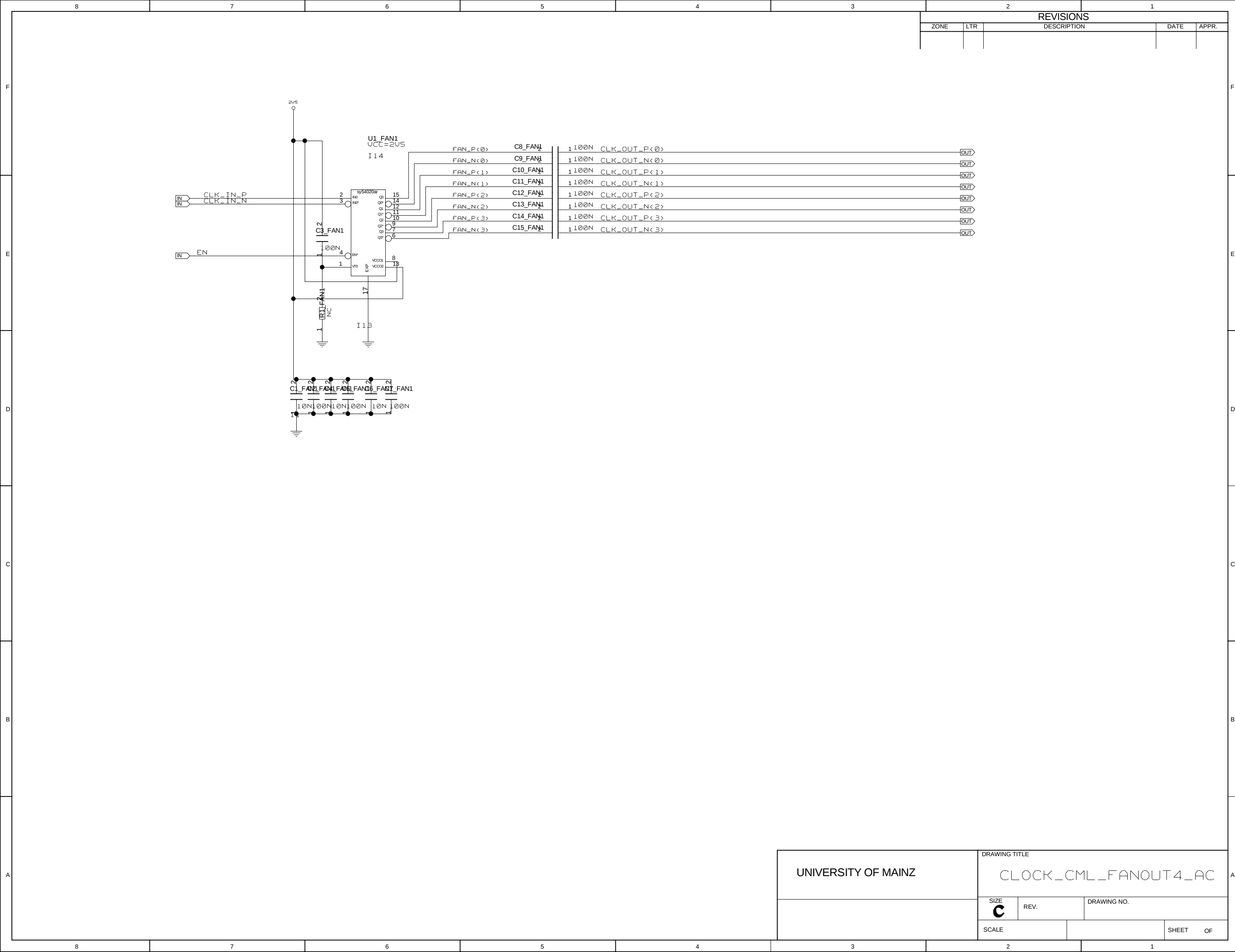
OF

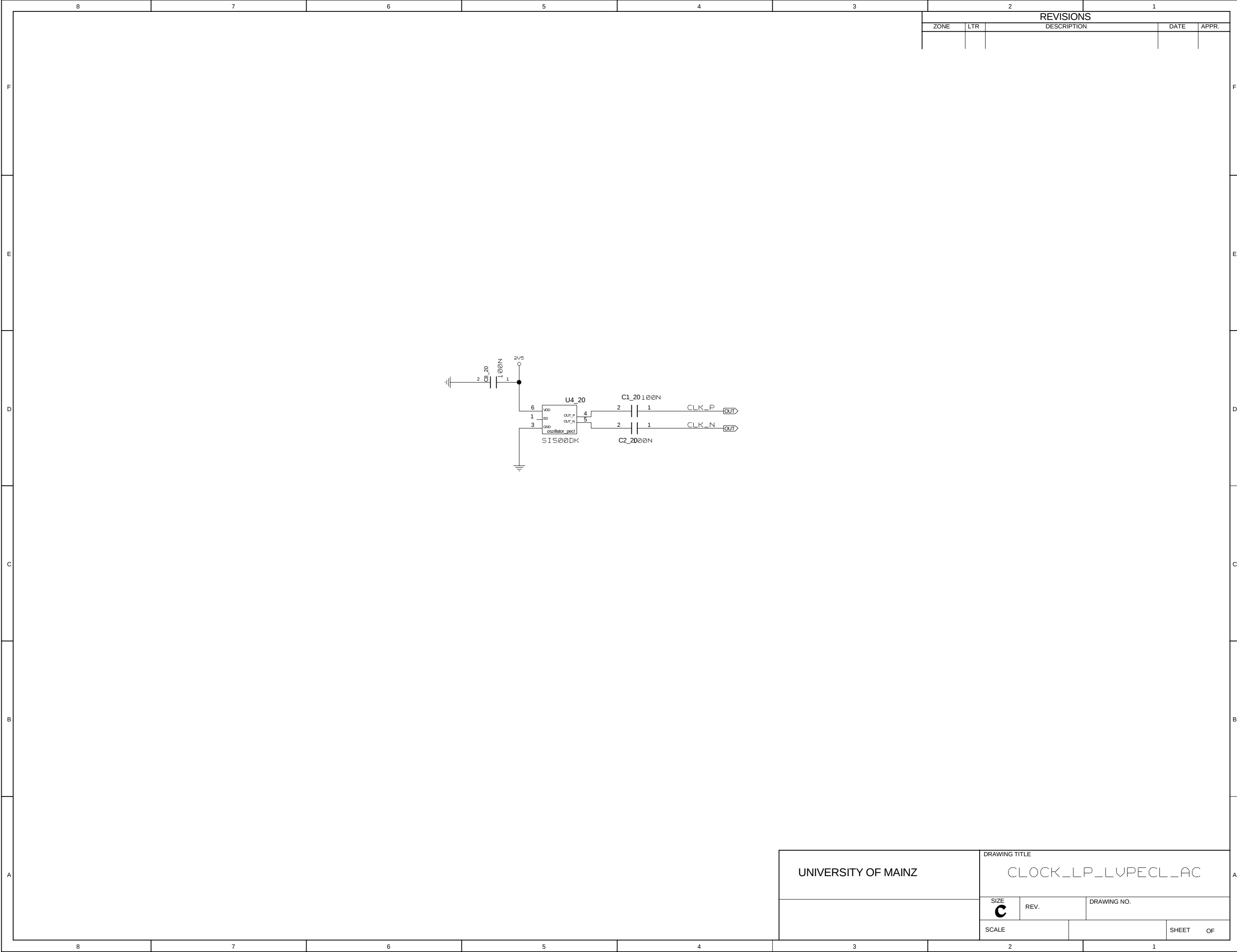




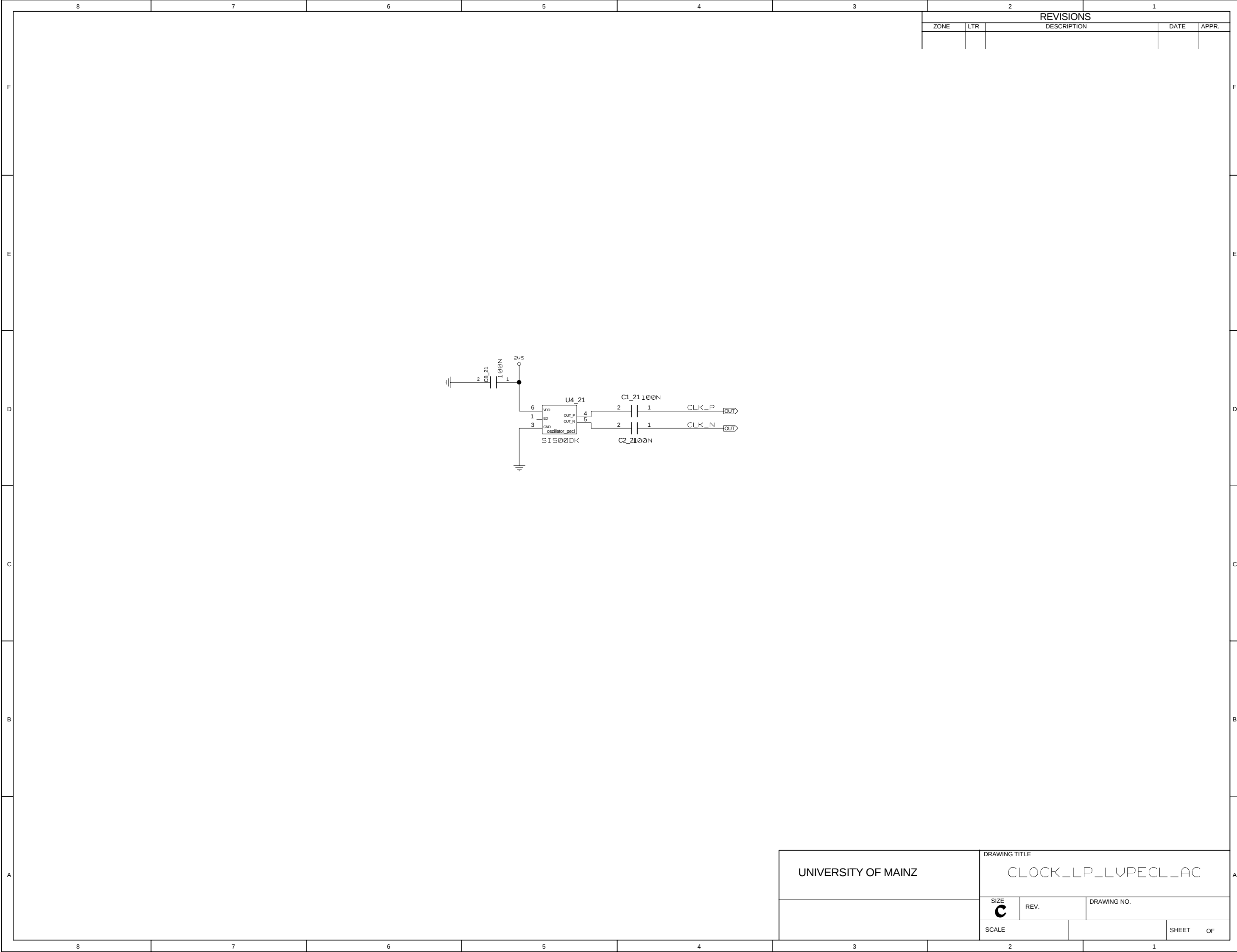


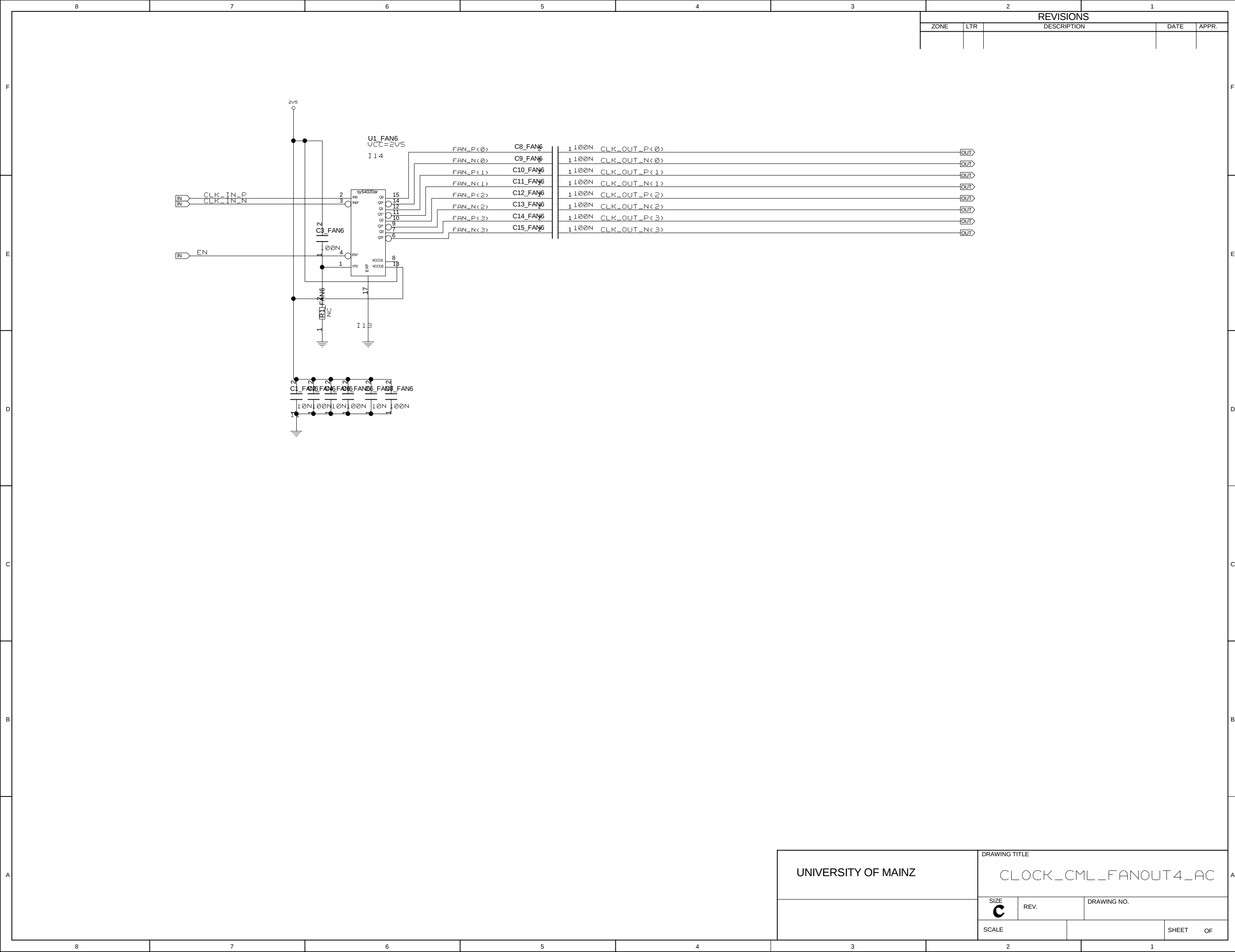


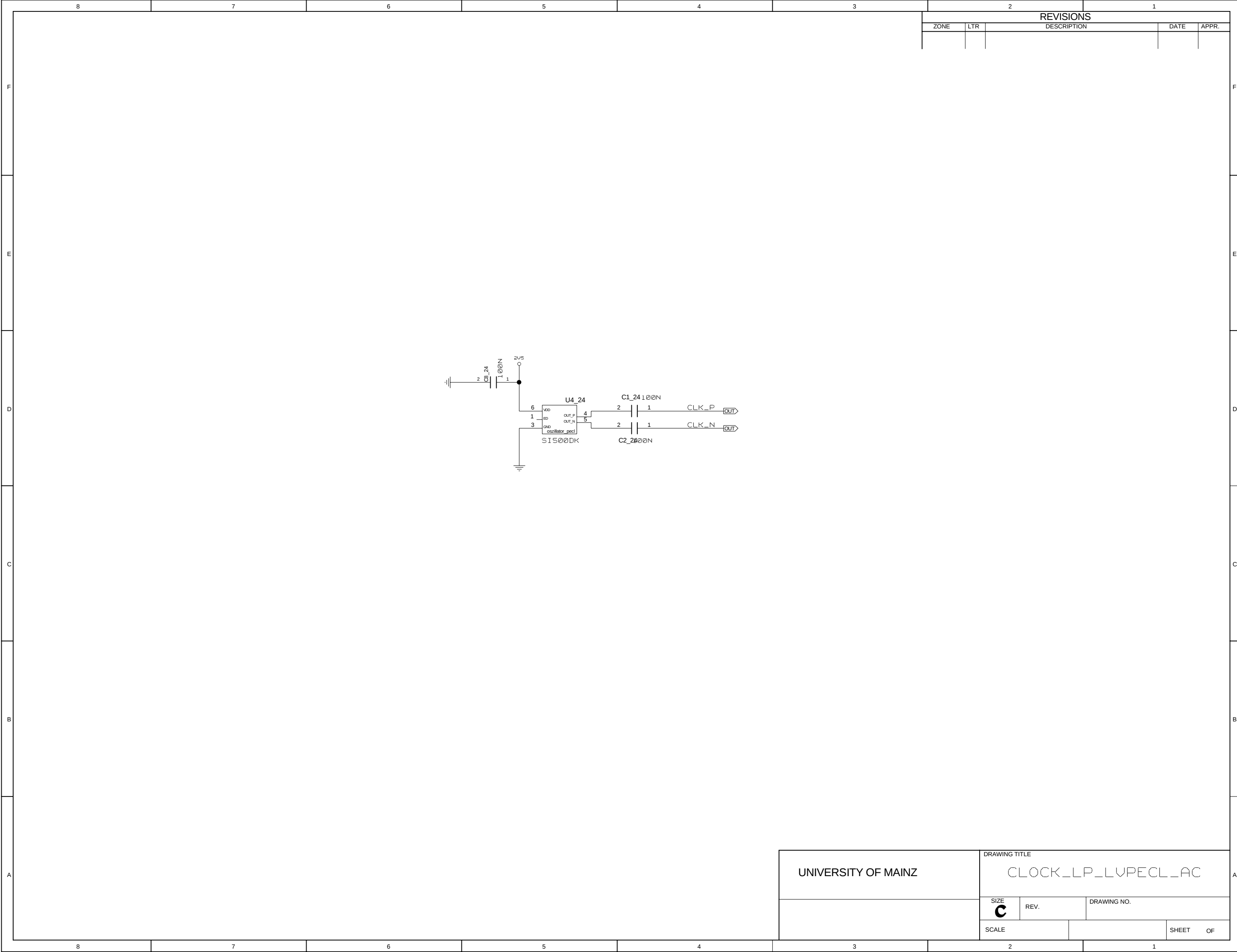




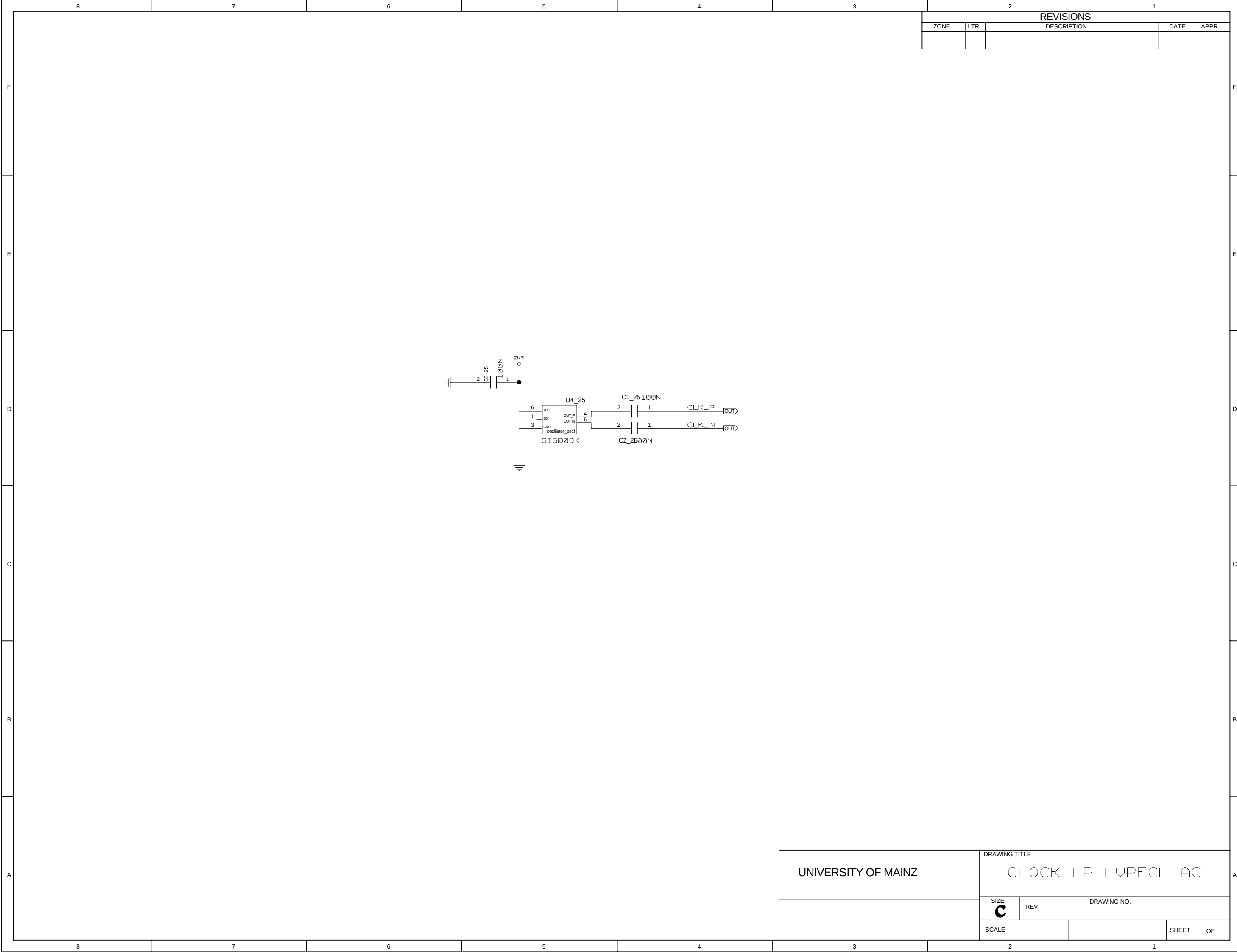
UNIVERSITY OF MAINZ		DRAWING TITLE	
		CLOCK_LP_LVPECL_AC	
		SIZE C	DRAWING NO.
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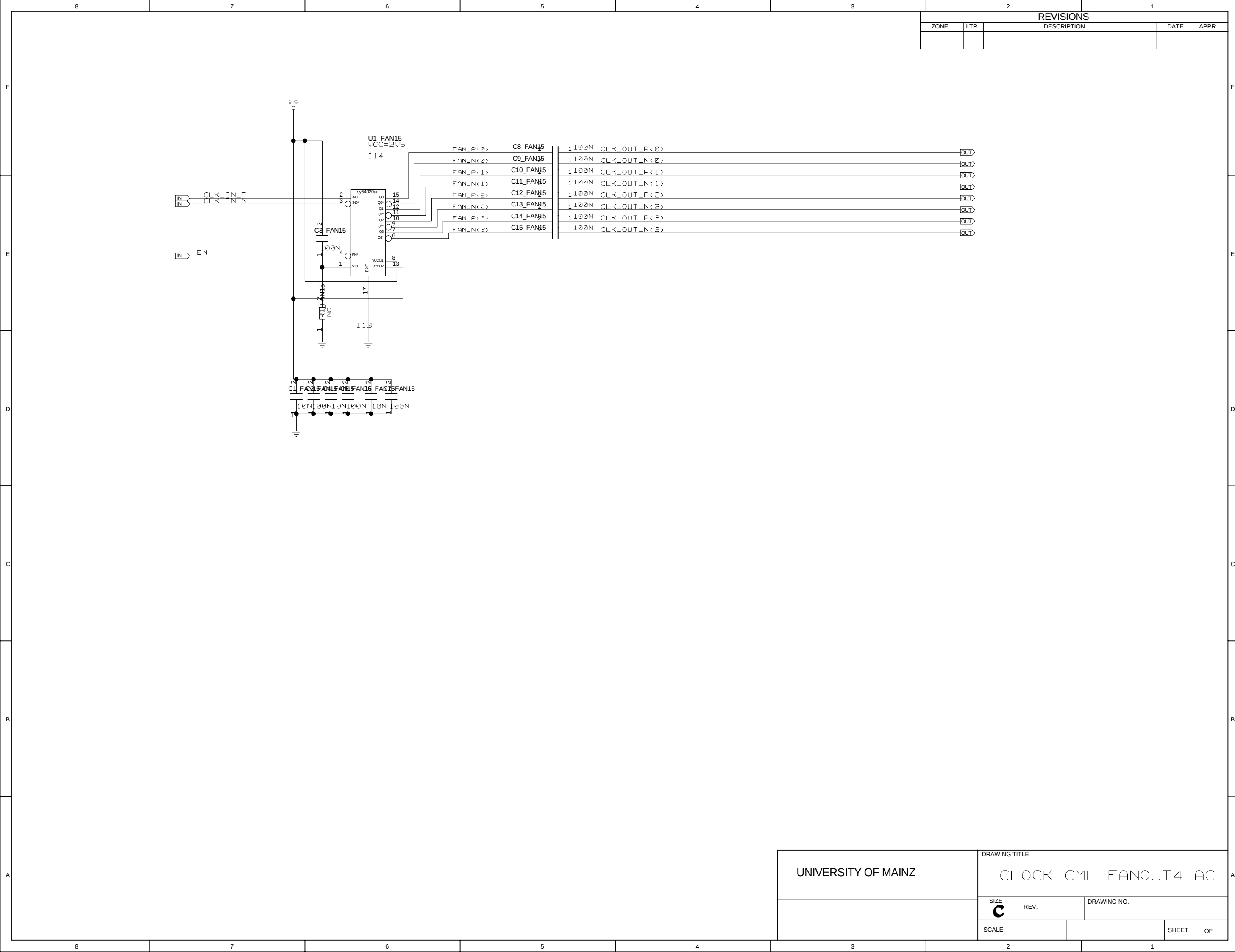


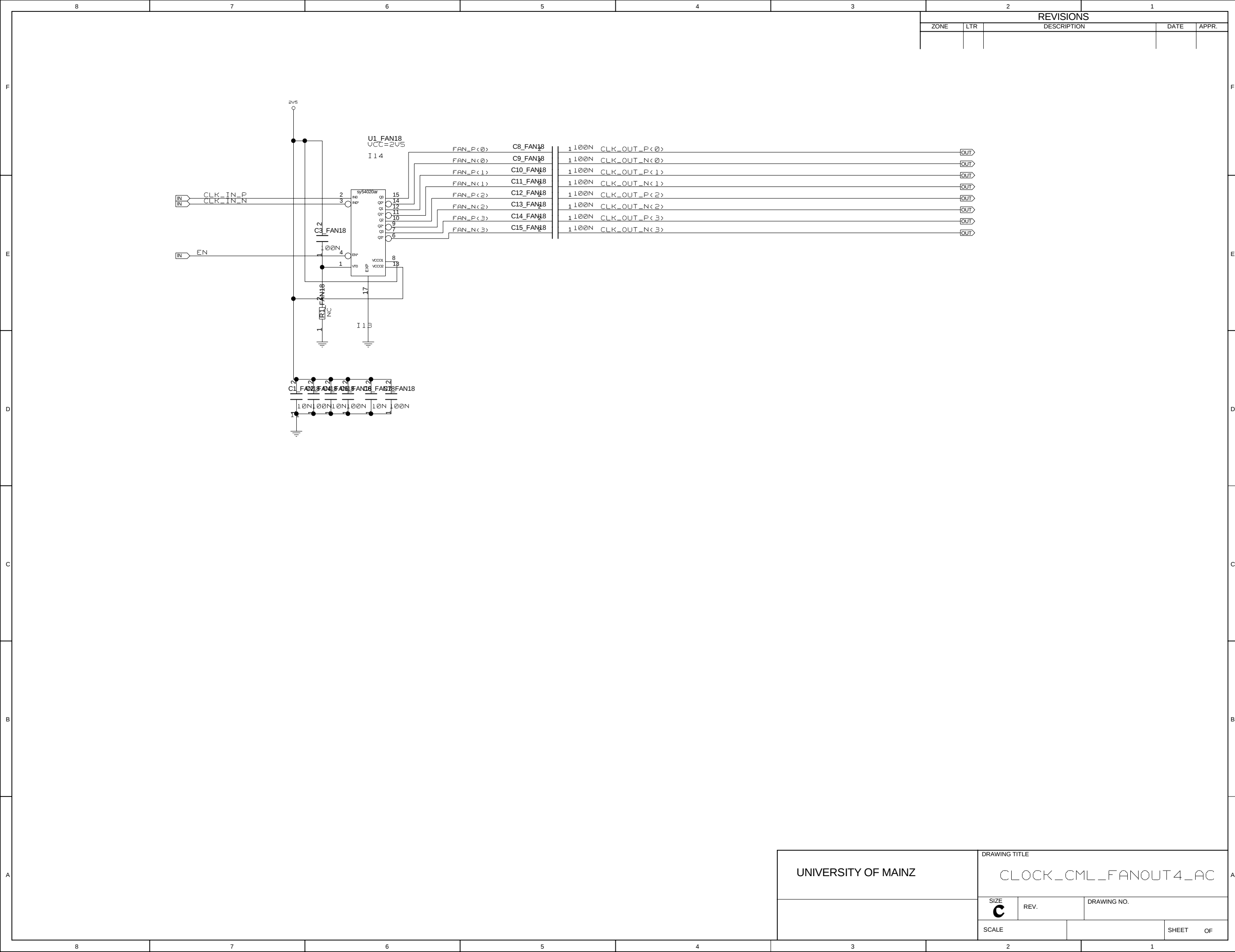


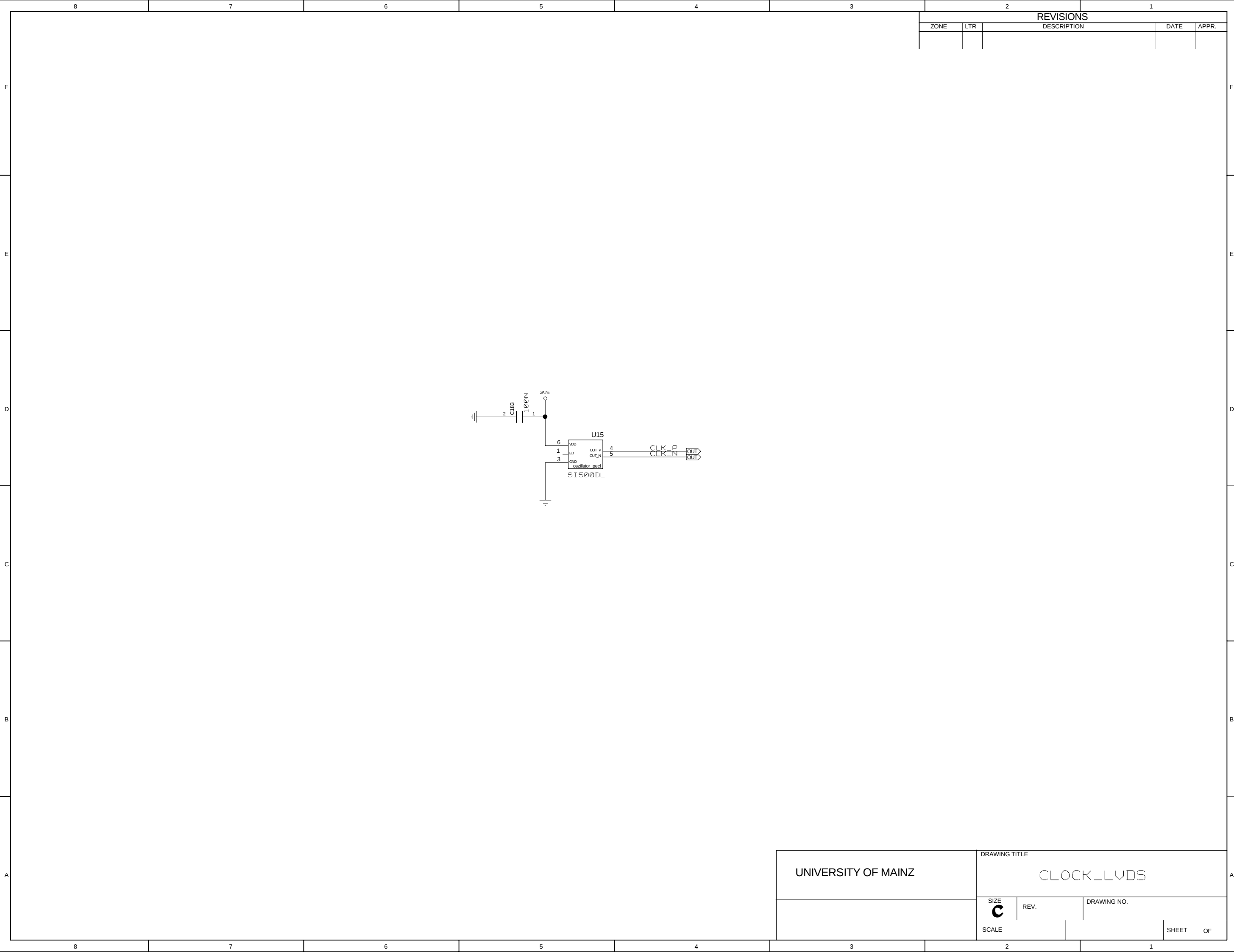
UNIVERSITY OF MAINZ		DRAWING TITLE	
		CLOCK_LP_LVPECL_AC	
SIZE	REV.	DRAWING NO.	
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		1	

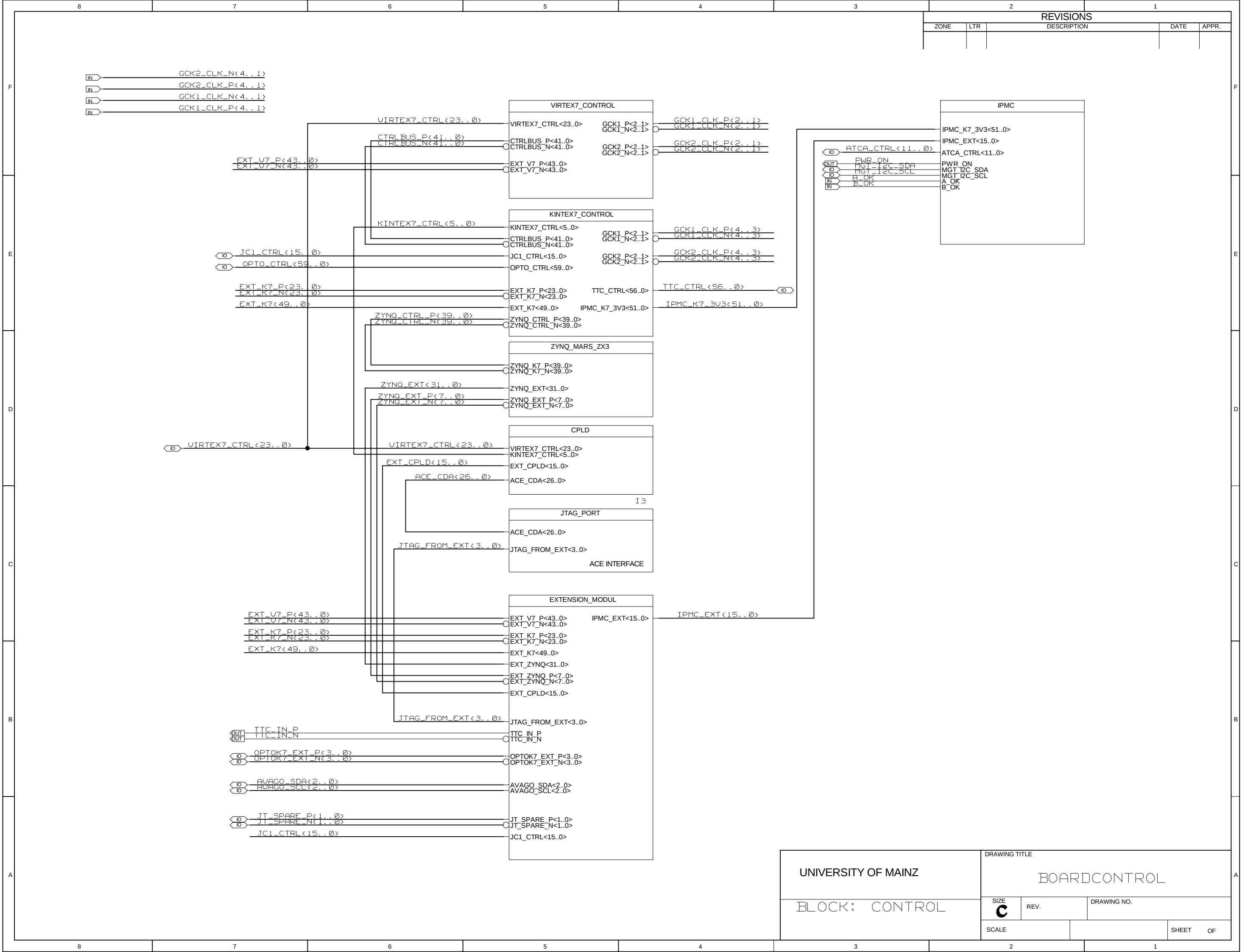


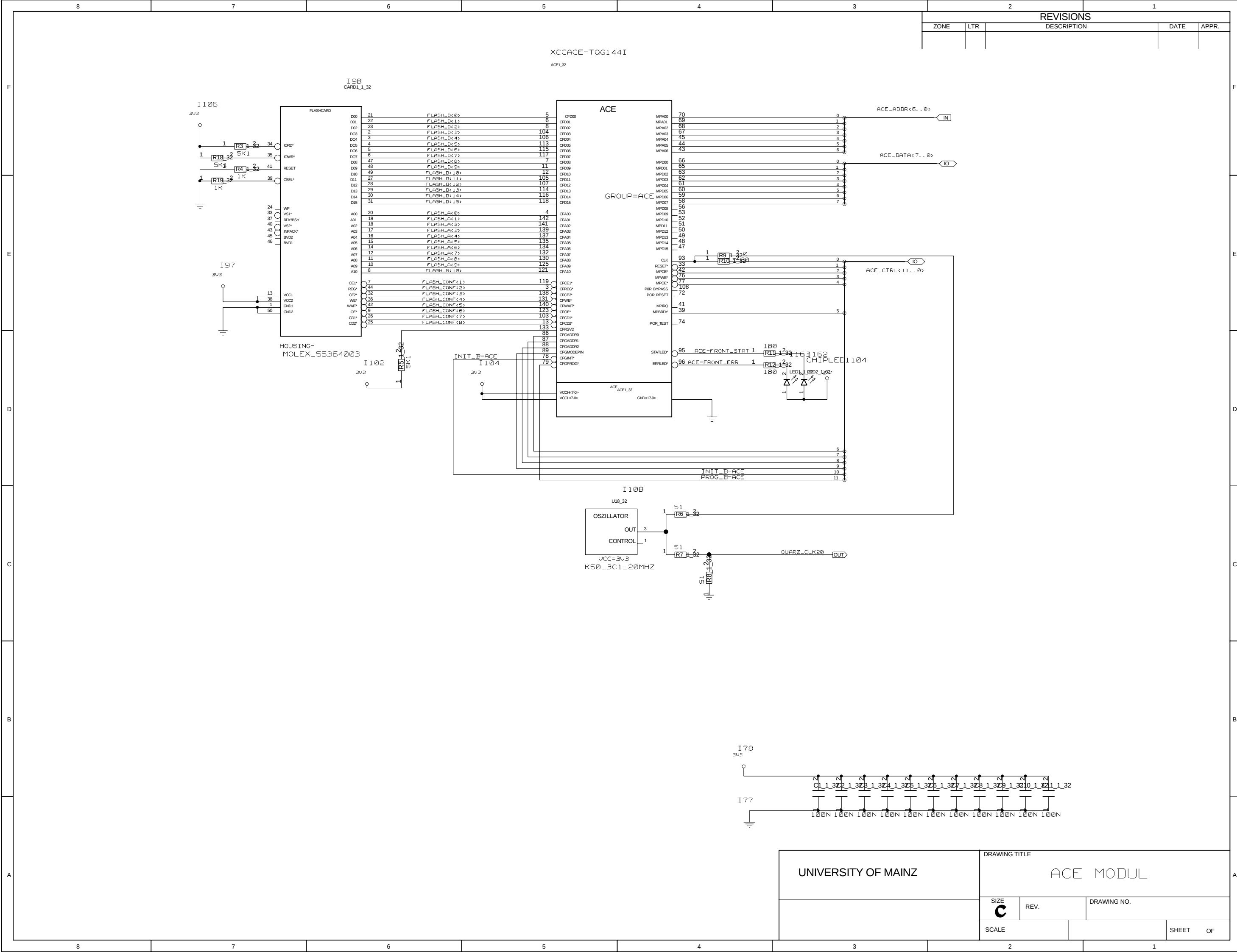
UNIVERSITY OF MAINZ		DRAWING TITLE	
		CLOCK_LP_LVPECL_AC	
		SIZE C	DRAWING NO.
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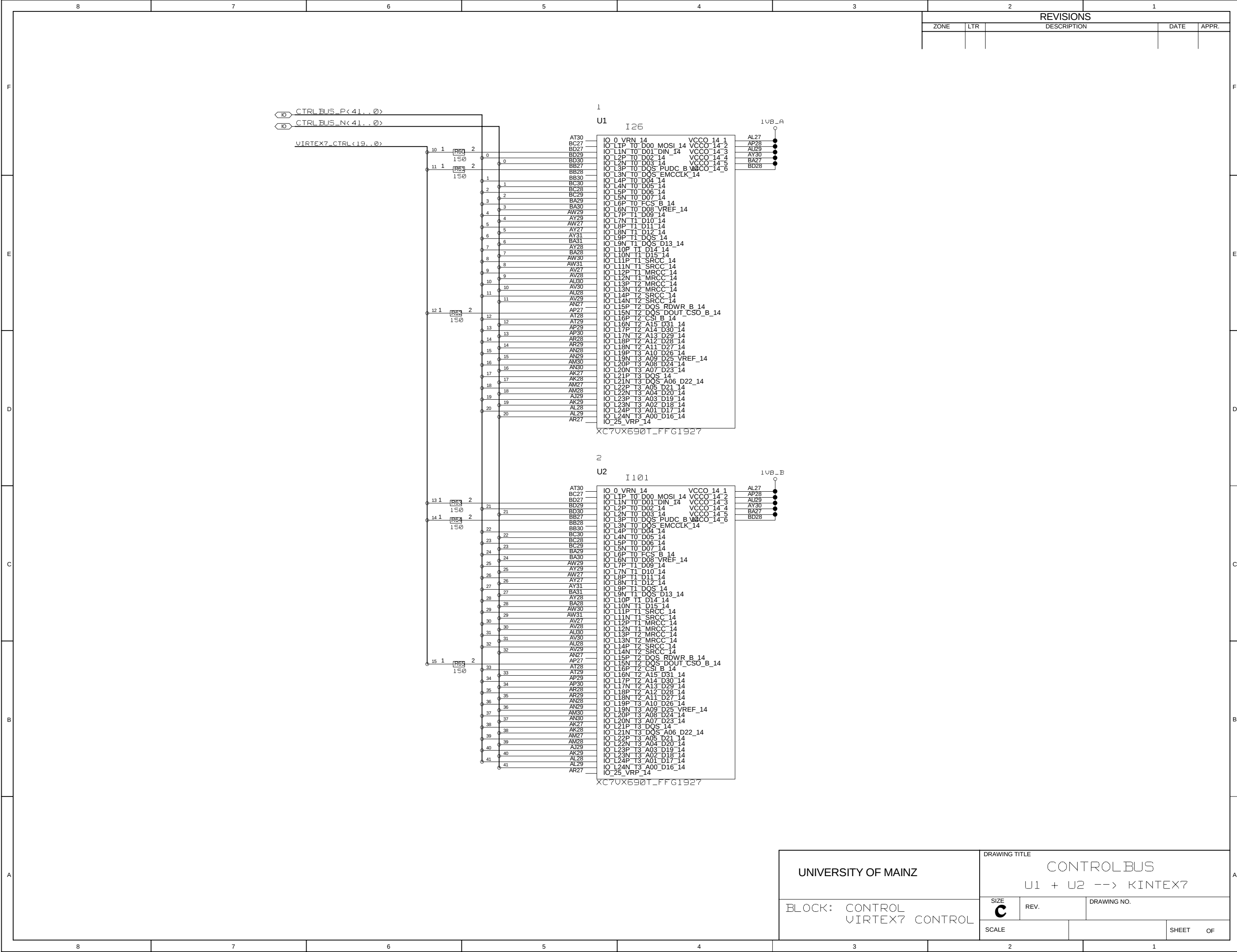


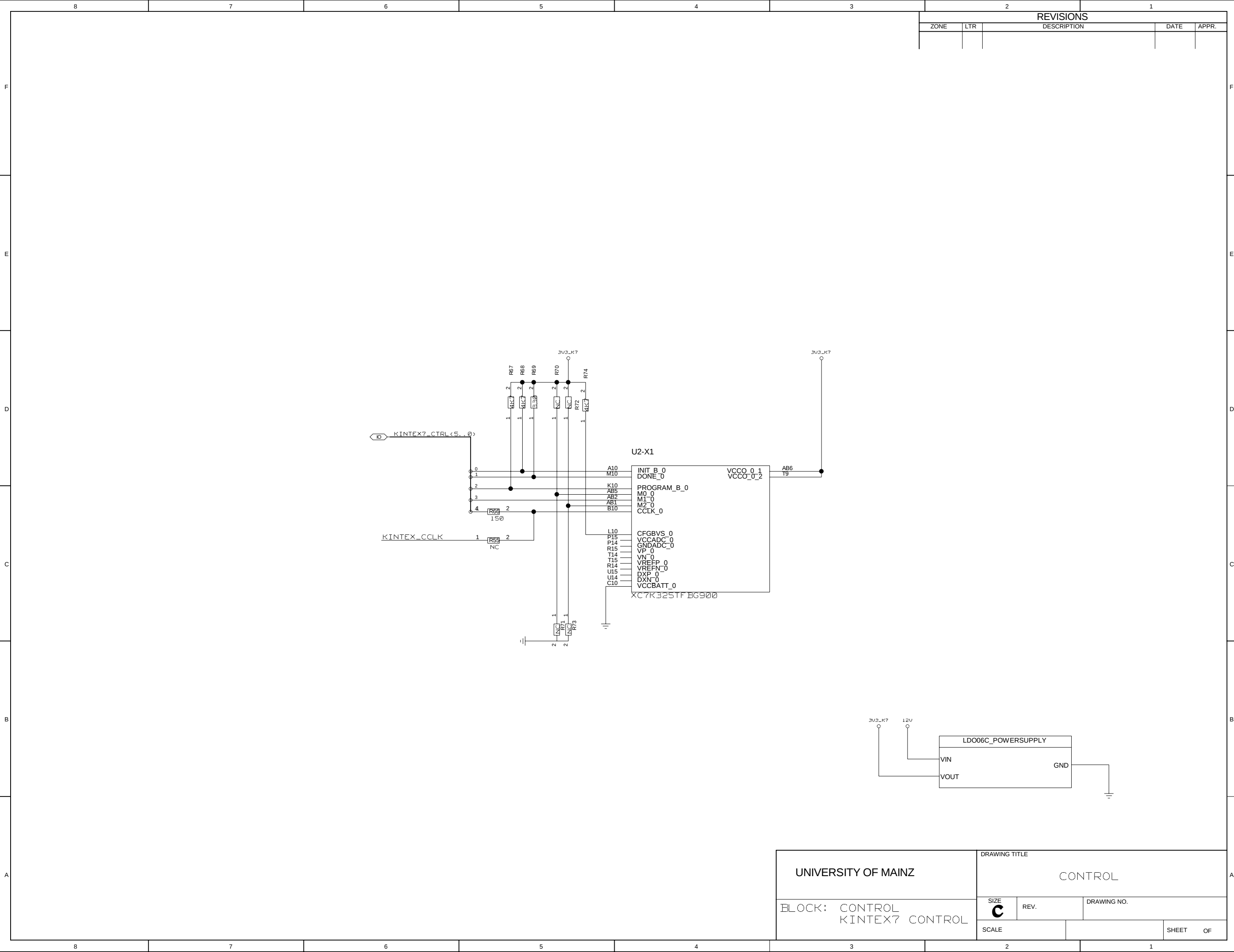


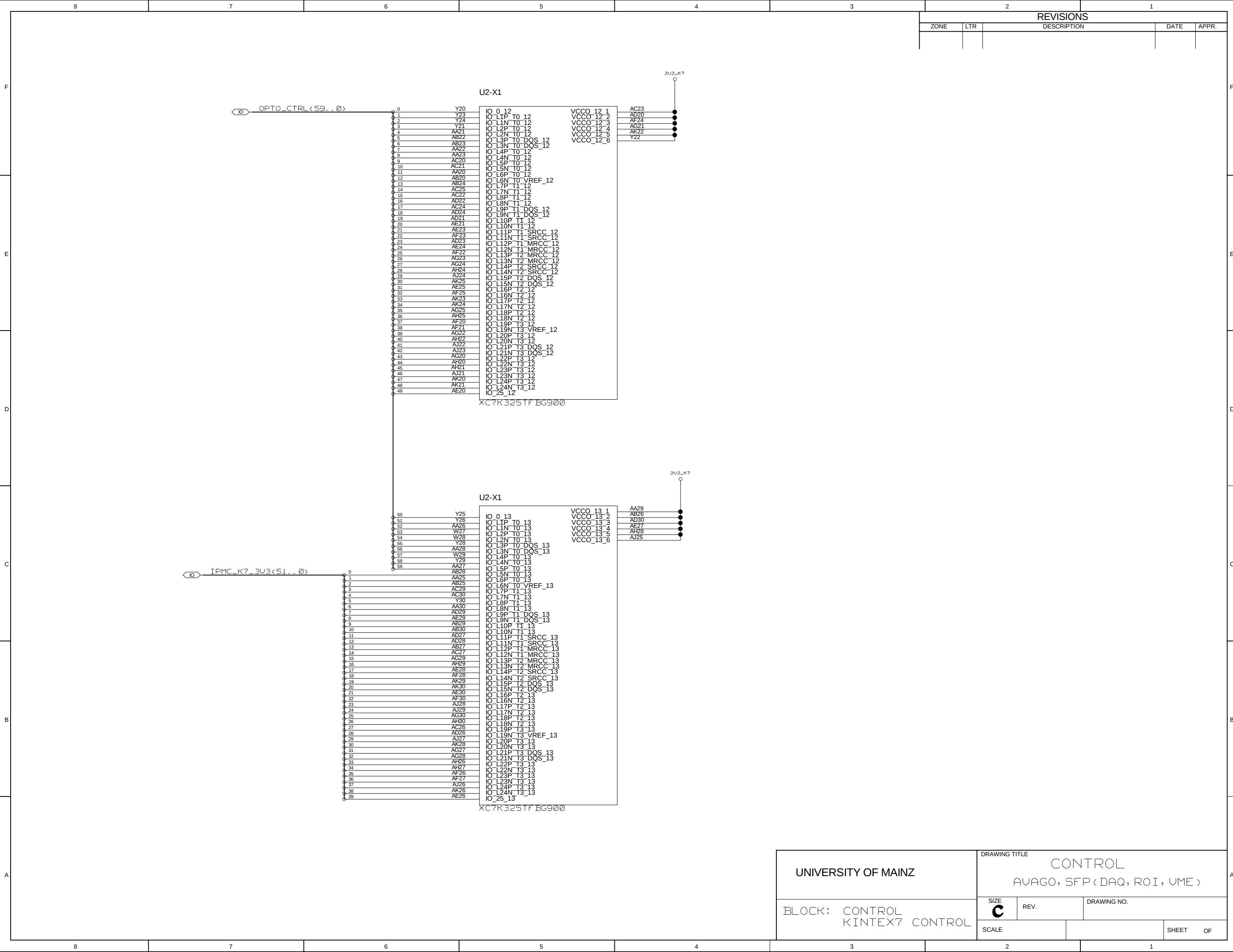














TO EXTENSION MODUL

EXT_K7_P<23...0>

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IO 0 VRN 34

IO L1P T0 34

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IO L2P T0 34

IO L2N T0 34

IO L3P T0 DOS 34

IO L3N T0 DOS 34

IO L4P T0 34

IO L4N T0 34

IO L5P T0 34

IO L5N T0 34

IO L6P T0 34

IO L6N T0 VREF 34

IO L7P T1 34

IO L7N T1 34

IO L8P T1 34

IO L8N T1 34

IO L9P T1 DOS 34

IO L9N T1 DOS 34

IO L10P T1 34

IO L10N T1 34

IO L11P T1 SRCC 34

IO L11N T1 SRCC 34

IO L12P T1 MRCC 34

IO L12N T1 MRCC 34

IO L13P T2 MRCC 34

IO L13N T2 MRCC 34

IO L14P T2 SRCC 34

IO L14N T2 SRCC 34

IO L15P T2 DOS 34

IO L15N T2 DOS 34

IO L16P T2 34

IO L16N T2 34

IO L17P T2 34

IO L17N T2 34

IO L18P T2 34

IO L18N T2 34

IO L19P T3 34

IO L19N T3 34

IO L19N T3 VREF 34

IO L20P T3 34

IO L20N T3 34

IO L21P T3 DOS 34

IO L21N T3 DOS 34

IO L22P T3 34

IO L22N T3 34

IO L23P T3 34

IO L23N T3 34

IO L24P T3 34

IO L24N T3 34

IO L25 VRP 34

VCCO 34 1

VCCO 34 2

VCCO 34 3

VCCO 34 4

VCCO 34 5

VCCO 34 6

VCCO 34 7

AC3

AE7

AF4

AG1

AH8

AJ5

AK2

IUB_C

XC7K1325T_FBG900?

U32

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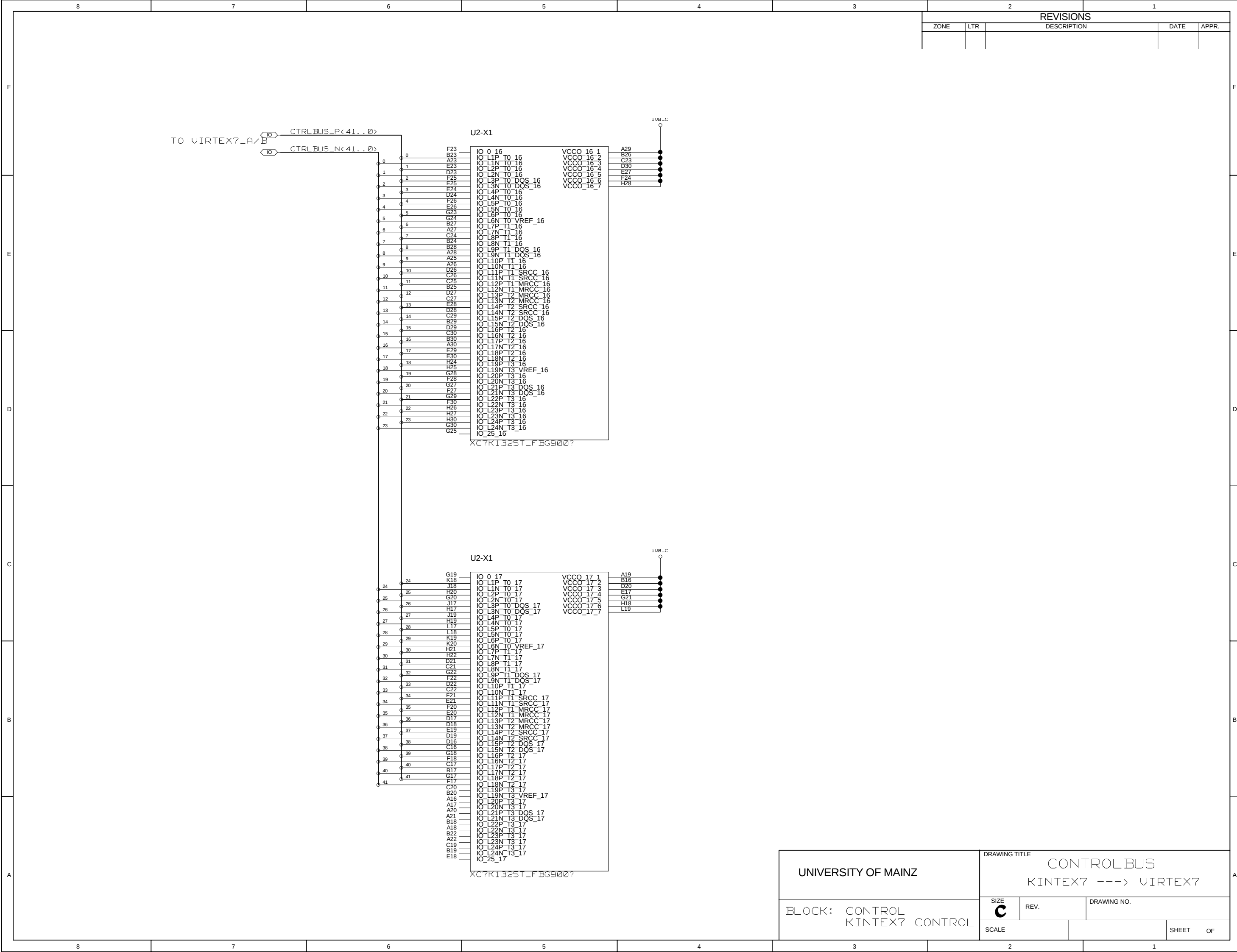
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CONTROLBUS KINTEX7 ---> VIRTEX7

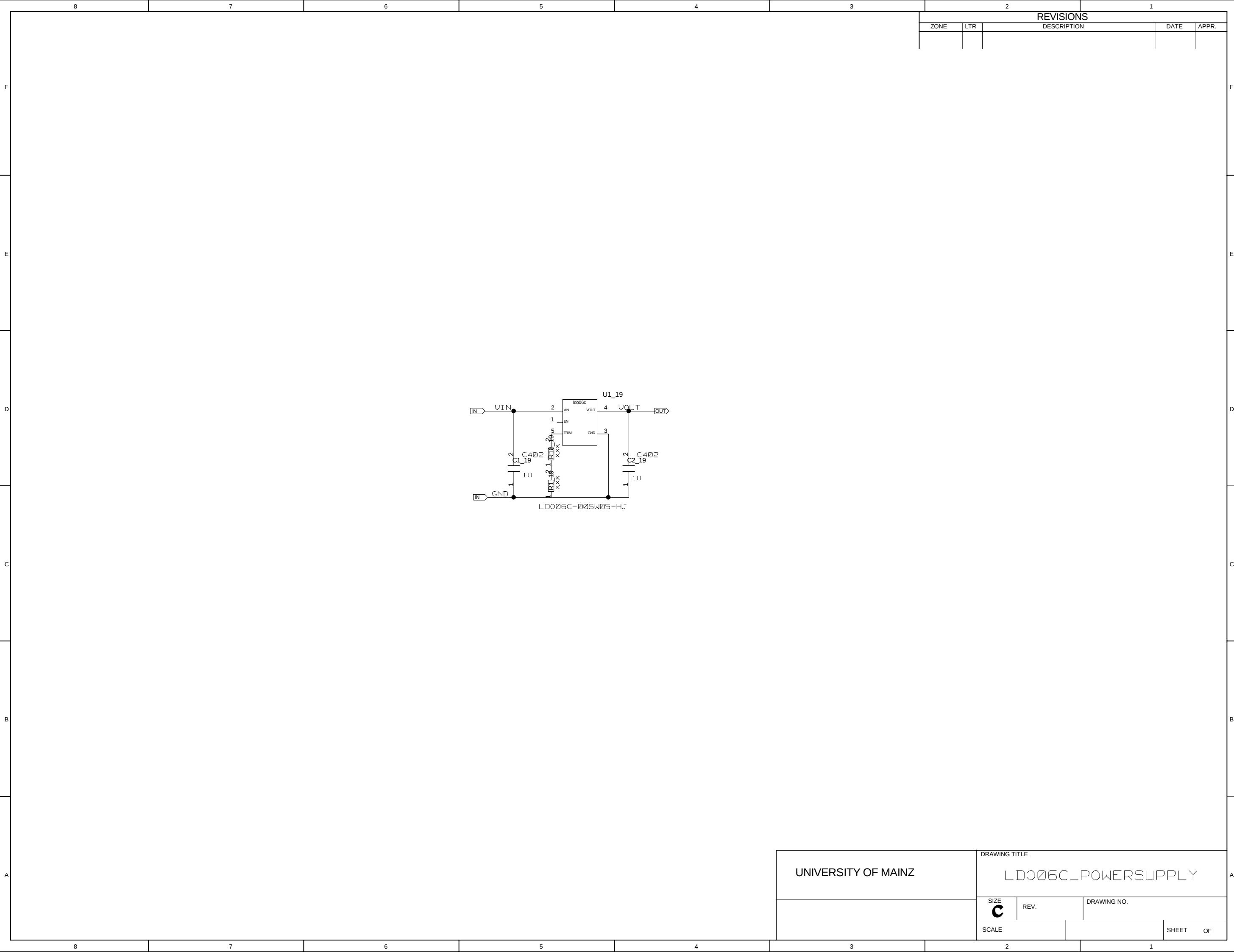
SIZE

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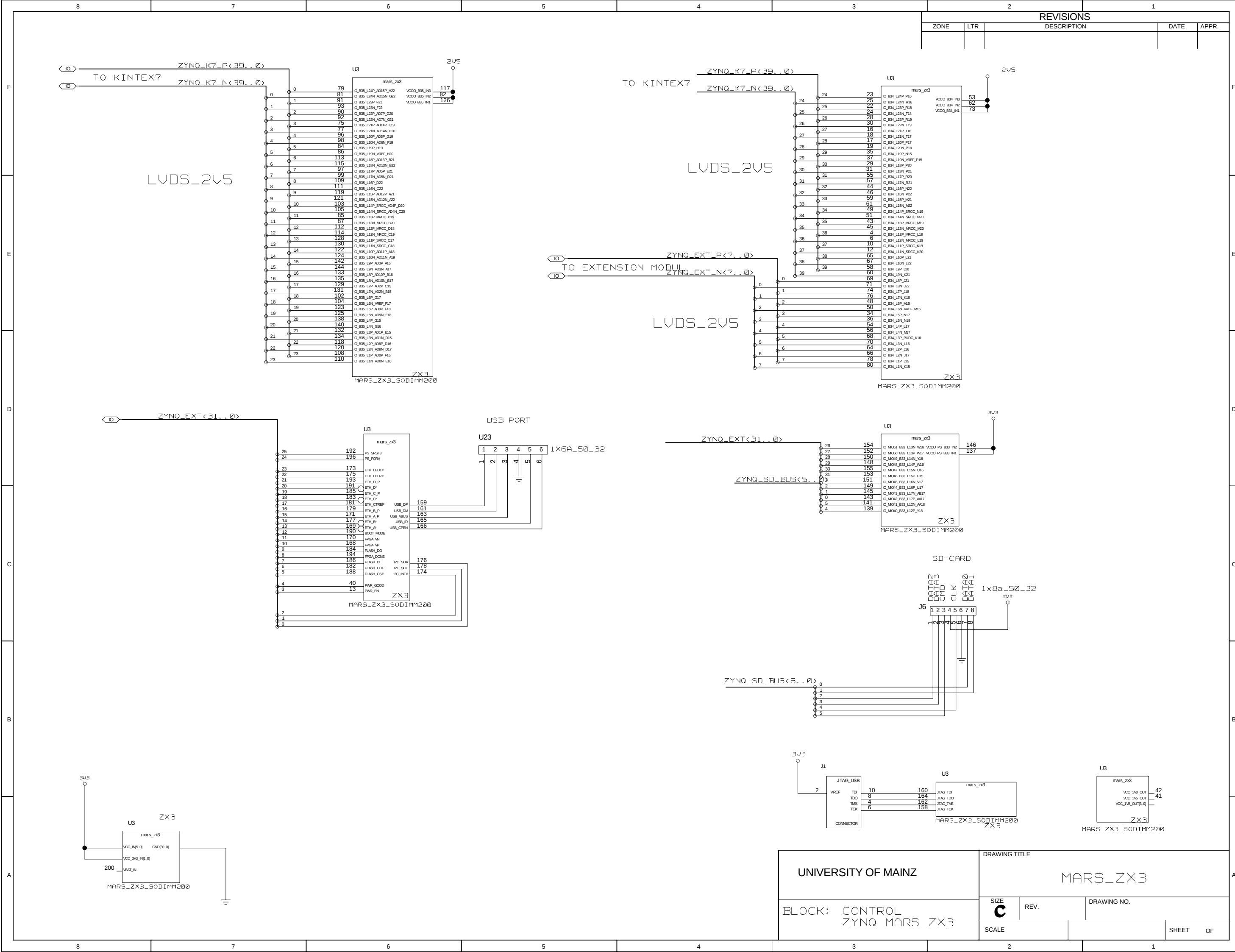
DRAWING NO.

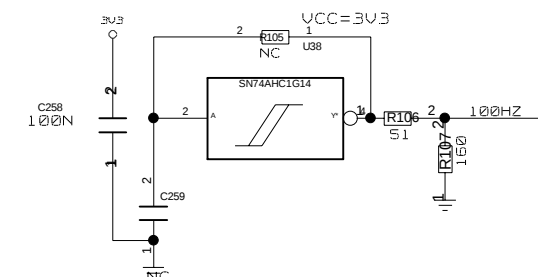
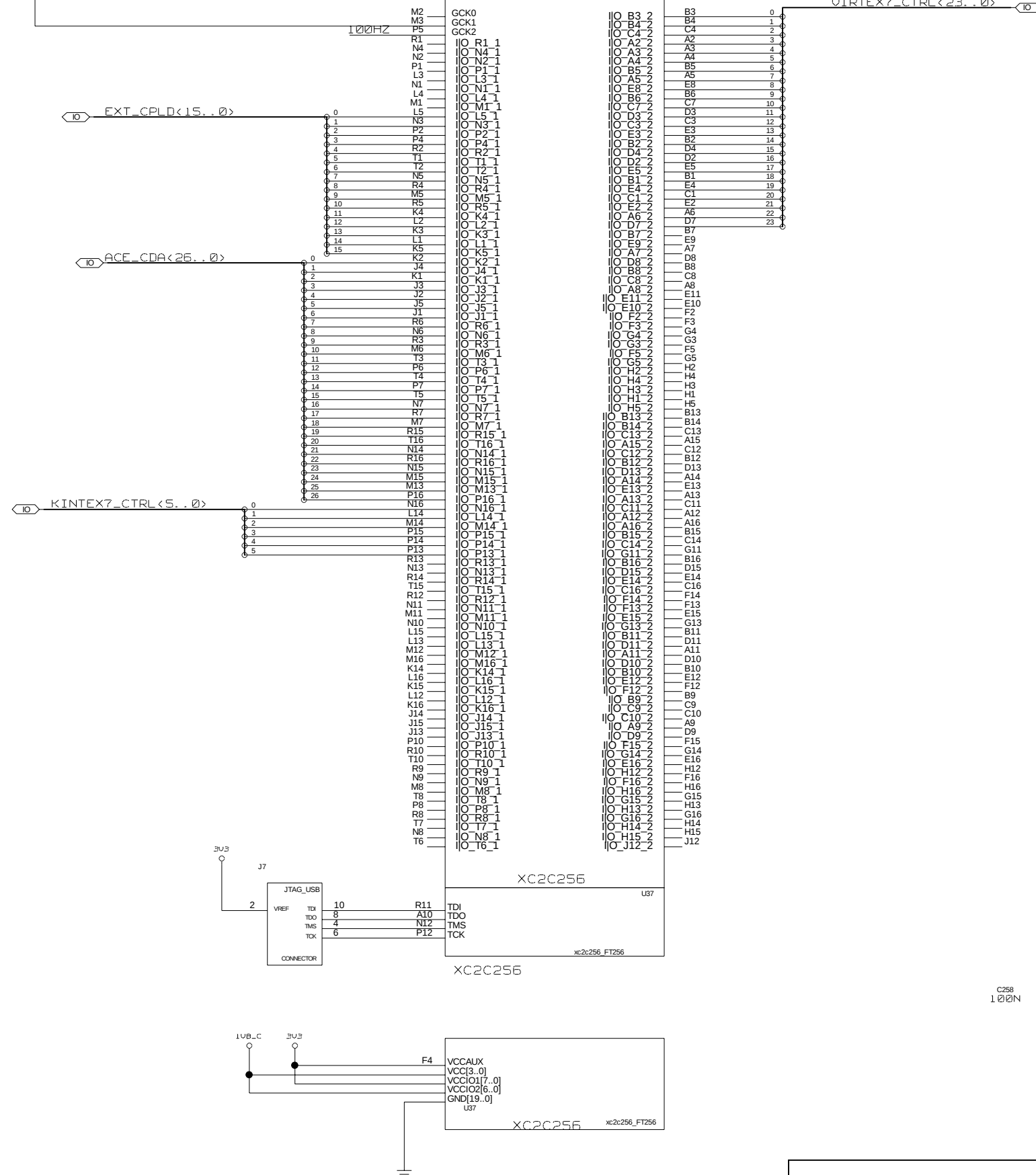
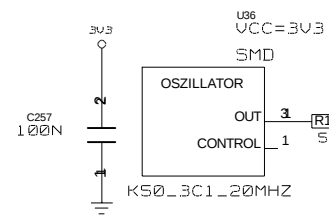
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