

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

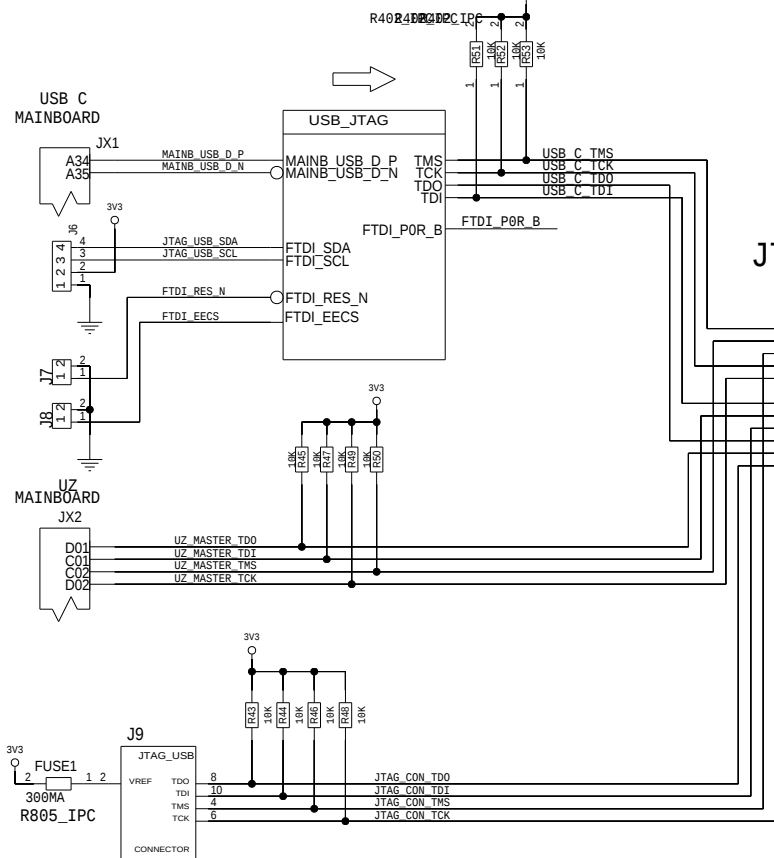
FFEX CONTROL MEZZANINE

INDEX PAGE(S) FUNCTION

PAGE2	JTAG
PAGE3	I2C-MULTIPLEXER, SERIAL ID
PAGE4	PHY IPBUS
PAGE5	PHY MAC/SGMII
PAGE6	SD CARD
PAGE7	POWER
PAGE8	FTDI

	NAME	DATE	SIGN	DRAWING TITLE FFEX CTRL_MEZZ B.BAUSS Mon Aug 11 08:44:56 2025		
CHECKED	U.SCHAEFER					
CHECKED						
CHECKED						
DRAWN	BAUSS			DRAWING NO.		
BLOCK TITLE ffex_jtag_testboard_v3						
UNIVERSITY OF MAINZ				SIZE C	REV.	PAGE 1 OF 8
				SCALE		

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.



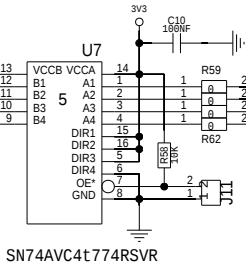
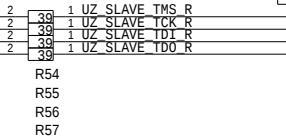
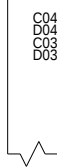
SN74AVC4T774

Table 9-1. Function Table (Each Bit)

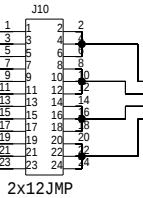
CONTROL INPUTS		OUTPUT CIRCUITS		OPERATION
OE	DIR	A PORT	B PORT	
L	L	Enabled	Hi-Z	B data to A data
L	H	Hi-Z	Enabled	A data to B data
H	X	Hi-Z	Hi-Z	Isolation

MAINBOARD TO UZ

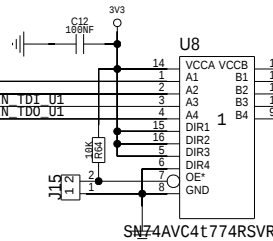
JX2



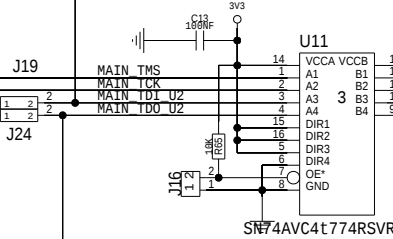
JTAG CHAIN SELECT



CHECK DIRECTION



B



U1

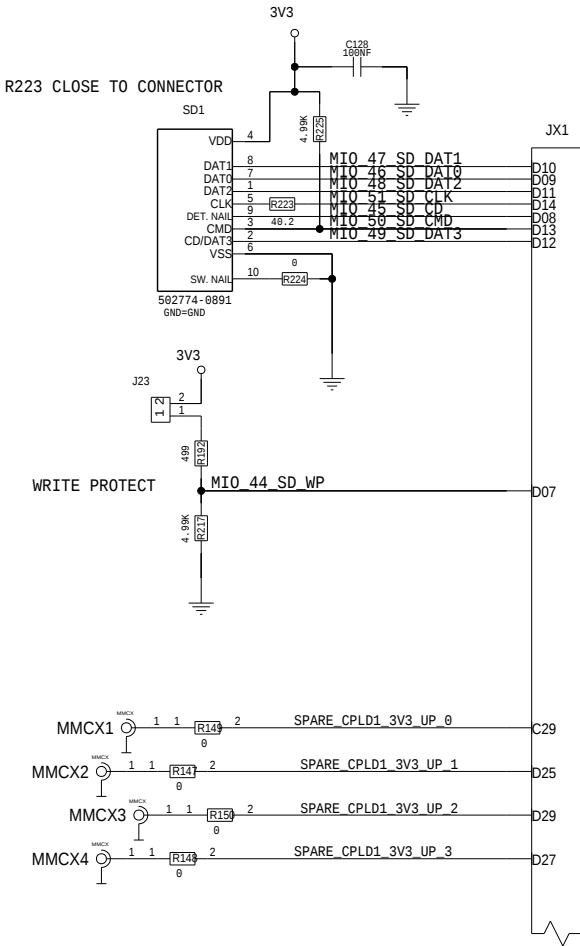
U2

NAME	DATE	SIGN	DRAWING TITLE	
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CHECKED		02.08.2022		
CHECKED				
DRAWN	BAUSS			
BLOCK TITLE			ffex_jtag_testboard_v3	
UNIVERSITY OF MAINZ			SIZE C	REV. DRAWING NO.
			SCALE	PAGE 2 OF 8

B.BAUSS Mon Aug 18 13:31:44 2025

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

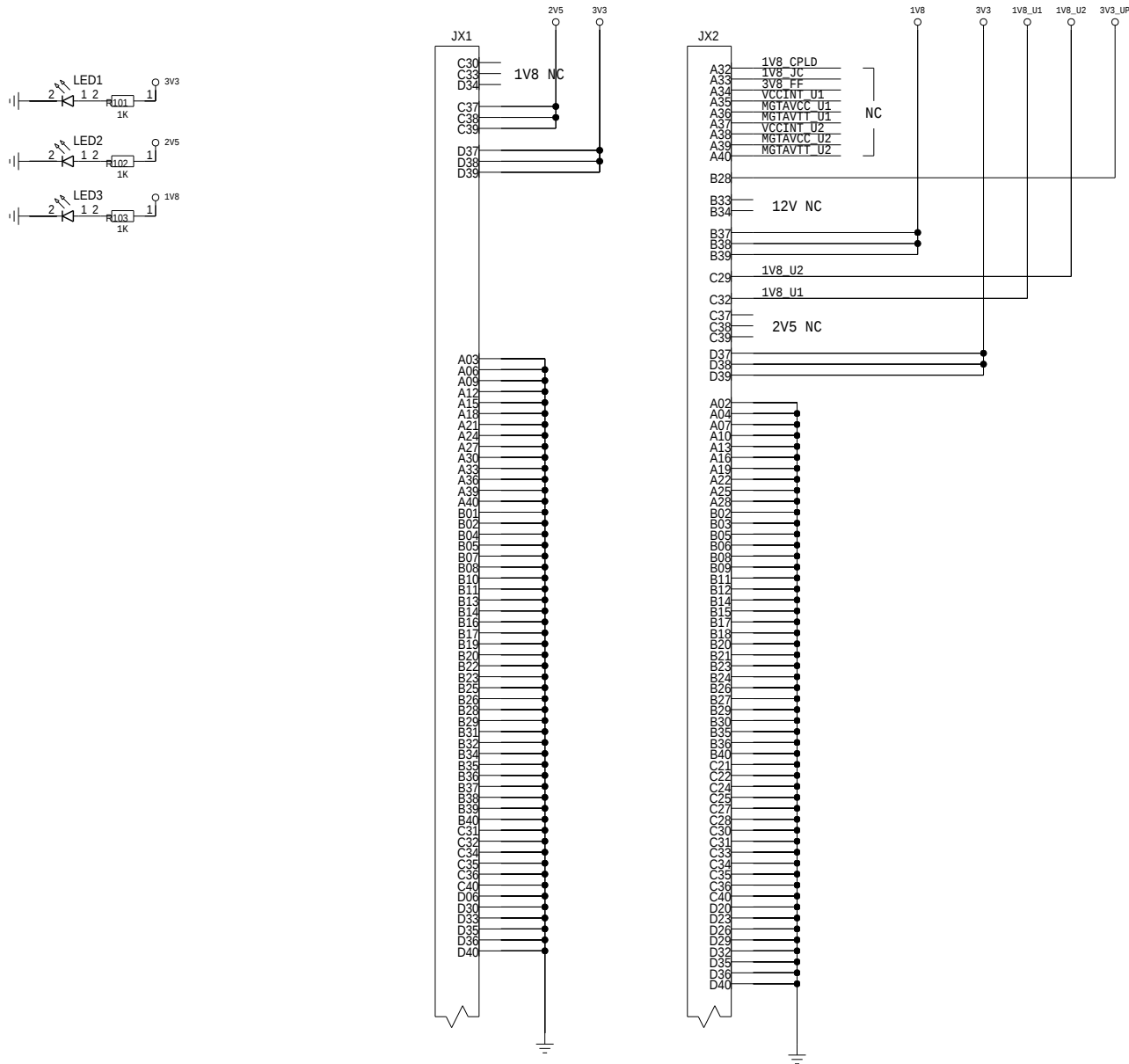
MICRO SD CARD



	NAME	DATE	SIGN	DRAWING TITLE FFEX CTRL_MEZZ B.BAUSS Mon Aug 18 13:31:45 2025	
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CHECKED					
CHECKED					
DRAWN	BAUSS			DRAWING NO.	
BLOCK TITLE ffex_jtag_testboard_v3				SIZE C	REV.
UNIVERSITY OF MAINZ				SCALE	PAGE 6 OF 8

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

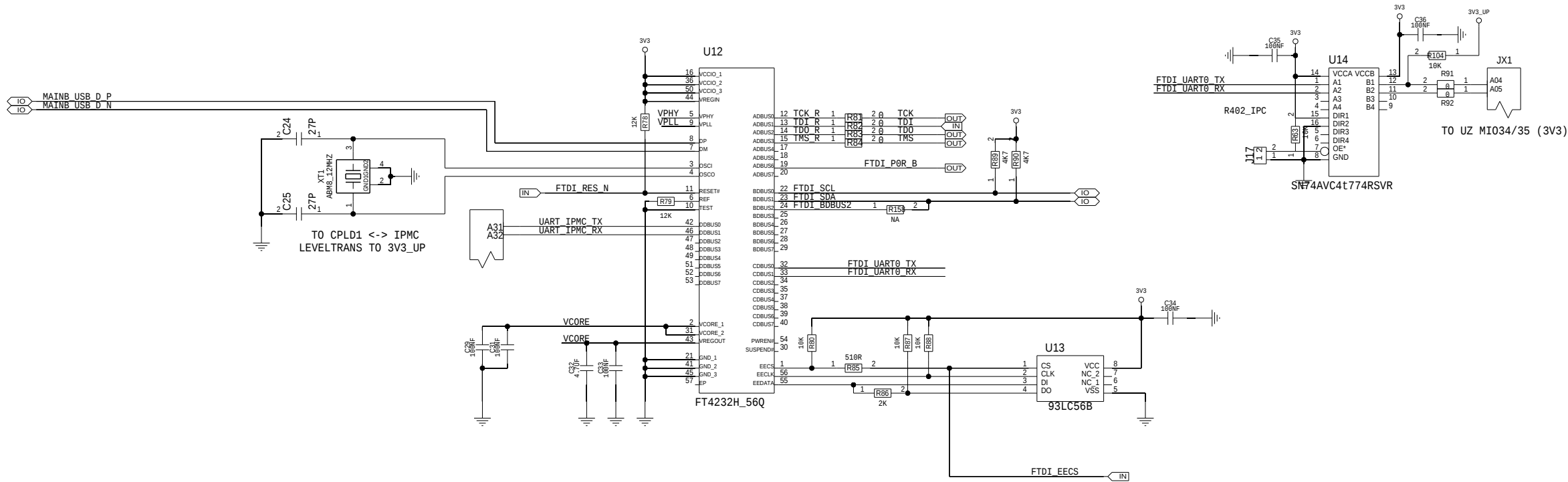
POWER CONNECTOR



	NAME	DATE	SIGN	DRAWING TITLE FFEX CTRL_MEZZ B.BAUSS Mon Aug 18 13:31:44 2025	
CHECKED	U.SCHAEFER				
CHECKED					
CHECKED					
DRAWN	BAUSS			DRAWING NO.	
BLOCK TITLE ffex_jtag_testboard_v3				SIZE C	PAGE 7 OF 8
UNIVERSITY OF MAINZ				SCALE	

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

JTAG FTDI



POWERED_VCCIO

