| REVISIONS | | | | |
|---|---|---|---|---|
| ZONE | LTR | DESCRIPTION | DATE | APPR. |

DEL1.11
I299
AMZ-DELAY
IN
OUT20
OUT40
OUT60
OUT80
OUT100
RHOMBUS
CONTRCLK-E
BUF-M-INPUTE
BUF-CCLK-E
PROGRAM-E
DONE-E
INIT-E
DIN-E
BUF-WRITE-E
CE-E
OUTPUT-TRI-E
TMS-E
TCK-E
J1.11
I22
R603
VCC_2V5
GND
R4.11
R3.11
R2.11
R1.11
J2.11
SAMTEC-SFMC-107
I265
LED
8XLOCK
3XCONTROL
LAENGE AN JE-BUS ANPASSEN
(JET-FPGA)
R5.11
E-WR-STRB
E-CE
E-WR
TTC-DAT0(1)
TTC-DAT1(1)
TTC-DAT2(1)
TTC-DAT3(1)
TTC-DAT4(1)
TTC-DAT5(1)
TTC-DAT6(1)
TTC-DAT7(1)
E-DAQ1
E-DAQ2
E-ROC1
E-ROC2
E-RDO(11..0)
D-RDO(11..0)
R-SIL4.11
I148
VSSR_SSOP20
80MHZ
JB4
B-JE1(4..0)
B-JE2(4..0)
B-JE3(4..0)
B-JE4(4..0)
J-JE1(4..0)
J-JE2(4..0)
J-JE3(4..0)
J-JE4(4..0)
R-SIL5.11
I145
FOR VERTEX-E (VREF)
VIRTEX 2.5V
XCV150-456
INPUT-FPGA
E
I144
IC1.11
VCCINT=VCC_2V5
VCCO_B0=VCC_2V5
VCCO_B1=VCC_2V5
VCCO_B2=VCC_2V5
VCCO_B3=VCC_2V5
VCCO_B4=VCC_2V5
VCCO_B5=VCC_2V5
VCCO_B6=VCC_2V5
VCCO_B7=VCC_2V5
GND

IC2.11
IE4E_PWRDN
IE4E_REN
IC3.11
IE4E_RCLK
IE4E_LOCK
IE4E_PWRDN
IE4E_REN
IC4.11
IE3E_PWRDN
IE3E_REN
IC5.11
IE2E_PWRDN
IE2E_REN
IC6.11
IE1E_PWRDN
IE1E_REN
IC7.11
IE4H_PWRDN
IE4H_REN
IC8.11
IE3H_PWRDN
IE3H_REN
IC9.11
IE2H_PWRDN
IE2H_REN
IC10.11
IE1H_PWRDN
IE1H_REN
IC11.11
SN65LVDS9637A
E-REFCLK
E-REFCLK*
PLL-40-E
PLL-40-E*
JB4
R6.11
R7.11
R8.11
R9.11
R10.11
R11.11
R12.11
R13.11
R14.11
100
VCC_3V3
VCC_ANA
VCC_2V5
10UF
47UF
470UF

RESISTORARRAY 100 VISHAY VSSR 1603
RESISTORARRAY 1K VISHAY VSSR 1603 FARNELL 309-9003

PINS U11 AND E11 FOR VIRTEX NOT USE !!!
PINS U11 AND E11 FOR VIRTEX-E NOT CONNECT
ALL CERAMIK CAPACITORS AVX X7R C0403
TANTALUM CAPACITORS AVX TPS 3528
TANTALUM CAPACITORS AVX TPS 6032

UNIVERSITY OF MAINZ

DRAWING TITLE: LVDS1-8_INPUT

| SIZE | REV. | DRAWING NO. |
|---|---|---|
| C | | |
| SCALE | 30.05.2000 | SHEET 7 OF 13 |