REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

DEL1.14
I299
AMZ-DELAY
IN
OUT20
OUT40
OUT60
OUT80
OUT100
RHOMBUS
J2.14
SAMTEC-SFMC-107
I265
LED
8XLOCK
3XCONTROL
LAENGE AN JH-BUS ANPASSEN
(JET-FPGA)
VCC_2V5
GND
R5.14
H-WR-STRB
H-CE
H-WR
TTC-DAT0(1)
TTC-DAT1(1)
TTC-DAT2(1)
TTC-DAT3(1)
TTC-DAT4(1)
TTC-DAT5(1)
TTC-DAT6(1)
TTC-DAT7(1)
H-DAQ1
H-DAQ2
H-ROC1
H-ROC2
H-RDO(11..0)
G-RDO(11..0)
R-SIL4.14
I148
VSSR_SSOP20
80MHZ
JB7
JB6
B-JH1(0)
B-JH1(1)
B-JH1(2)
B-JH1(3)
B-JH1(4)
B-JH2(0)
B-JH2(1)
B-JH2(2)
B-JH2(3)
B-JH2(4)
B-JH4(0)
B-JH4(1)
B-JH4(2)
B-JH4(3)
B-JH4(4)
B-JH3(0)
B-JH3(1)
B-JH3(2)
B-JH3(3)
B-JH3(4)
B-JH3(4..0)
R-SIL4.14
I146
R-SIL5.14
I145
J-JH1(4..0)
J-JH2(4..0)
J-JH3(4..0)
J-JH4(4..0)
R-SIL5.14
I144
I/O223 AA22
I/O222 AB15
I/O221 AA20
I/O220 AA15
I/O219 AB20
I/O218 AB17
I/O217 AA19
I/O216 AA17
I/O215 AB19
I/O214 AB16
I/O213 Y18
I/O212 A17
I/O211 C18
I/O210 F20
I/O209 A16
I/O208 F12
I/O207 E20
I/O206 E16
I/O205 E15
I/O204 E22
I/O203 F19
I/O202 U20
I/O201 F18
I/O200 D17
I/O199 C17
I/O198 D16
I/O197 C16
I/O196 D15
I/O195 C15
I/O194 D14
I/O193 C14
I/O192 D12
I/O191 D11
I/O190 E12
I/O189 E11
I/O188 C12
I/O187 B8
I/O186 A8
I/O185 B13
I/O184 B12
I/O183 B15
I/O182 A10
I/O181 B14
I/O180 A9
I/O179 B10
I/O178 B9
I/O177 A15
I/O176 A13
I/O175 Y9
I/O174 AB9
I/O173 Y14
I/O172 AB13
I/O171 AA15
I/O170 AB11
I/O169 AA14
I/O168 AB10
I/O167 AA12
I/O166 Y10
I/O165 AB15
L17
L18
L20
J18
J19
J20
H19
H18
G20
G19
T18
T19
R18
R19
P18
P19
N18
N19
M18
M19
F21
F22
G21
J21
J22
K21
K22
L21
L22
M20
M22
N20
N21
P21
P20
T21
U22
U21
V22
D21
D13
D10
C22
C18
A7
C13
C10
B19
D22
A19
B18
E14
FOR VERTEX-E (VREF)
VIRTEX 2.5V
XCV150-456
INPUT-FPGA
H
I144
IC1.14
VCCINT=VCC_2V5
VCCO_B0=VCC_2V5
VCCO_B1=VCC_2V5
VCCO_B2=VCC_2V5
VCCO_B3=VCC_2V5
VCCO_B4=VCC_2V5
VCCO_B5=VCC_2V5
VCCO_B6=VCC_2V5
VCCO_B7=VCC_2V5
GND
CONTRCLK-H
BUF-M-INPUTH
BUF-CCLK-H
PROGRAM-H
DONE-H
INIT-H
DIN-H
BUF-WRITE-H
CE-H
INPUTG-TIO
INPUTH-TIO
TMS-B
TCK-B
J1.14
I22
R603
R4.14
R3.14
R2.14
R1.14
VCC_2V5
GND
CCK0 W12
CCK1 Y11
CCK2 A11
CCK3 AB2
M0 U5
M1 Y4
M2 AB3
CCLK W20
PROGRAM Y19
DONE V19
INIT* U19
BUSY/DOUT U20
D0 T20
D1 M22
D2 M20
D3 N22
D4 N20
D5 R21
D6 T22
D7 P20
WRITE C19
CS* E20
TDI D21
TDO E21
TMS B5
TCK C4
DXN Y5
DXP V6
I/O00 Y2 IH4E<0>
I/O01 W3 IH4E<1>
I/O02 AB3 IH4E<2>
I/O03 AA4 IH4E<3>
I/O04 AB4 IH4E<4>
I/O05 AA5 IH4E<5>
I/O06 AB5 IH4E<6>
I/O07 AA6 IH4E<7>
I/O08 AB6 IH4E<8>
I/O09 AA7 IH4E<9>
I/O010 Y7 IH4E_RCLK
I/O011 AA8 IH4E_LOCK
I/O012 W14 IH4E_PWRDN
I/O013 W14 IH4E_REN
I/O014 P2 IH3E<0>
I/O015 R1 IH3E<1>
I/O016 R2 IH3E<2>
I/O017 T1 IH3E<3>
I/O018 T2 IH3E<4>
I/O019 U1 IH3E<5>
I/O020 U2 IH3E<6>
I/O021 V1 IH3E<7>
I/O022 V2 IH3E<8>
I/O023 W1 IH3E<9>
I/O024 W2 IH3E_RCLK
I/O025 Y1 IH3E_LOCK
I/O026 V12 IH3E_PWRDN
I/O027 U12 IH3E_REN
I/O028 G1 IH2E<0>
I/O029 H2 IH2E<1>
I/O030 H1 IH2E<2>
I/O031 J2 IH2E<3>
I/O032 J3 IH2E<4>
I/O033 K1 IH2E<5>
I/O034 K3 IH2E<6>
I/O035 L1 IH2E<7>
I/O036 L3 IH2E<8>
I/O037 M1 IH2E<9>
I/O038 M3 IH2E_RCLK
I/O039 N2 IH2E_LOCK
I/O040 V13 IH2E_PWRDN
I/O041 V13 IH2E_REN
I/O042 E4 IH1E<0>
I/O043 A4 IH1E<1>
I/O044 A3 IH1E<2>
I/O045 B3 IH1E<3>
I/O046 B1 IH1E<4>
I/O047 C1 IH1E<5>
I/O048 D2 IH1E<6>
I/O049 E2 IH1E<7>
I/O050 E1 IH1E<8>
I/O051 F2 IH1E<9>
I/O052 F1 IH1E_RCLK
I/O053 G3 IH1E_LOCK
I/O054 V11 IH1E_PWRDN
I/O055 U11 IH1E_REN
I/O056 W9 IH4H<0>
I/O057 Y8 IH4H<1>
I/O058 V9 IH4H<2>
I/O059 W8 IH4H<3>
I/O060 V8 IH4H<4>
I/O061 W7 IH4H<5>
I/O062 V7 IH4H<6>
I/O063 Y6 IH4H<7>
I/O064 W6 IH4H<8>
I/O065 W5 IH4H<9>
I/O066 W10 IH4H_RCLK
I/O067 V10 IH4H_LOCK
I/O068 E9 IH4H_PWRDN
I/O069 E10 IH4H_REN
I/O070 U4 IH3H<0>
I/O071 T3 IH3H<1>
I/O072 T4 IH3H<2>
I/O073 T5 IH3H<3>
I/O074 R4 IH3H<4>
I/O075 R5 IH3H<5>
I/O076 P3 IH3H<6>
I/O077 P4 IH3H<7>
I/O078 N3 IH3H<8>
I/O079 N4 IH3H<9>
I/O080 V3 IH3H_RCLK
I/O081 U4 IH3H_LOCK
I/O082 E7 IH3H_PWRDN
I/O083 D8 IH3H_REN
I/O084 M6 IH2H<0>
I/O085 L4 IH2H<1>
I/O086 L5 IH2H<2>
I/O087 K4 IH2H<3>
I/O088 K5 IH2H<4>
I/O089 J5 IH2H<5>
I/O090 L6 IH2H<6>
I/O091 H3 IH2H<7>
I/O092 H4 IH2H<8>
I/O093 G4 IH2H<9>
I/O094 N5 IH2H_RCLK
I/O095 M5 IH2H_LOCK
I/O096 E8 IH2H_PWRDN
I/O097 D9 IH2H_REN
I/O098 F3 IH1H<0>
I/O099 F4 IH1H<1>
I/O0100 F5 IH1H<2>
I/O0101 E3 IH1H<3>
I/O0102 E4 IH1H<4>
I/O0103 E6 IH1H<5>
I/O0104 D5 IH1H<6>
I/O0105 D6 IH1H<7>
I/O0106 C5 IH1H<8>
I/O0107 G6 IH1H<9>
I/O0108 H5 IH1H_RCLK
I/O0109 G5 IH1H_LOCK
I/O0110 D7 IH1H_PWRDN
I/O0111 C7 IH1H_REN
I/O0223 AA22
I/O0222 AB15
I/O0221 AA20
I/O0220 AA15
I/O0219 AB20
I/O0218 AB17
I/O0217 AA19
I/O0216 AA17
I/O0215 AB19
I/O0214 AB16
I/O0213 Y18
I/O0212 A17
I/O0211 C18
I/O0210 F20
I/O0209 A16
I/O0208 F12
I/O0207 E20
I/O0206 E16
I/O0205 E15
I/O0204 E22
I/O0203 F19
I/O0202 U20
I/O0201 F18
I/O0200 D17
I/O0199 C17
I/O0198 D16
I/O0197 C16
I/O0196 D15
I/O0195 C15
I/O0194 D14
I/O0193 C14
I/O0192 D12
I/O0191 D11
I/O0190 E12
I/O0189 E11
I/O0188 C12
I/O0187 B8
I/O0186 A8
I/O0185 B13
I/O0184 B12
I/O0183 B15
I/O0182 A10
I/O0181 B14
I/O0180 A9
I/O0179 B10
I/O0178 B9
I/O0177 A15
I/O0176 A13
I/O0175 Y9
I/O0174 AB9
I/O0173 Y14
I/O0172 AB13
I/O0171 AA15
I/O0170 AB11
I/O0169 AA14
I/O0168 AB10
I/O0167 AA12
I/O0166 Y10
I/O0165 AB15
I/O0164 L17
I/O0163 L18
I/O0162 L20
I/O0161 J18
I/O0160 J19
I/O0159 J20
I/O0158 H19
I/O0157 H18
I/O0156 G20
I/O0155 G19
I/O0154 T18
I/O0153 T19
I/O0152 R18
I/O0151 R19
I/O0150 P18
I/O0149 P19
I/O0148 N18
I/O0147 N19
I/O0146 M18
I/O0145 M19
I/O0144 F21
I/O0143 F22
I/O0142 G21
I/O0141 J21
I/O0140 J22
I/O0139 K21
I/O0138 K22
I/O0137 L21
I/O0136 L22
I/O0135 M20
I/O0134 M22
I/O0133 N20
I/O0132 N21
I/O0131 P21
I/O0130 P20
I/O0129 T21
I/O0128 U22
I/O0127 U21
I/O0126 V22
I/O0125 D21
I/O0124 D13
I/O0123 D10
I/O0122 C22
I/O0121 C18
I/O0120 A7
I/O0119 C13
I/O0118 C10
I/O0117 B19
I/O0116 D22
I/O0115 A19
I/O0114 B18
I/O0113 E14
I/O0112
I/O-VREF-B0 B5
I/O-VREF-B1 A6
I/O-VREF-B2 B6
I/O-VREF-B3 A18
I/O-VREF-B4 B7
I/O-VREF-B5 T9
I/O-VREF-B6 U9
I/O-VREF-B7 U13
U15
V15
V16
V17
V20
V21
W11
W14
W17
W18
W21
Y12
Y14
Y15
Y16
Y17
Y18
Y5
A5
B17
GND
IC2.14
IH4E_PWRDN
IH4E_REN
IH3E_PWRDN
IH3E_REN
IH2E_PWRDN
IH2E_REN
IH1E_PWRDN
IH1E_REN
GND
VCC_3V3
IC3.14
IC4.14
IC5.14
IC6.14
R6.14
R7.14
R8.14
R9.14
JBS
DS92LV1210
DIN
DIN*
RCLK
LOCK*
PWRDN*
REN
REFCLK
RCLK_R/F
IC12.14
IH4H_PWRDN
IH4H_REN
IH3H_PWRDN
IH3H_REN
IH2H_PWRDN
IH2H_REN
IH1H_PWRDN
IH1H_REN
IC7.14
IC8.14
IC9.14
IC10.14
R10.14
R11.14
R12.14
R13.14
IH4H<0> D12
IH4H<1> E12
IH3H<0> D14
IH3H<1> E14
IH2H<0> D16
IH2H<1> E16
IH1H<0> D18
IH1H<1> E18
IH4H_RCLK
IH4H_LOCK
IH3H_RCLK
IH3H_LOCK
IH2H_RCLK
IH2H_LOCK
IH1H_RCLK
IH1H_LOCK
H-REFCLK
H-REFCLK*
IC11.14
SN65LVDS9637A
R14.14
PLL-40-H
PLL-40-H*
10UF
47UF
470UF
VCC_2V5
VCC_ANA
C46.14
C48.14
C4.14
C47.14
C5.14
C49.14
C2.14
C50.14
C53.14
C14.14
C51.14
C62.14
C55.14
C3.14
C23.14
C22.14
C52.14
C6.14
C15.14
C58.14
C64.14
C16.14
C12.14
C51.14
C7.14
C59.14
C60.14
C63.14
C17.14
C70.14
C13.14
C26.14
C27.14
C28.14
C29.14
C30.14
C31.14
C44.14
C32.14
C33.14
C35.14
C36.14
C37.14
C38.14
C45.14
C39.14
C40.14
C41.14
C42.14
C25.14
C24.14
C1.14
C9.14
C54.14
C8.14
C55.14
C18.14
C56.14
C11.14
C57.14
C10.14
C19.14
C68.14
C69.14
C66.14
C67.14
C20.14
C21.14

RESISTORARRAY 100 VISHAY VSSR 1603
RESISTORARRAY 1K VISHAY VSSR 1603 FARNELL 309-9003

PINS U11 AND E11 FOR VIRTEX NOT USE !!!
PINS U11 AND E11 FOR VIRTEX-E NOT CONNECT
ALL CERAMIH CAPACITORS AVX X7R C0403
TANTALUM CAPACITORS AVX TPS 3528
TANTALUM CAPACITORS AVX TPS 6032

UNIVERSITY OF MAINZ

DRAWING TITLE: LVDS1-8_INPUT

SIZE	REV.	DRAWING NO.
C		
SCALE	30.05.2000	SHEET 7 OF 13