REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

DEL1.15

I302 AMZ-DELAY
IN
OUT20
OUT40
OUT60
OUT80
OUT100
RHOMBUS

CONTRCLK-J
BUF-M-INPUT
BUF-CCLK-J
PROGRAM-J
DONE-J
INIT-J
DIN-J
BUF-WRITE-J
CE-J
BUSY/DOUT
TMS-B
TCK-B

J1.15 I22

R4.15 R3.15 R2.15 R1.15 VCC_2V5 GND R603

J2.15
SAMTEC-SFMC-107 I266
LED 8XLOCK 3XCONTROL
VCC_2V5 GND

LAENGE AN JJ-BUS ANPASSEN (JET-FPGA)

R5.15 I145

J-WR-STRB J-CE J-WR
TTC-DAT0(2) TTC-DAT1(2) TTC-DAT2(2) TTC-DAT3(2) TTC-DAT4(2) TTC-DAT5(2) TTC-DAT6(2) TTC-DAT7(2)
J-DAQ1 J-DAQ2 J-ROC1 J-ROC2

J-RDO(11..0)
J-RDO(0) J-RDO(1) J-RDO(2) J-RDO(3) J-RDO(4) J-RDO(5) J-RDO(6) J-RDO(7) J-RDO(8) J-RDO(9) J-RDO(10) J-RDO(11)

H-RDO(11..0)
H-RDO(0) H-RDO(1) H-RDO(2) H-RDO(3) H-RDO(4) H-RDO(5) H-RDO(6) H-RDO(7) H-RDO(8) H-RDO(9) H-RDO(10) H-RDO(11)

R-SIL4.15 I148 80MHZ VSSR_SSOP20

JB7
B-JJ1(0) B-JJ1(1) B-JJ1(2) B-JJ1(3) B-JJ1(4) B-JJ2(0) B-JJ2(1) B-JJ2(2) B-JJ2(3) B-JJ2(4) B-JJ4(0) B-JJ4(1) B-JJ4(2) B-JJ4(3) B-JJ4(4)

JB8
B-JJ2(11..6) R-SIL4.15 I147
B-JJ3(0) B-JJ3(1) B-JJ3(2) B-JJ3(3) B-JJ3(4)

B-JJ3(4..0)
J-JJ1(0) J-JJ1(1) J-JJ1(2) J-JJ1(3) J-JJ1(4)

J-JJ1(4..0) J-JJ2(4..0) J-JJ3(4..0) J-JJ4(4..0)
R-SIL5.15 I146

GND

FOR VERTEX-E (VREF)

VIRTEX 2.5V

XCV150-456

INPUT-FPGA

J

I144

IC1.15

VCCINT=VCC_2V5

VCCO_B0=VCC_2V5
VCCO_B1=VCC_2V5
VCCO_B2=VCC_2V5
VCCO_B3=VCC_2V5
VCCO_B4=VCC_2V5
VCCO_B5=VCC_2V5
VCCO_B6=VCC_2V5
VCCO_B7=VCC_2V5

IC2.15 IC3.15 IC4.15 IC5.15 IC6.15 IC7.15 IC8.15 IC9.15 IC10.15 IC11.15 IC12.15

IJ4E_PWRDN IJ4E_REN IJ3E_PWRDN IJ3E_REN IJ2E_PWRDN IJ2E_REN IJ1E_PWRDN IJ1E_REN
IJ4H_PWRDN IJ4H_REN IJ3H_PWRDN IJ3H_REN IJ2H_PWRDN IJ2H_REN IJ1H_PWRDN IJ1H_REN

DS92LV1210 VCC_3V3 JB5 R6.15 R7.15 R8.15 R9.15 R10.15 R11.15 R12.15 R13.15 R14.15
T-REFCLK PLL-40-J PLL-40-J* SN65LVDS9637A J-REFCLK*

10UF 47UF 470UF VCC_ANA VCC_2V5

RESISTORARRAY 100 VISHAY VSSR 1603
RESISTORARRAY 1K VISHAY VSSR 1603 FARNELL 309-9003

PINS U11 AND E11 FOR VIRTEX NOT USE !!!
PINS U11 AND E11 FOR VIRTEX-E NOT CONNECT
ALL CERAMIJ CAPACITORS AVX X7R C0403
TANTALUM CAPACITORS AVX TPS 3528
TANTALUM CAPACITORS AVX TPS 6032

UNIVERSITY OF MAINZ

DRAWING TITLE: LVDS1-8_INPUT

SIZE	REV.	DRAWING NO.
C		
SCALE	30.05.2000	SHEET 7 OF 13