| REVISIONS | | | | |
|---|---|---|---|---|
| ZONE | LTR | DESCRIPTION | DATE | APPR. |

DEL1.17

I298 AMZ-DELAY
IN
OUT20
OUT40
OUT60
OUT80
OUT100
RHOMBUS

CONTRACLK-L
VCC_2V5
GND
R4.17 R3.17 R2.17 R1.17

BUF-M-INPUTL
BUF-CCLK-L
PROGRAM-L
DONE-L
INIT-L
DIN-L
BUF-WRITE-L
CE-L
INPUTCTRL
TMS-B
TCK-B
J1.17 I22

J2.17
SAMTEC-SFMC-107
I265

LED
8XLOCK
3XCONTROL

LAENGE AN JL-BUS ANPASSEN
(JET-FPGA)

R5.17

L-WR-STRB
L-CE
L-WR
TTC-DAT0(2)
TTC-DAT1(2)
TTC-DAT2(2)
TTC-DAT3(2)
TTC-DAT4(2)
TTC-DAT5(2)
TTC-DAT6(2)
TTC-DAT7(2)
L-DAQ1
L-DAQ2
L-ROC1
L-ROC2

L-RDO(11..0)
K-RDO(11..0)

R-SIL4.17 I148
80MHZ VSSR_SSOP20

JB8
B-JL1(4..0)
B-JL2(4..0)
B-JL3(4..0)
B-JL4(4..0)

R-SIL5.17 I145
J-JL1(4..0)
J-JL2(4..0)
J-JL3(4..0)
J-JL4(4..0)

FOR VERTEX-E (VREF)

VIRTEX 2.5V

XCV150-456

INPUT-FPGA

I144
IC1.17

VCCINT=VCC_2V5

VCCO_B0=VCC_2V5
VCCO_B1=VCC_2V5
VCCO_B2=VCC_2V5
VCCO_B3=VCC_2V5
VCCO_B4=VCC_2V5
VCCO_B5=VCC_2V5
VCCO_B6=VCC_2V5
VCCO_B7=VCC_2V5

GND

IC2.17 IC3.17 IC4.17 IC5.17 IC6.17 IC7.17 IC8.17 IC9.17 IC10.17 IC11.17 IC12.17

IL4E_PWRDN IL4E_REN IL3E_PWRDN IL3E_REN IL2E_PWRDN IL2E_REN IL1E_PWRDN IL1E_REN

IL4H_PWRDN IL4H_REN IL3H_PWRDN IL3H_REN IL2H_PWRDN IL2H_REN IL1H_PWRDN IL1H_REN

DS92LV1210
DIN DIN*
RCLK LOCK* PWRDN* REN REFCLK RCLK_R/F

SN65LVDS9637A
L-REFCLK L-REFCLK*
PLL-40-L PLL-40-L*

VCC_3V3
VCC_2V5
VCC_ANA

10UF 47UF 470UF

RESISTORARRAY 100 VISHAY VSSR 1603
RESISTORARRAY 1K VISHAY VSSR 1603 FARNELL 309-9003

PINS U11 AND E11 FOR VIRTEX NOT USE !!!
PINS U11 AND E11 FOR VIRTEX-E NOT CONNECT
ALL CERAMIL CAPACITORS AVX X7R C0403
TANTALUM CAPACITORS AVX TPS 3528
TANTALUM CAPACITORS AVX TPS 6032

UNIVERSITY OF MAINZ

DRAWING TITLE: LVDS1-8_INPUT

SIZE C | REV. | DRAWING NO.

SCALE | 30.05.2000 | SHEET 7 OF 13