REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

LED
J3.3
CONTROL-FPGA
SAMTEC-SFMC-107
I337

J2.3
I173
TP

J1.3
I195

CLK
FEETB-CLK1
QUABZCLK-CON
M0-3-CON
BUF-CCLK-CON
PROGRAM-CON
DONE-CON
INIT-CON
DIN-CON
BUF-WRITE-CON
CE-CON
SM-TDO
VME-TDO
TMS-CA
TCK-CA

GND
VCC_2V5

SHEET1
JTAG
CONT-TMS
CONT-TCK
CONT-TDI
CONT-TDO

VME-CONTROL<23..0>
SHEET1
VME_D_C<15..0>
TTC-J2<31..0>
SHEET4
TTCRX
CAN<3..0>

CONTROL-FPGA

VIRTEX 2.5V

XCV150-456

I145
IC1.3

VCCINT=VCC_2V5

VCCREF_B0=NC
VCCREF_B1=NC
VCCREF_B2=NC
VCCREF_B3=NC
VCCREF_B4=NC
VCCREF_B5=NC
VCCREF_B6=NC
VCCREF_B7=NC
VCCO_B0=VCC_2V5
VCCO_B1=VCC_2V5
VCCO_B2=VCC_2V5
VCCO_B3=VCC_2V5
VCCO_B4=VCC_2V5
VCCO_B5=VCC_2V5
VCCO_B6=VCC_2V5
VCCO_B7=NC

INPUT-FPGA A
VME-RDO<11..0>
SHEET7
RDO-JET<19..0>
SHEET6
RDO-DAQ<15..0>
SHEET6
L-RDO<11..0>
VME-CLK
SM-CLK
RDO-JET<19..0>

VCC_2V5
GND
47UF
470UF
10UF

TTC-DAT

VCC_3V3
GND
VCC=VCC_3V3
SSOP-DL
I335
IC2.3
2DIR 2E
1DIR 1E
74LVC16245
TTC-DAT0<3..0>
TTC-DAT1<3..0>
TTC-DAT2<3..0>
TTC-DAT3<3..0>
I336
IC3.3
TTC-DAT4<3..0>
TTC-DAT5<3..0>
TTC-DAT6<3..0>
TTC-DAT7<3..0>

CLK-FANOUT

VCC=VCC_3V3
SSOP-DL
I154
IC4.3
R-SIL1.3
VSSR_SSOP16
I340
CDC318A
I174
J4.3
TP
OE
SDATA
SCLOCK
CONTRCLK-H
CONTRCLK-G
CONTRCLK-F
CONTRCLK-D
CONTRCLK-C
CONTRCLK-B
CONTRCLK-A
CONTRCLK-SM
CONTRCLK-VME
FEETB-CLK1
CONTRCLK-DAQ
CONTRCLK-JET
CONTRCLK-I
CONTRCLK-K
CONTRCLK-J
GCK1
R-SIL2.3
I341
IC5.3
IC6.3
IC7.3
IC8.3
IC9.3
IC10.3
IC11.3
IC12.3
IC13.3
IC33.3
IC34.3

UNIVERSITY OF MAINZ	DRAWING TITLE: CONTROL-FPGA		
	SIZE: C	REV.	DRAWING NO.
	SCALE	30.05.2000	SHEET 3 OF 13