REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

CONTRCLK-JET

VCC_2V5 R1.5 GND R9.5 VCC_2V5 R1.6 GND R2.5

FULL I205

D<7..1>

I206 R503 J1.6

Z-PACK_B19_FEMALE JB2 JB3 JB1

VCCINT=VCC_1V8
VCCO_B0=VCC_2V5
VCCO_B1=VCC_2V5
VCCO_B2=VCC_2V5
VCCO_B3=VCC_2V5
VCCO_B4=VCC_2V5
VCCO_B5=VCC_2V5
VCCO_B6=VCC_2V5
VCCO_B7=VCC_2V5

I634 J2.6

JET-CE JET-WR JET-WR-STRB VCC_2V5 GND

RDO-JET<19..0>

VIRTEX
XCV600E_FG680
IC1.6

J-JA1<4..0> J-JA2<4..0> J-JA3<4..0> J-JA4<4..0> J-JB1<4..0> J-JB2<4..0> J-JB3<4..0> J-JB4<4..0> J-JC1<4..0> J-JC2<4..0> J-JC3<4..0> J-JC4<4..0> J-JD1<4..0> J-JD2<4..0> J-JD3<4..0> J-JD4<4..0> J-JE1<4..0> J-JE2<4..0> J-JE3<4..0> J-JE4<4..0> J-JF1<4..0> J-JF2<4..0> J-JF3<4..0> J-JF4<4..0>

J-JL1<4..0> J-JL2<4..0> J-JL3<4..0> J-JL4<4..0> J-JK1<4..0> J-JK2<4..0> J-JK3<4..0> J-JK4<4..0> J-JJ1<4..0> J-JJ2<4..0> J-JJ3<4..0> J-JJ4<4..0> J-JH1<4..0> J-JH2<4..0> J-JH3<4..0> J-JH4<4..0> J-JG1<4..0> J-JG2<4..0> J-JG3<4..0> J-JG4<4..0>

JB4 JB5 JB6 JB7 JB8 JB1 JB6

RDO-JET<19..0>

VCC_1V8 GND 47UF 47UF 470UF 10UF

VCC_2V5 GND 47UF 47UF 470UF 10UF

UNIVERSITY OF MAINZ	DRAWING TITLE: JET FPGA	
	SIZE: C	REV.
	SCALE	SHEET OF