REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

DEL1.8

I299 AMZ-DELAY (IN; OUT20, OUT40, OUT60, OUT80, OUT100; RHOMBUS)

CONTRCLK-B, BUF-M-INPUTB, BUF-CCLK-B, PROGRAM-B, DONE-B, INIT-B, DIN-B, BUF-WRITE-B, CE-B, INPUTB_TIO, INPUTB_TMS, TCK-B

R4. 1k, R3. 1k, R2. 1k, R1. 1k — VCC_2V5, GND

R603 1k — J1.8 I22

J2.8 — SAMTEC-SFMC-107 I264

LED 8XLOCK 3XCONTROL — VCC_2V5, GND

LAENGE_AN JB-BUS ANPASSEN (JET-FPGA)

R5.8 1k

B-WR-STRB, B-CE, B-WR

TTC-DAT0(0) … TTC-DAT7(0)

B-DAQ1, B-DAQ2, B-ROC1, B-ROC2

B-RDO(11..0) — B-RDO(0) … B-RDO(11)

A-RDO(11..0) — A-RDO(0) … A-RDO(11)

R-SIL4.8 4k7, R-SIL3.8 1k, I148 VSSR_SSOP20

80MHZ — I147

JB2: B-JB1(0..4), B-JB2(0..4), B-JB4(0..4)

JB1 E25

B-JB3(4..0) — B-JB3(0) … B-JB3(4)

J-JB1(4..0) — J-JB1(0) … J-JB1(4)

J-JB2(4..0) — J-JB2(0) … J-JB2(4)

J-JB3(4..0) — J-JB3(0) … J-JB3(4)

J-JB4(4..0) — J-JB4(0) … J-JB4(4)

I146 R-SIL6.8 0R, R-SIL5.8 0R

I145

FOR VERTEX-E (VREF)

VIRTEX 2.5V

XCV150-456

INPUT-FPGA

B

I144 IC1.8

VCCINT=VCC_2V5

VCCO_B0=VCC_2V5
VCCO_B1=VCC_2V5
VCCO_B2=VCC_2V5
VCCO_B3=VCC_2V5
VCCO_B4=VCC_2V5
VCCO_B5=VCC_2V5
VCCO_B6=VCC_2V5
VCCO_B7=VCC_2V5

I/O0 … I/O111 — IB4E_RCLK, IB4E_LOCK, IB4E_PWRDN, IB4E_REN, IB3E_RCLK, IB3E_LOCK, IB3E_PWRDN, IB3E_REN, IB2E_RCLK, IB2E_LOCK, IB2E_PWRDN, IB2E_REN, IB1E_RCLK, IB1E_LOCK, IB1E_PWRDN, IB1E_REN, IB4H_RCLK, IB4H_LOCK, IB4H_PWRDN, IB4H_REN, IB3H_RCLK, IB3H_LOCK, IB3H_PWRDN, IB3H_REN, IB2H_RCLK, IB2H_LOCK, IB2H_PWRDN, IB2H_REN, IB1H_RCLK, IB1H_LOCK, IB1H_PWRDN, IB1H_REN

I226 IB4E_PWRDN, I213 IB4E_REN, I214 IB3E_PWRDN, I215 IB3E_REN, I216 IB2E_PWRDN, I217 IB2E_REN, I218 IB1E_PWRDN, IB1E_REN — VCC=VCC_3V3, IC2.8

DS92LV1210 — DIN/DIN*, RCLK, LOCK*, PWRDN*, REN, REFCLK, RCLK_R/F — VCC_3V3

JB2 R6.8 1k, R7.8 1k, R8.8 1k, R9.8 1k, R10.8 1k, R11.8 1k, R12.8 1k, R13.8 1k

IB4H_PWRDN, IB4H_REN, IB3H_PWRDN, IB3H_REN, IB2H_PWRDN, IB2H_REN, IB1H_PWRDN, IB1H_REN — IC12.8

IC3.8, IC4.8, IC5.8, IC6.8, IC7.8, IC8.8, IC9.8, IC10.8 — DVCC=VCC_3V3, AVCC=VCC_ANA, DGND=GND, AGND=GND

IC11.8 SN65LVDS9637A — B-REFCLK, B-REFCLK* — R14.8 100R — PLL-40-B, PLL-40-B*

I212

10UF, 47UF, 470UF — C45.8, C46.8, C48.8, C4.8, C47.8, C5.8, C49.8, C2.8, C50.8, C53.8, C14.8, C62.8, C55.8, C3.8, C22.8, C23.8, C52.8, C6.8, C15.8, C58.8, C64.8, C16.8, C12.8, C51.8, C7.8, C59.8, C60.8, C63.8, C17.8, C70.8, C13.8, C32.8, C31.8, C30.8, C29.8, C28.8, C27.8, C44.8, C26.8, C33.8, C43.8, C41.8, C40.8, C39.8, C38.8, C45.8, C37.8, C36.8, C35.8, C42.8, C34.8, C25.8, C24.8, C1.8, C19.8, C54.8, C18.8, C55.8, C8.8, C56.8, C11.8, C57.8, C10.8, C9.8, C68.8, C69.8, C66.8, C67.8, C20.8, C21.8

VCC_3V3, VCC_2V5, VCC_ANA, GND

RESISTORARRAY 100 VISHAY VSSR 1603

RESISTORARRAY 1K VISHAY VSSR 1603 FARNELL 309-9003

PINS U11 AND E11 FOR VIRTEX NOT USE !!!

PINS U11 AND E11 FOR VIRTEX-E NOT CONNECT

ALL CERAMIC CAPACITORS AVX X7R C0403

TANTALUM CAPACITORS AVX TPS 3528
TANTALUM CAPACITORS AVX TPS 6032

UNIVERSITY OF MAINZ

DRAWING TITLE: LVDS9-16_INPUT

SIZE	REV.	DRAWING NO.
C		
SCALE	30.05.2000	SHEET 8 OF 13