REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

I298 AMZ-DELAY (IN; OUT20, OUT40, OUT60, OUT80, OUT100; RHOMBUS)

SAMTEC-SFMC-107 I265 — LED 8XLOCK 3XCONTROL — VCC_2V5 — GND

LAENGE AN JC-BUS ANPASSEN (JET-FPGA)

R5.9

C-WR-STRB, C-CE, C-WR

TTC-DAT0(0), TTC-DAT1(0), TTC-DAT2(0), TTC-DAT3(0), TTC-DAT4(0), TTC-DAT5(0), TTC-DAT6(0), TTC-DAT7(0)

C-DAQ1, C-DAQ2, C-ROC1, C-ROC2

C-RDO(0) … C-RDO(11) — C-RDO(11..0)

B-RDO(0) … B-RDO(11) — B-RDO(11..0)

R-SIL4.9 I148 VSSR_SSOP20

80MHZ

JB3: B-JC1(0..4), B-JC2(0..4), B-JC3(0..4), B-JC4(0..4)

JB2: J-JC1(0..4), J-JC2(0..4), J-JC3(0..4), J-JC4(0..4)

R-SIL5.9 I145

CONTRCLK-C, BUF-M-INPUTC, BUF-CCLK-C, PROGRAM-C, DONE-C, INIT-C, DIN-C, BUF-WRITE-C, CE-C, INPUTE-TIQ, INPUTE-TMS, TCK-E

R603 J1.9 I22

FOR VERTEX-E (VREF)

VIRTEX 2.5V

XCV150-456

INPUT-FPGA

C

I144

IC1.9

VCCINT=VCC_2V5

VCCO_B0=VCC_2V5
VCCO_B1=VCC_2V5
VCCO_B2=VCC_2V5
VCCO_B3=VCC_2V5
VCCO_B4=VCC_2V5
VCCO_B5=VCC_2V5
VCCO_B6=VCC_2V5
VCCO_B7=VCC_2V5

I/O00 … I/O111 — IC4E(0..7), IC4E_RCLK, IC4E_LOCK, IC4E_PWRDN, IC4E_REN; IC3E(0..7), IC3E_RCLK, IC3E_LOCK, IC3E_PWRDN, IC3E_REN; IC2E(0..7), IC2E_RCLK, IC2E_LOCK, IC2E_PWRDN, IC2E_REN; IC1E(0..7), IC1E_RCLK, IC1E_LOCK, IC1E_PWRDN, IC1E_REN; IC4H(0..7), IC4H_RCLK, IC4H_LOCK, IC4H_PWRDN, IC4H_REN; IC3H(0..7), IC3H_RCLK, IC3H_LOCK, IC3H_PWRDN, IC3H_REN; IC2H(0..7), IC2H_RCLK, IC2H_LOCK, IC2H_PWRDN, IC2H_REN; IC1H(0..7), IC1H_RCLK, IC1H_LOCK, IC1H_PWRDN, IC1H_REN

IC2.9 — IC4E_PWRDN, IC4E_REN; IC3E_PWRDN, IC3E_REN; IC2E_PWRDN, IC2E_REN; IC1E_PWRDN, IC1E_REN

IC4E(0..7) — IC4E_RCLK — IC4E_LOCK — IC4E_PWRDN — IC4E_REN — VCC_3V3 — JB3 — IC3.9

IC3E(0..7) — IC3E_RCLK — IC3E_LOCK — IC3E_PWRDN — IC3E_REN — VCC_3V3 — IC4.9

IC2E(0..7) — IC2E_RCLK — IC2E_LOCK — IC2E_PWRDN — IC2E_REN — VCC_3V3 — IC5.9

IC1E(0..7) — IC1E_RCLK — IC1E_LOCK — IC1E_PWRDN — IC1E_REN — VCC_3V3 — IC6.9

IC4H(0..7) — IC4H_RCLK — IC4H_LOCK — IC4H_PWRDN — IC4H_REN — VCC_3V3 — IC7.9

IC3H(0..7) — IC3H_RCLK — IC3H_LOCK — IC3H_PWRDN — IC3H_REN — VCC_3V3 — IC8.9

IC2H(0..7) — IC2H_RCLK — IC2H_LOCK — IC2H_PWRDN — IC2H_REN — VCC_3V3 — IC9.9

IC1H(0..7) — IC1H_RCLK — IC1H_LOCK — IC1H_PWRDN — IC1H_REN — VCC_3V3 — IC10.9

DS92LV1210 — DIN/DIN* — RCLK, LOCK*, PWRDN*, REN, REFCLK, RCLK_R/F

R6.9 … R14.9 — 100

IC11.9 SN65LVDS9637A — C-REFCLK, C-REFCLK* — PLL-40-C, PLL-40-C*

10UF

47UF

VCC_3V3 — IC46.9, IC48.9, IC4.9, IC47.9, IC5.9, IC49.9, IC2.9, IC50.9, IC53.9, IC14.9, IC61.9, IC62.9, IC65.9, IC3.9, IC23.9, IC22.9; IC52.9, IC6.9, IC15.9, IC58.9, IC64.9, IC16.9, IC12.9, IC51.9, IC7.9, IC59.9, IC60.9, IC63.9, IC17.9, IC70.9, IC13.9

VCC_ANA — IC9.9, IC54.9, IC8.9, IC55.9, IC18.9, IC56.9, IC11.9, IC57.9, IC10.9, IC19.9, IC68.9, IC69.9, IC66.9, IC67.9, IC20.9, IC21.9

VCC_2V5 — IC26.9, IC27.9, IC28.9, IC29.9, IC30.9, IC31.9, IC44.9, IC32.9, IC43.9, IC33.9, IC35.9, IC36.9, IC37.9, IC38.9, IC45.9; IC39.9, IC40.9, IC41.9, IC34.9, IC42.9, IC25.9, IC24.9, IC1.9

47UF 47UF 470UF — 10UF

RESISTORARRAY 100 VISHAY VSSR 1603

RESISTORARRAY 1K VISHAY VSSR 1603 FARNELL 309-9003

PINS U11 AND E11 FOR VIRTEX NOT USE !!!

PINS U11 AND E11 FOR VIRTEX-E NOT CONNECT

ALL CERAMIC CAPACITORS AVX X7R C0403

TANTALUM CAPACITORS AVX TPS 3528
TANTALUM CAPACITORS AVX TPS 6032

UNIVERSITY OF MAINZ

DRAWING TITLE: LVDS1-8_INPUT

SIZE	REV.	DRAWING NO.
C		
SCALE	30.05.2000	SHEET 7 OF 13