Phase-1 with new JEP

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New Jet/Energy processor

- JEP will be of very old age by time of phase-1 upgrade
  - Limited availability of spares
  - FPGAs not supported by current design tools
- LArg signals will be available optically at high granularity
- Tile optical signals might become available at a later stage

JEP comprised of JEMs, based on modular concept (mezzanines)

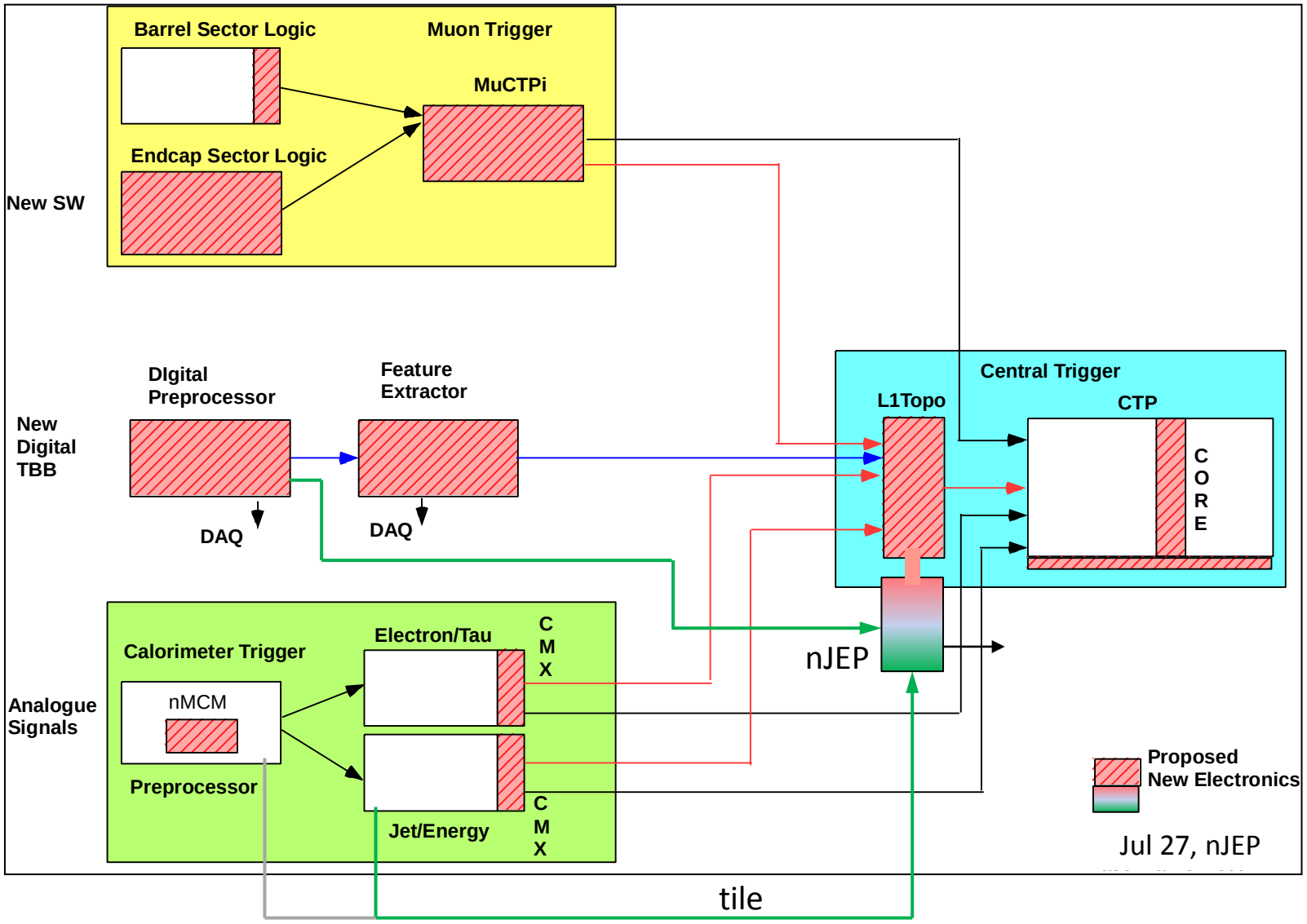
→ Staged upgrade of JEP, so as to guarantee smooth changeover.

Minimal approach:

- Increase JEM optical output capacity by renewal of low-cost daughter modules
- Add nJEP processor next to topology processor module, to receive JEM optical real-time output
- Keep JEP running while nJEP being commissioned…
- Eventually switch to nJEP based jet trigger
- Use old JEMs for e/o conversion of tile signals only
- Replace JEMs with simple and cheap converter modules as further spares are required
- Consider feeding nJEP directly from PPr

→ In case we were running into a latency crisis, full JEP replacement by o/e converter modules would be effective and affordable

Phase-1 upgrade with new JEP



nJEP : towards phase-2

- Minimal approach is basically replacement of aging JEP
- nJEP to provide plenty of spare input capacity
  - Improve algorithms by making use of granularity available from DPP
  - Add higher granularity optical input from tile as it becomes available
- nJEP to be built phase-2 compliant