Software

- Upgrade of JetProcessor pre-phase 1
  - Using updated firmware register map from Stockholm

	Reserved			
Offset + 200	RoI0:Threshold1/Size1	12	0x3FF	Read/Write
Offset + 202	RoI0:Threshold2/Size2	12	0x3FF	Read/Write
		12	0x3FF	Read/Write
Offset + 214	RoI3:Threshold1/Size1	12	0x3FF	Read/Write
Offset + 216	RoI3:Threshold2/Size2	12	0x3FF	Read/Write
		12	0x3FF	Read/Write
Offset + 228	RoI7:Threshold1/Size1	12	0x3FF	Read/Write
Offset + 230	RoI7:Threshold2/Size2	12	0x3FF	Read/Write

Bits	Function
Threshold / FCAL Threshold	
0 – 9	Threshold value
10 – 11	Cluster size

- Upgrade of EnergySumProcessor pre-phase 1
  - Firmware not yet updated
  - Updates to playback and spy memory
  - changes in the module services, simulation and test vectors
- Integration of IPBus sw/register model for L1Topo
  - In cooperation with Christian and Murrough
  - Trying to setup IPBus software and L1Calo package on a virtual machine in Mainz
  - Write module services from scratch
  - Register model, test vectors, … to be done