

Software/firmware activities @ MZ

- Working concurrently on support of existent hardware and pre-phase1 circuitry
 - Software (Jan) and firmware (Uli) for JEM processors
 - Software (Jan, Christian) and firmware (Christian, Patric, Stephan) for L1Topo
- Software for current systems is VME/HDMC based
 - Mainz in charge of JEM module services / register maps ...
 - Some contribution made to test vector generation in the not so recent past
 - Probably just minor modifications required during LS1
- Software for L1Topo makes use of IPBus suite
 - Need to write module services from scratch
 - Register model, test vectors, GUIs, ..., ...
- L1Topo firmware under way
 - Regina: physics / algo firmware coordination
 - Stephan: algorithms
 - Patric: infrastructure – MGTs, real-time path before algos, including masking, error checking, play/spy, firmware based test vectors
 - Christian: IPBus
 - (Marek: readout)