

Wing LDA/CCC Status

- AHCAL wing data aggregator has been built
- Initial power-up tests successful
- System integration started (André, Rouven,...), but further effort required
 - Firmware for Kintex FPGAs
 - Firmware / Software for Zynq processor ("MARS ZX3")
- VME-sized clock/control unit acting as MARS test bench
 - Issues in firmware load stage observed on engineering sample
 - Further effort required to debug on production silicon available now
- For the time being cover all immediate needs (AHCAL LDA/CCC) with adapter modules on "Zedboard"
 - Clock/control adapters available
 - Data aggregator adapter ready for production
 - New batch of clock fanout units in production
 - Julien working on software integration